

Kirigami, the Verifiable Art of Network Cutting

General Exam

Advised by Aarti Gupta and Dave Walker

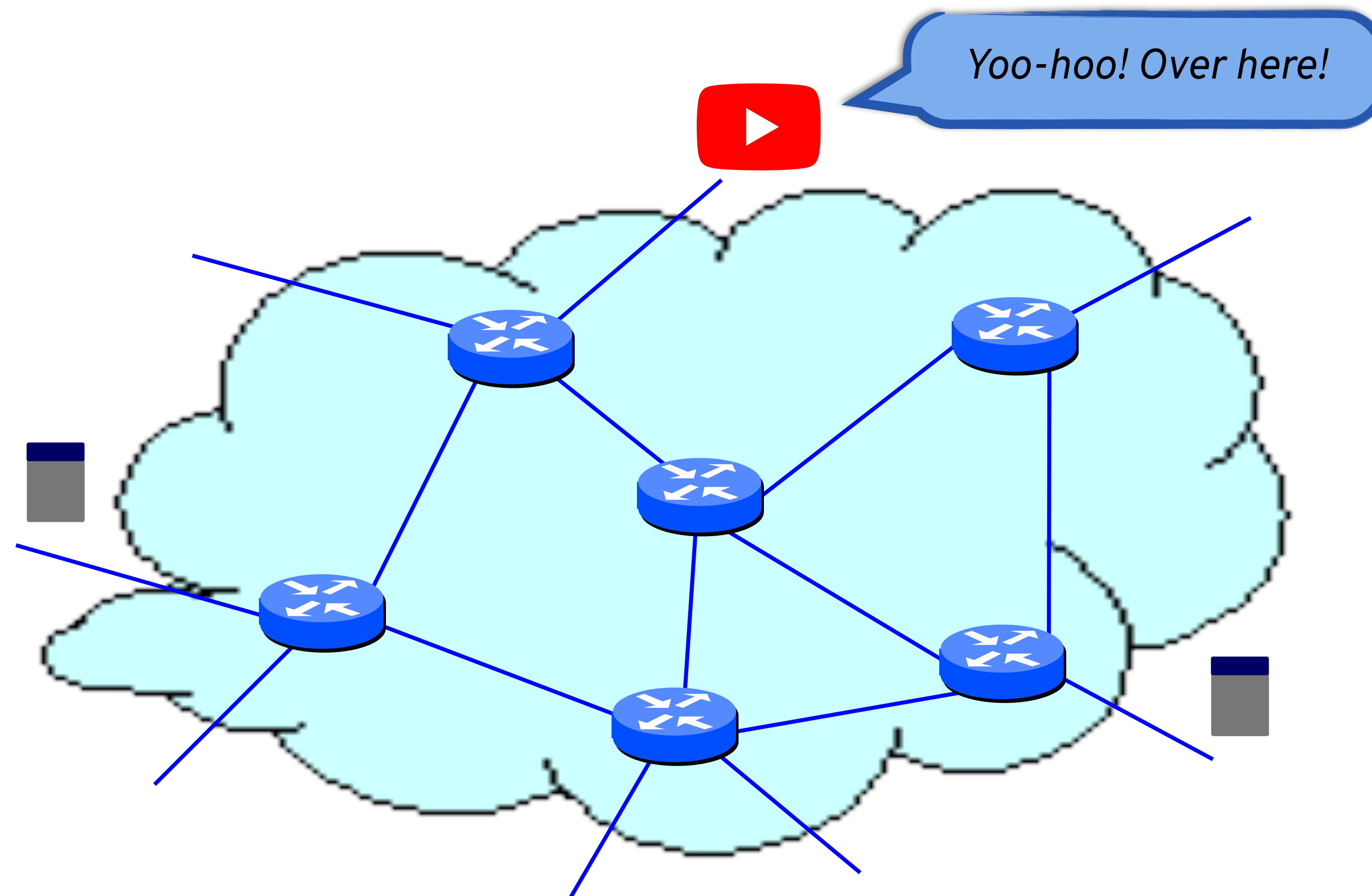
Tim Alberdingk Thijm, 2020-05-18

Network Routing

A (Simplified) Control Plane Protocol

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A (Simplified) Control Plane Protocol



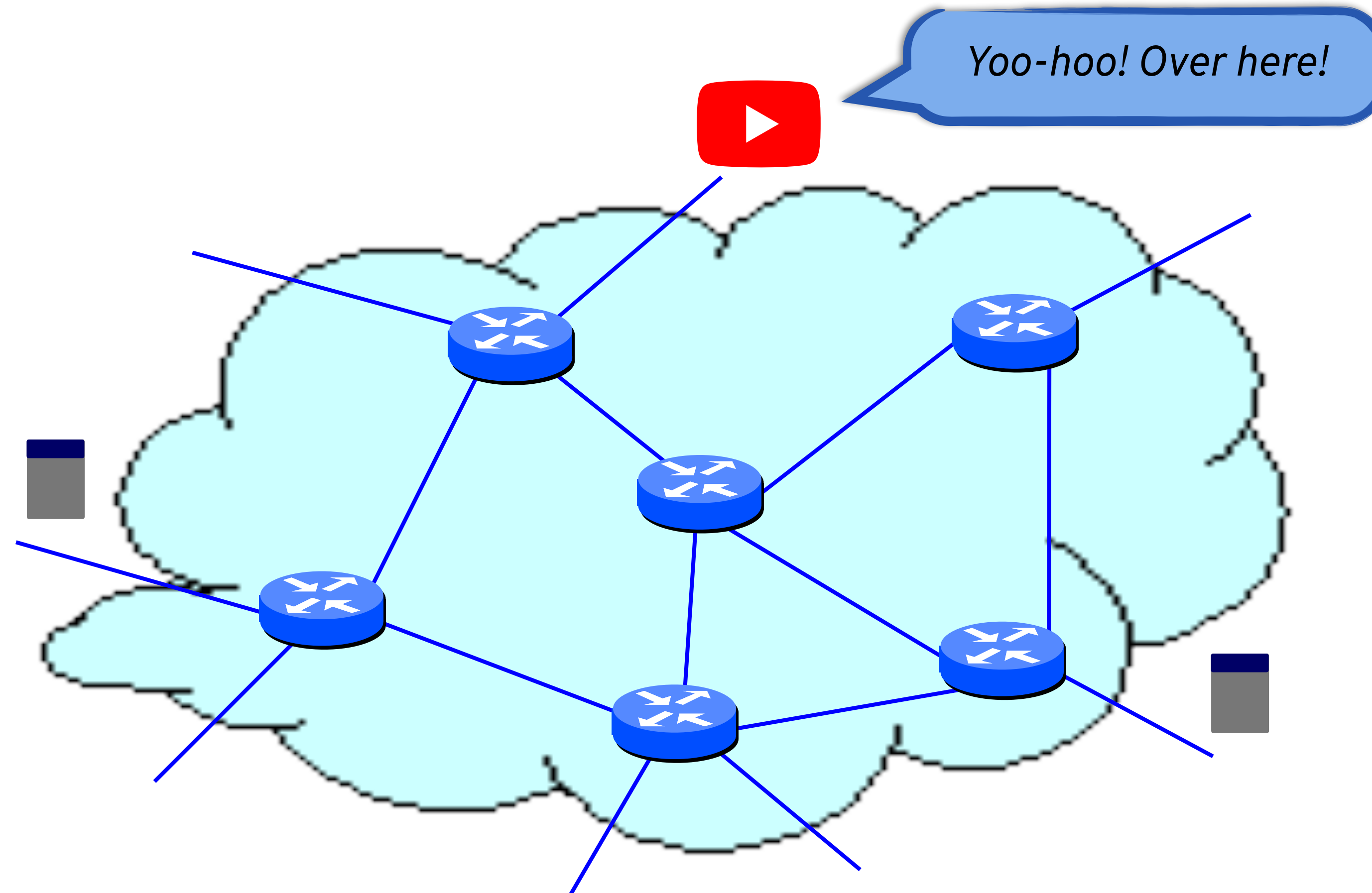
Some images from Jen Rexford's COS561 slides

Network Routing

A (Simplified) Control Plane Protocol

Routers can:

- *Send messages*
- *Receive messages*
- *Compare routes by incentives*



Network Routing

A (Simplified) Control Plane Protocol

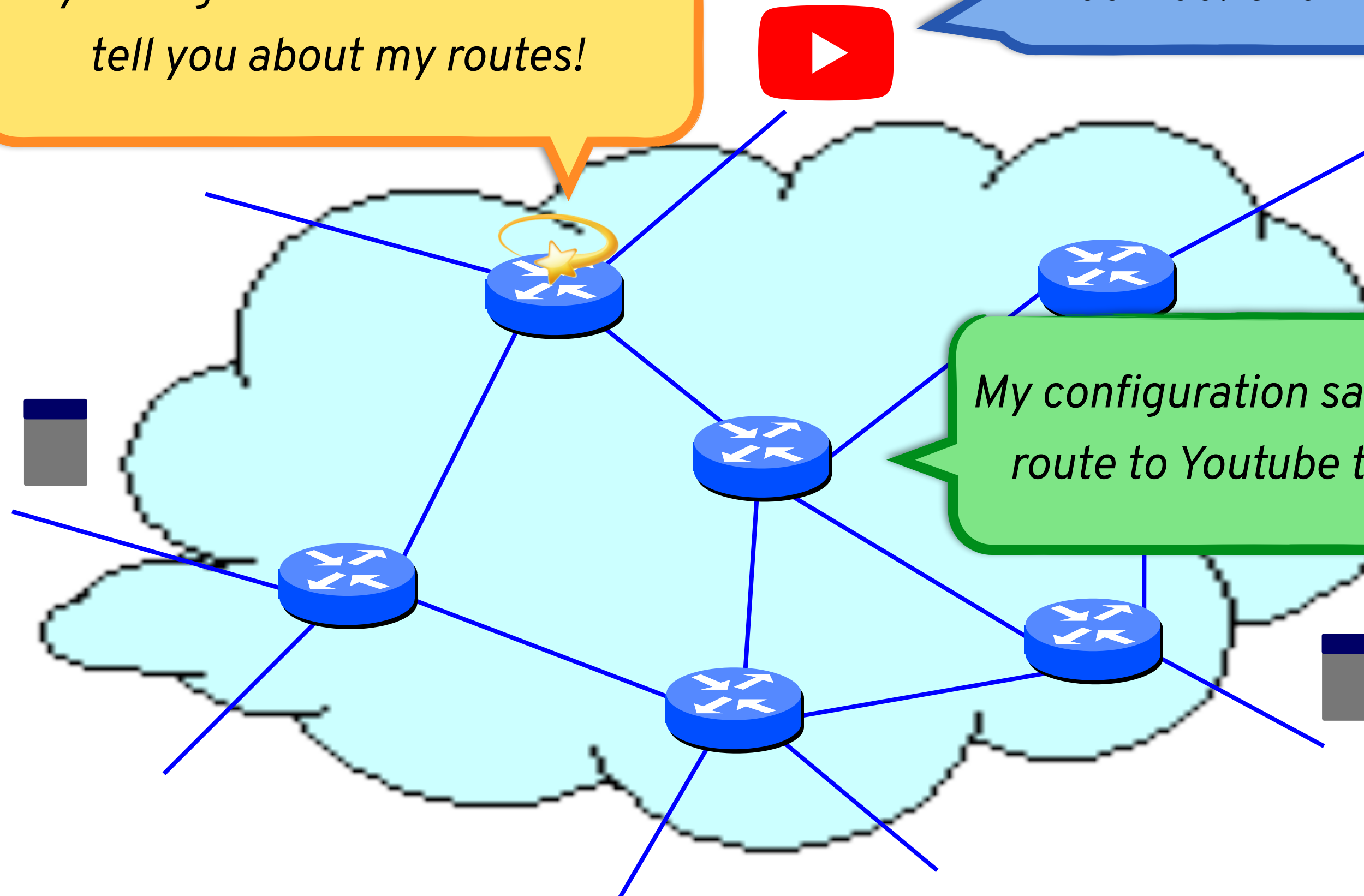
Routers can:

- Send messages
- Receive messages
- Compare routes by incentives

My configuration tells me not to tell you about my routes!

Yoo-hoo! Over here!

My configuration says to prefer to route to Youtube through you!

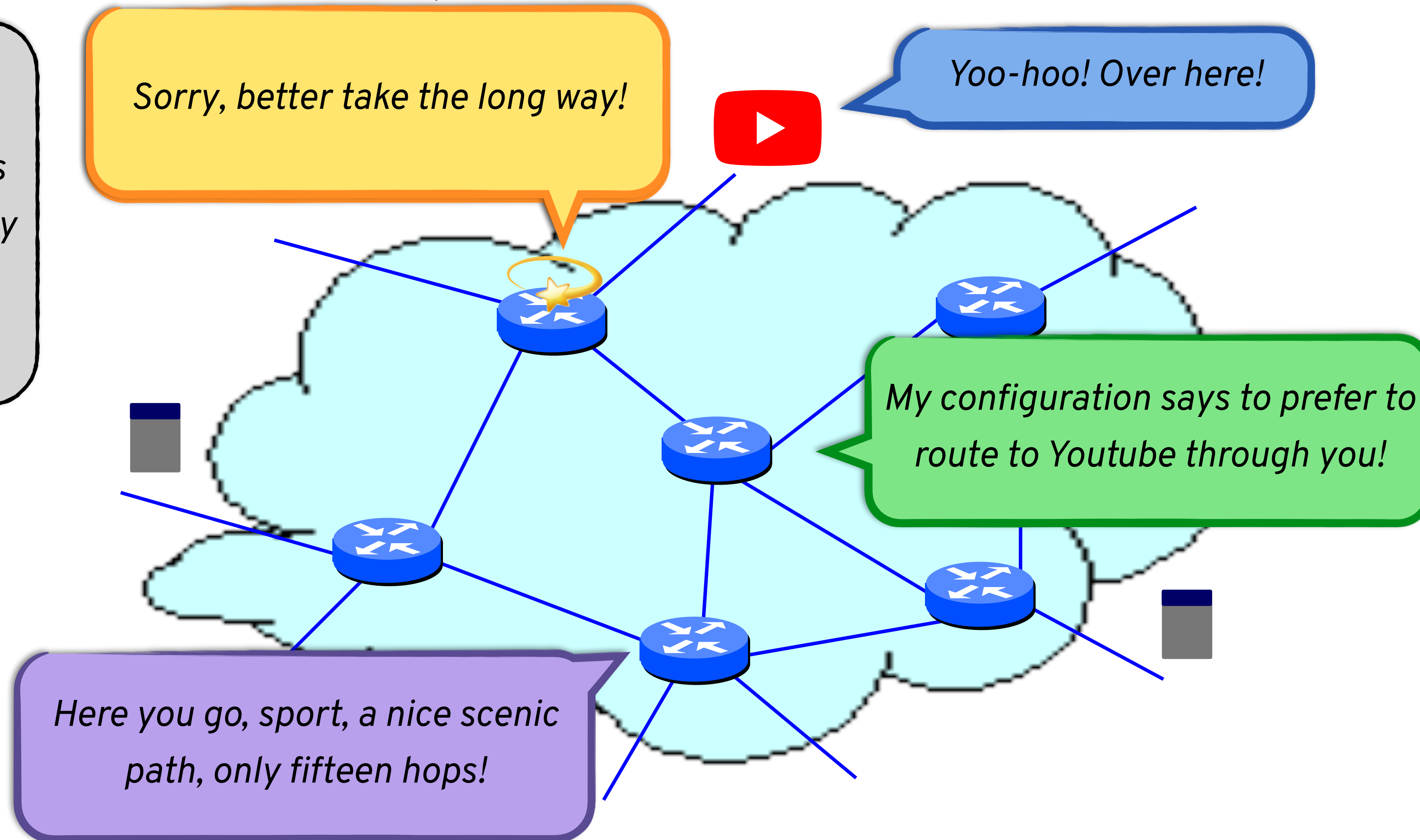


Network Routing

A (Simplified) Control Plane Protocol

Routers can:

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Network Routing

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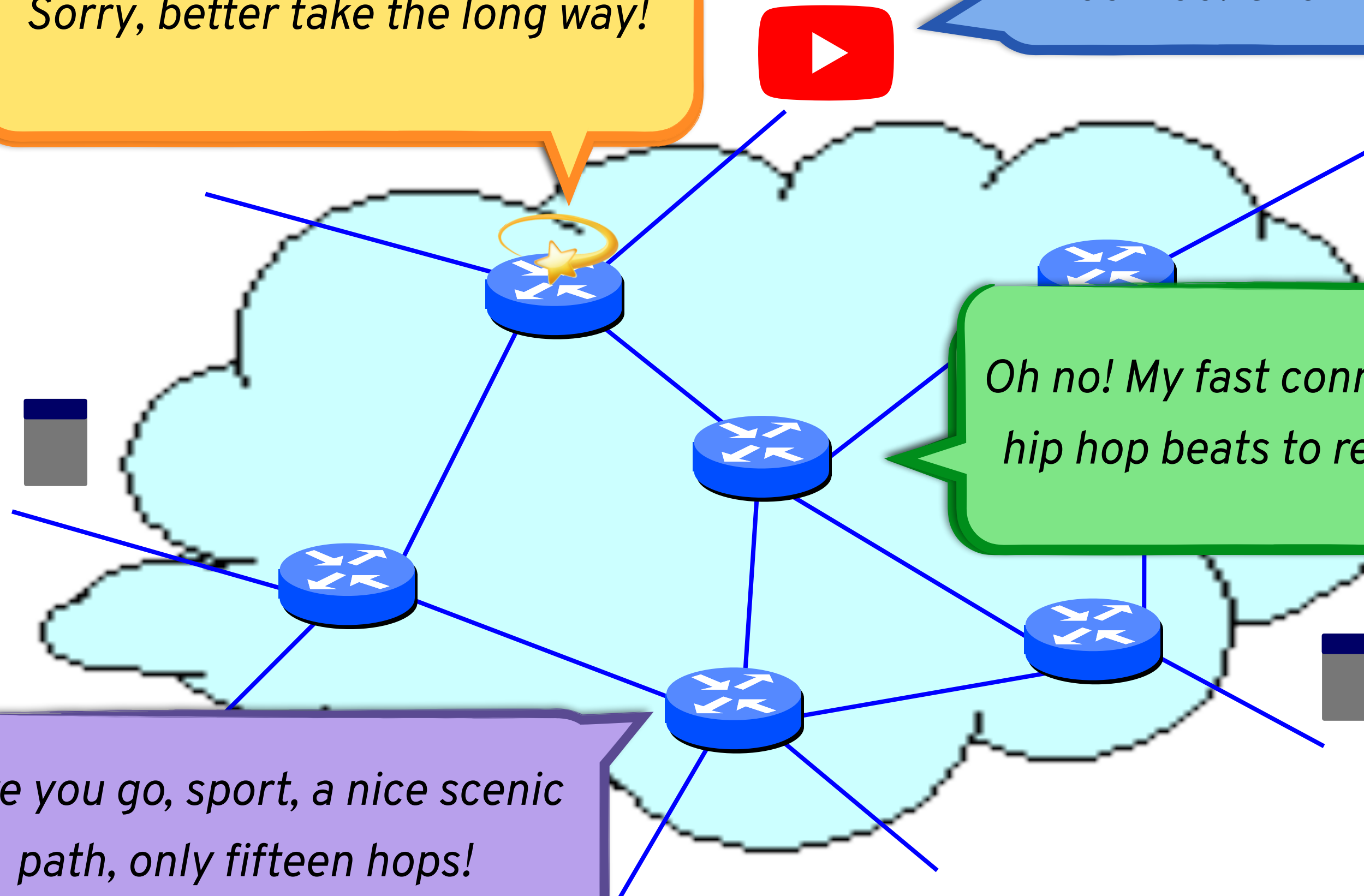
- Send messages
- Receive messages
- Compare routes by incentives
- Have bugs

Sorry, better take the long way!

Yoo-hoo! Over here!

Oh no! My fast connection to lo-fi hip hop beats to relax/study to!

Here you go, sport, a nice scenic path, only fifteen hops!



Network Routing

A (Simplified) Control Plane Protocol

Routers can:

- Send messages
- Receive messages
- Compare routes by incentives
- Have bugs

Sorry, better take the long way!

Yoo-hoo! Over here!

Can anything be done?

Here you go, sport, a nice scenic path, only fifteen hops!

*...on to lo-fi
...seats to relax/study to!*

Solver-Aided Network Verification

In A Nutshell

[Beckett et al., SIGCOMM 2017]

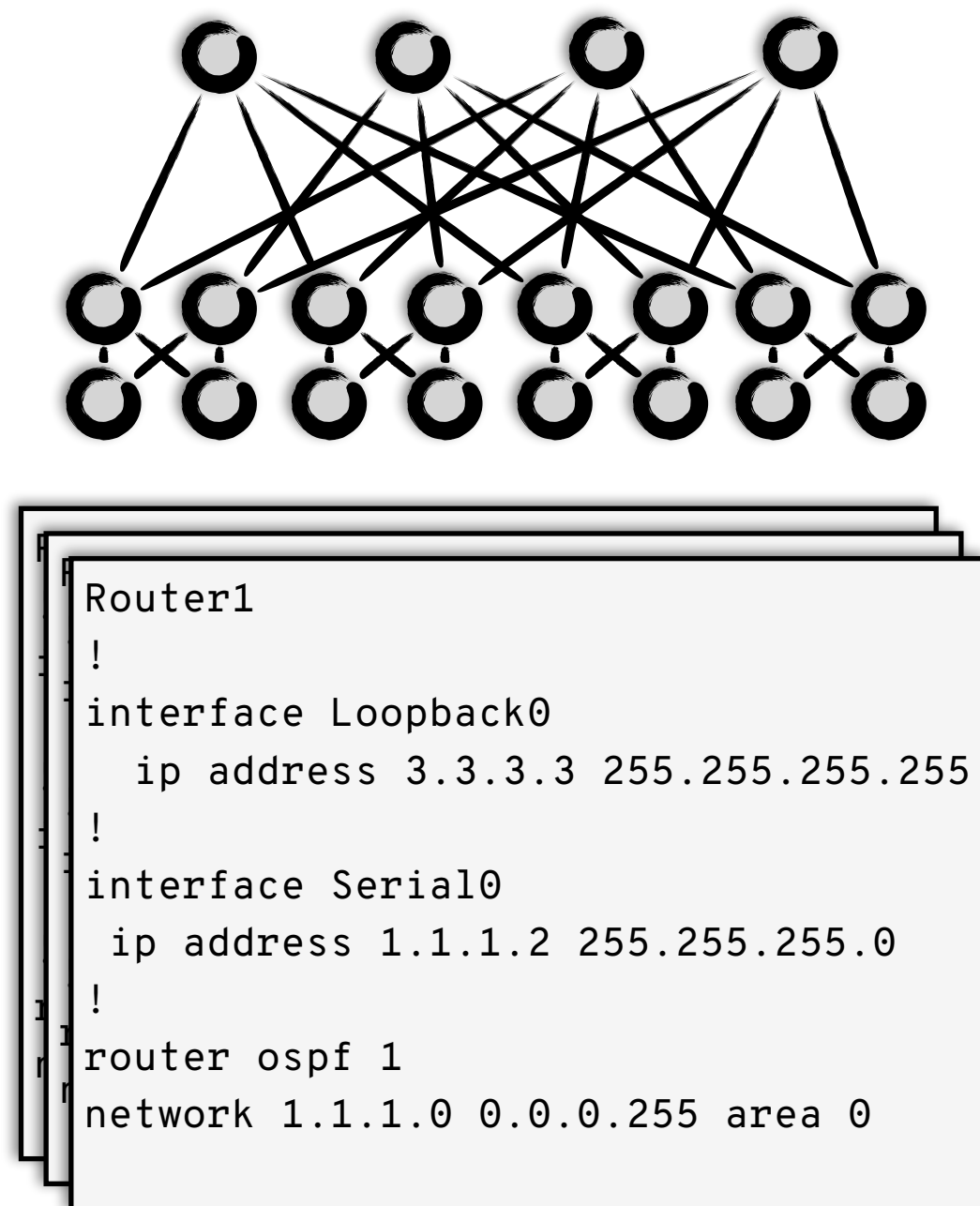
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Solver-Aided Network Verification

In A Nutshell



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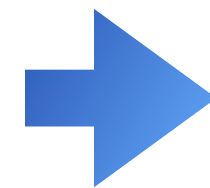
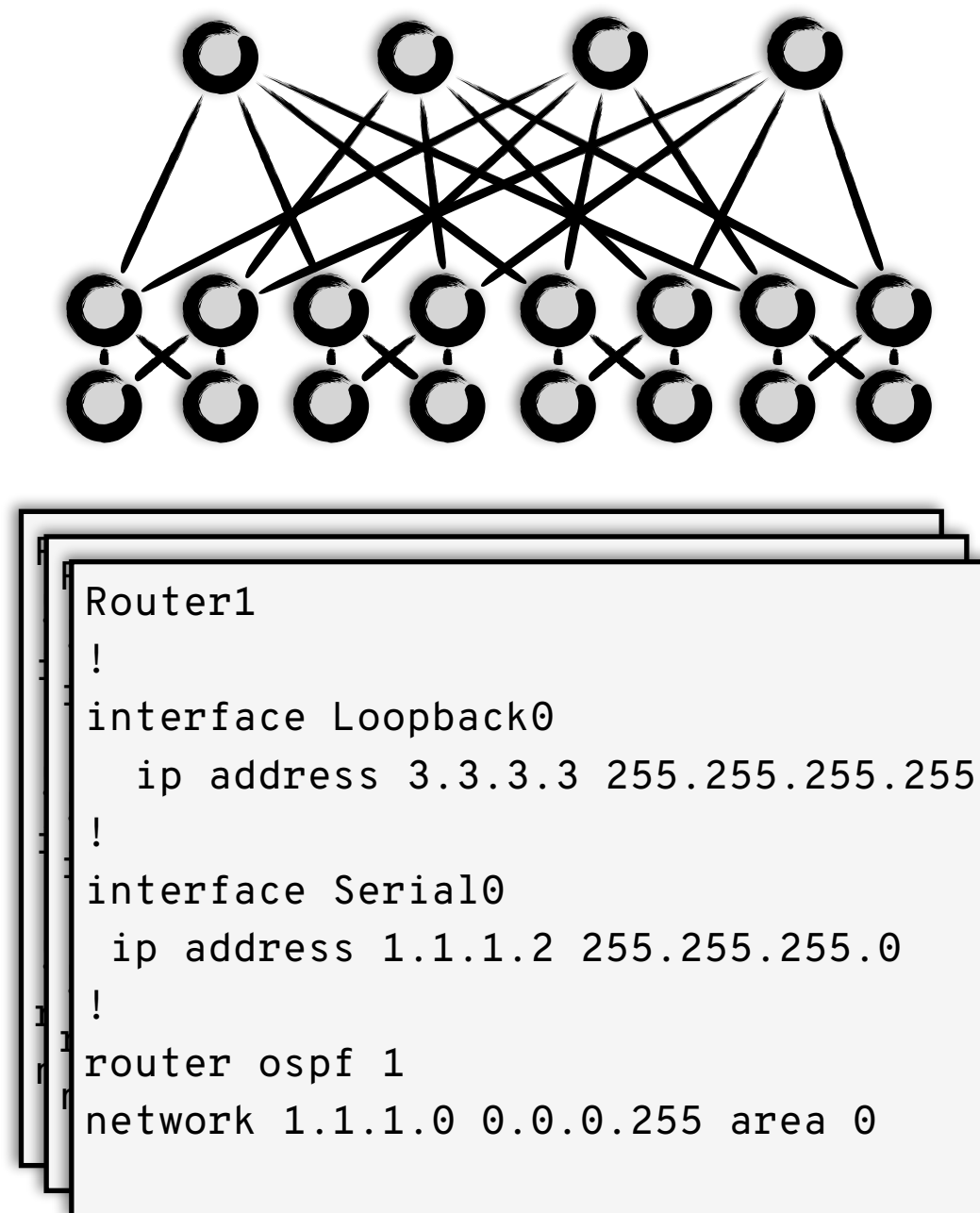
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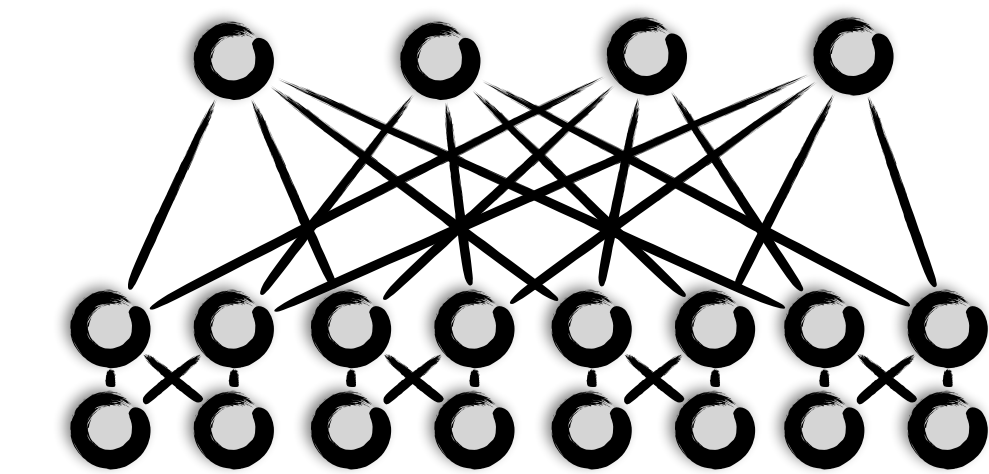


$$\varphi_1 \wedge \varphi_2 \wedge \dots \wedge \varphi_n$$

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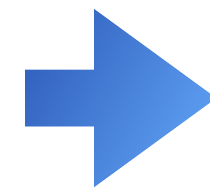
Solver-Aided Network Verification

In A Nutshell



P

```
Router1
!  
interface Loopback0  
  ip address 3.3.3.3 255.255.255.255  
!  
interface Serial0  
  ip address 1.1.1.2 255.255.255.0  
!  
router ospf 1  
network 1.1.1.0 0.0.0.255 area 0
```

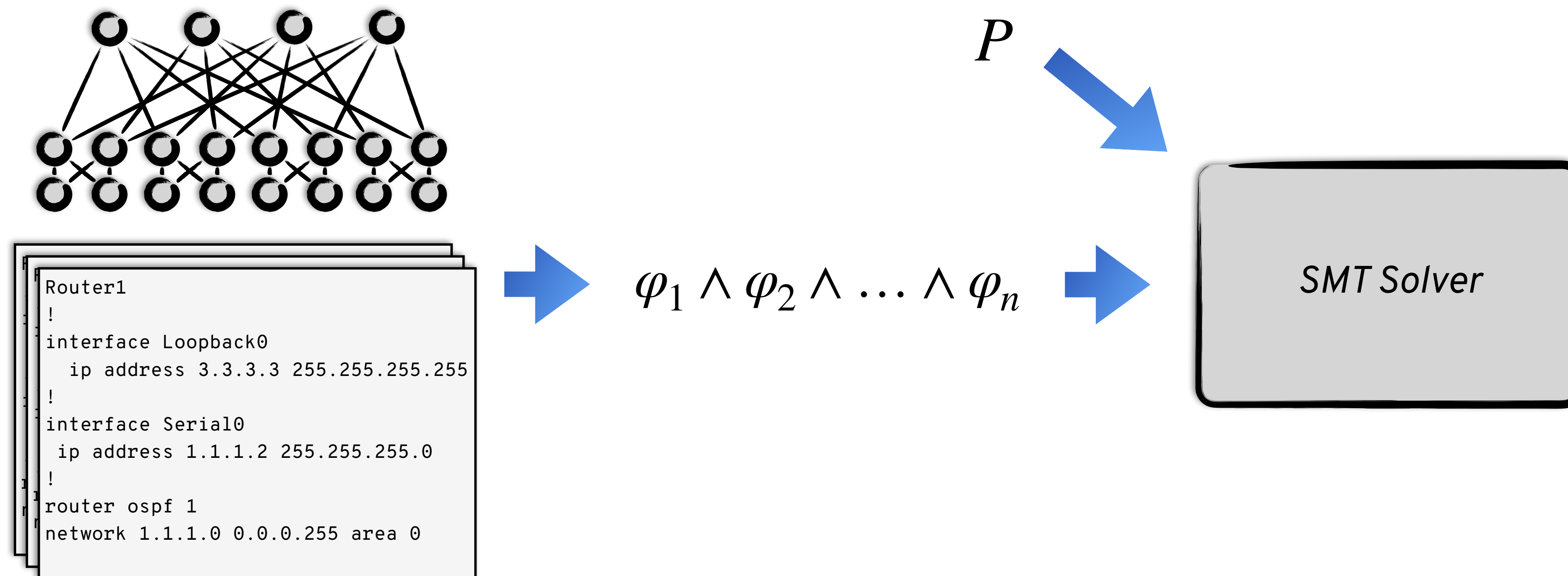


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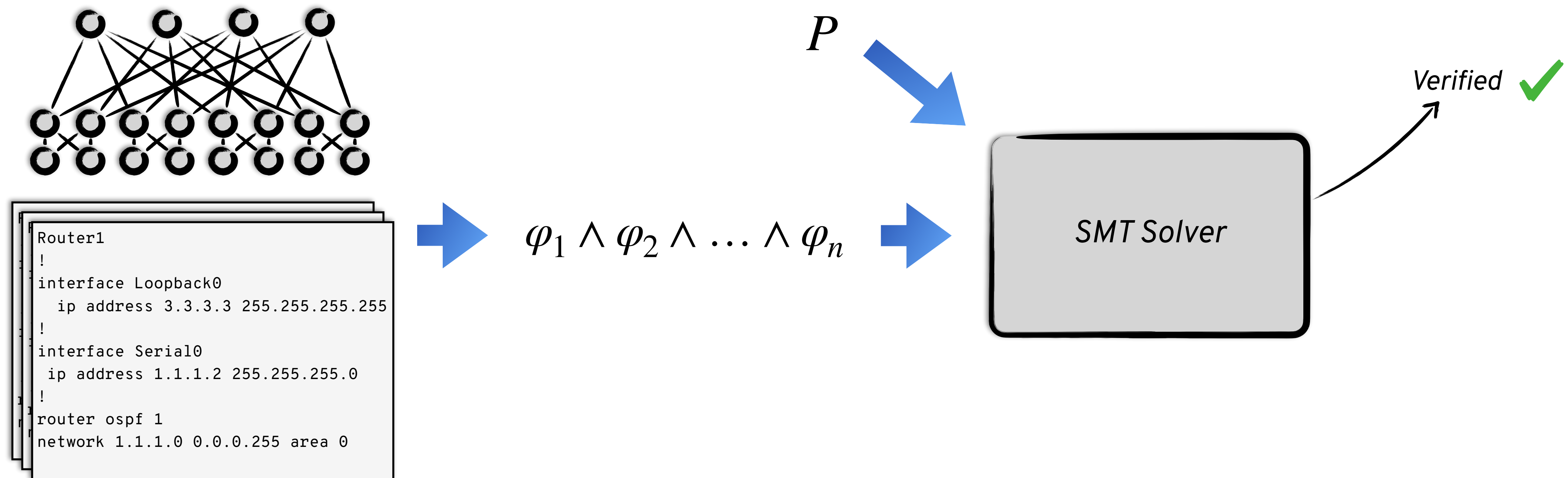
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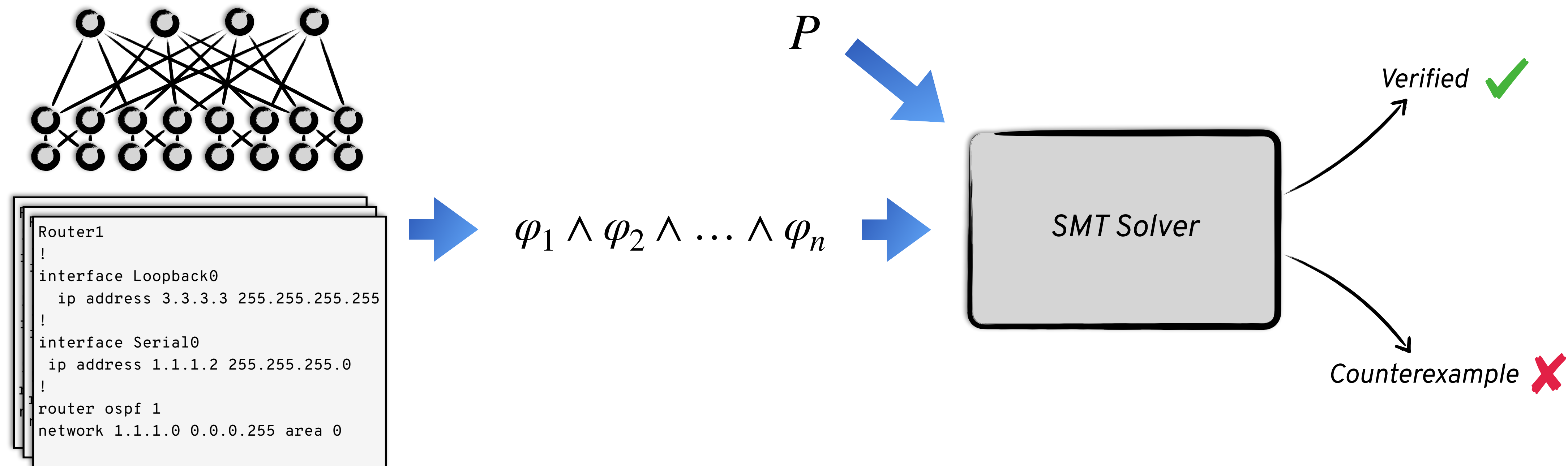
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SMT Verification Scalability

[Beckett et al., SIGCOMM 2017]

SMT Verification Scalability

- # SMT Variables \propto Network Size

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- Time to check certain properties can be **exponential** in the number of variables!

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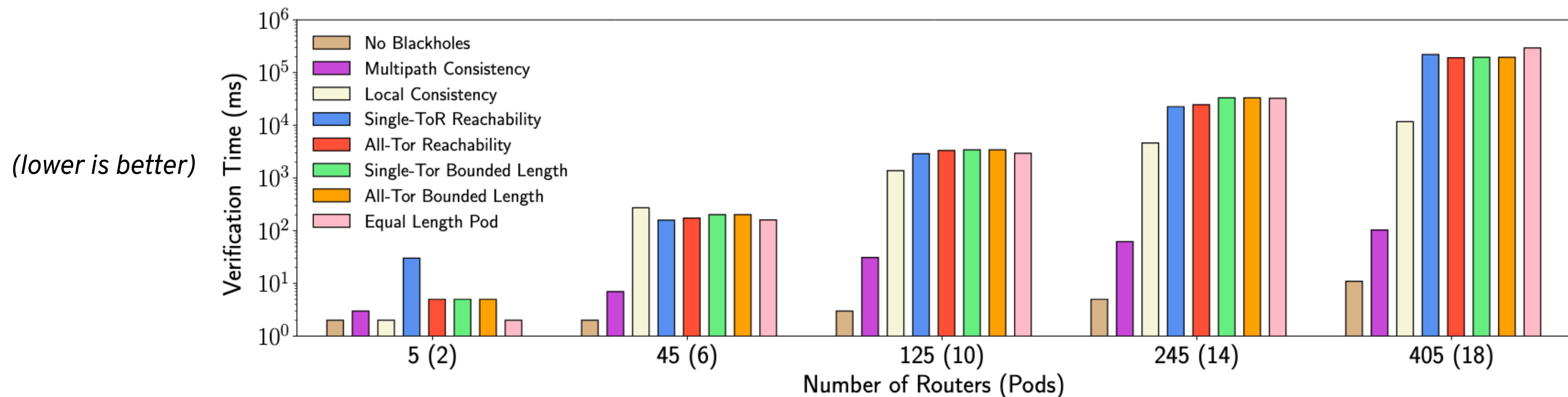


Figure 8: Verification time for synthetic configurations for different properties and network sizes.

[Beckett et al., SIGCOMM 2017]

SMT Verification Scalability

- # SMT Variables \propto Network Size
- Time to check certain properties can be **exponential** in the number of variables!

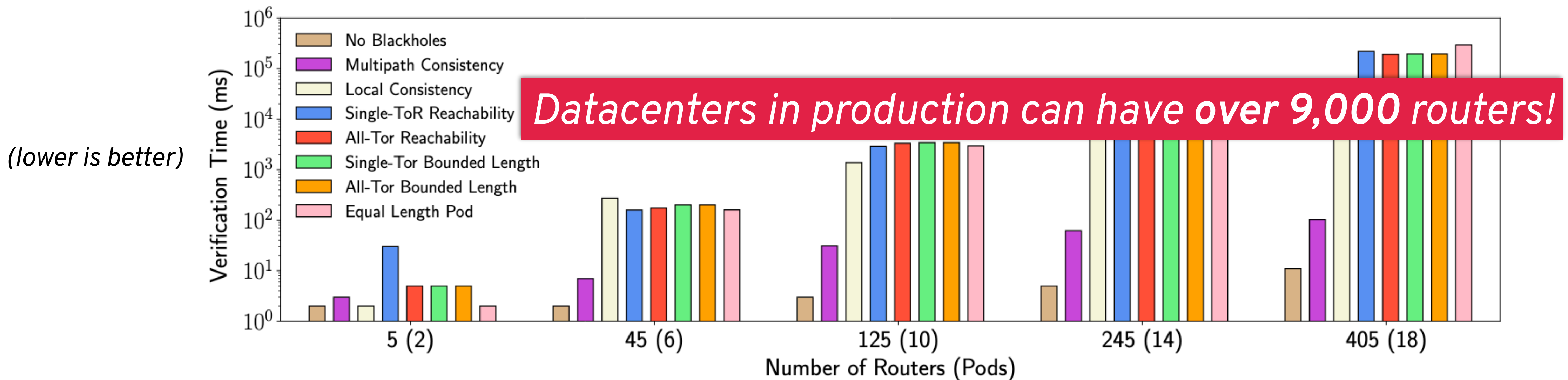
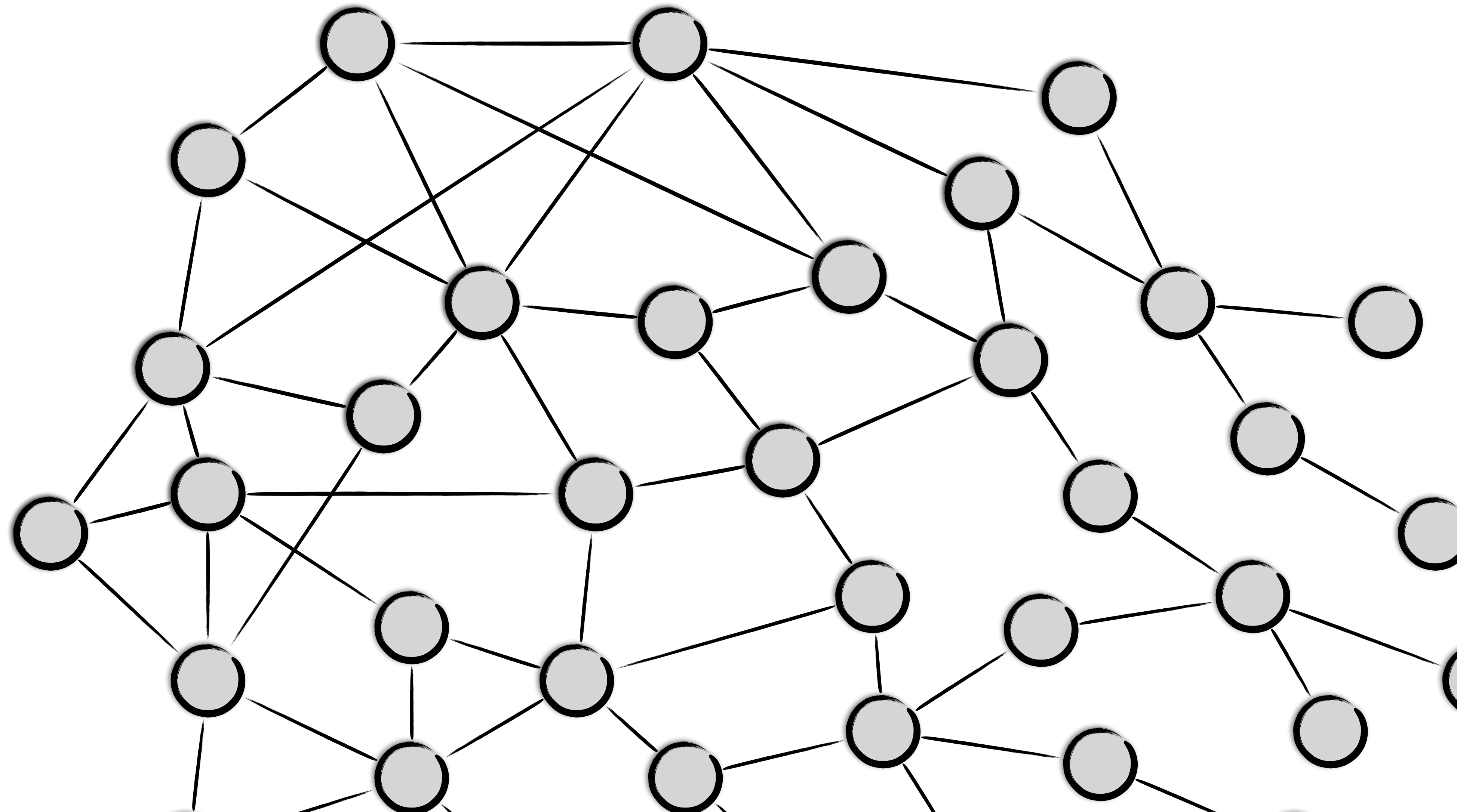


Figure 8: Verification time for synthetic configurations for different properties and network sizes.

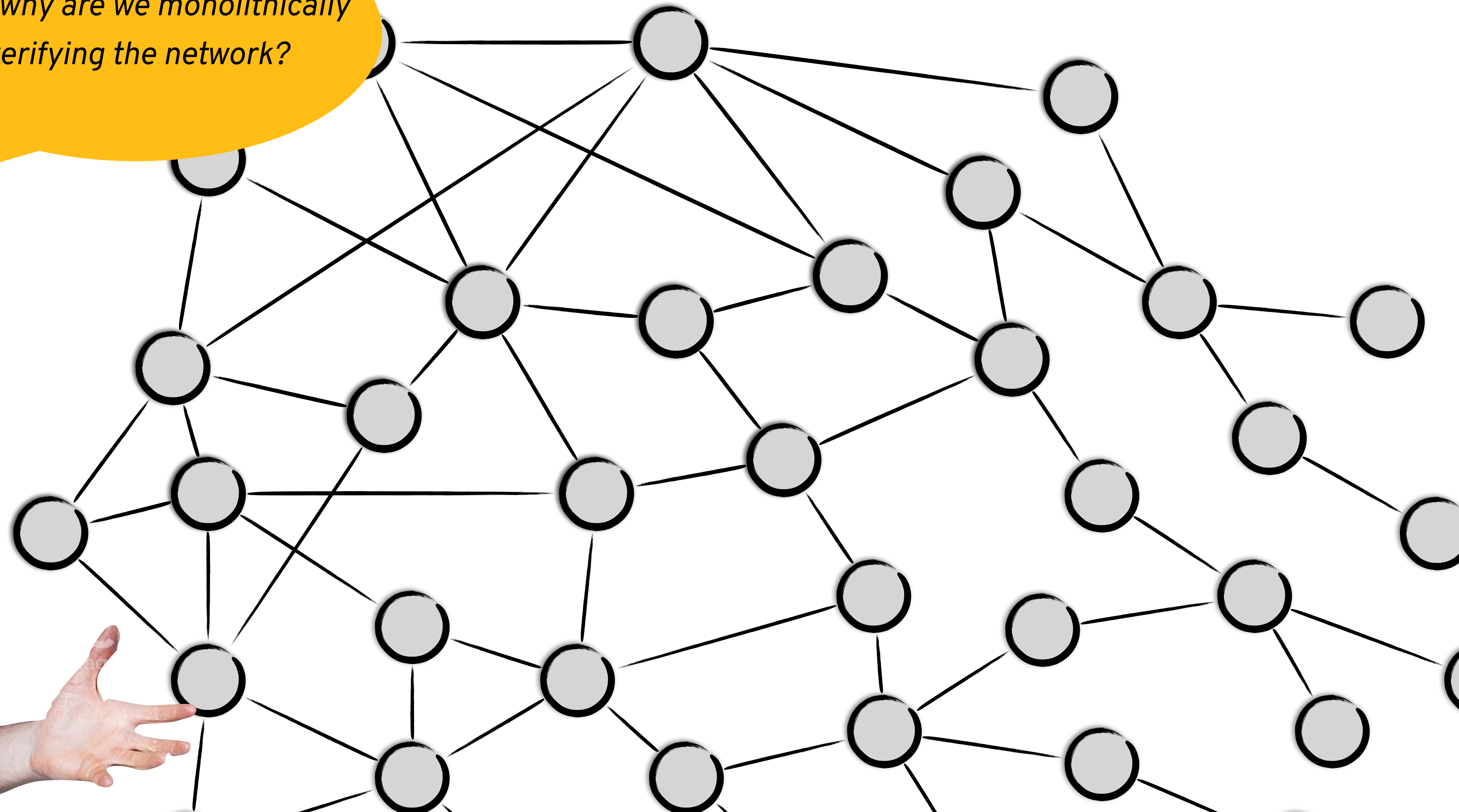
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Is All Hope Lost?



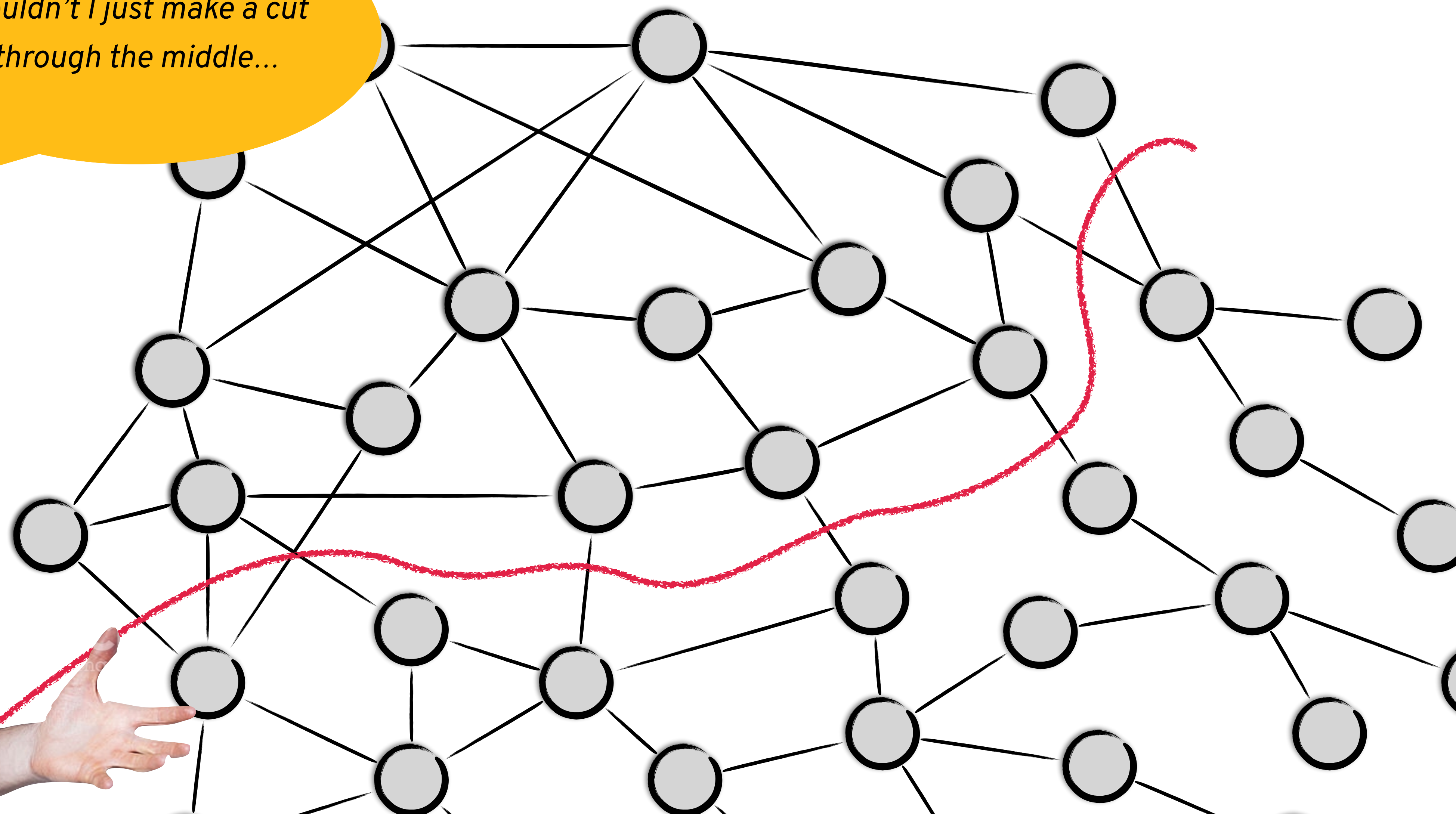
Is All Hope Lost?

Wait, why are we monolithically verifying the network?



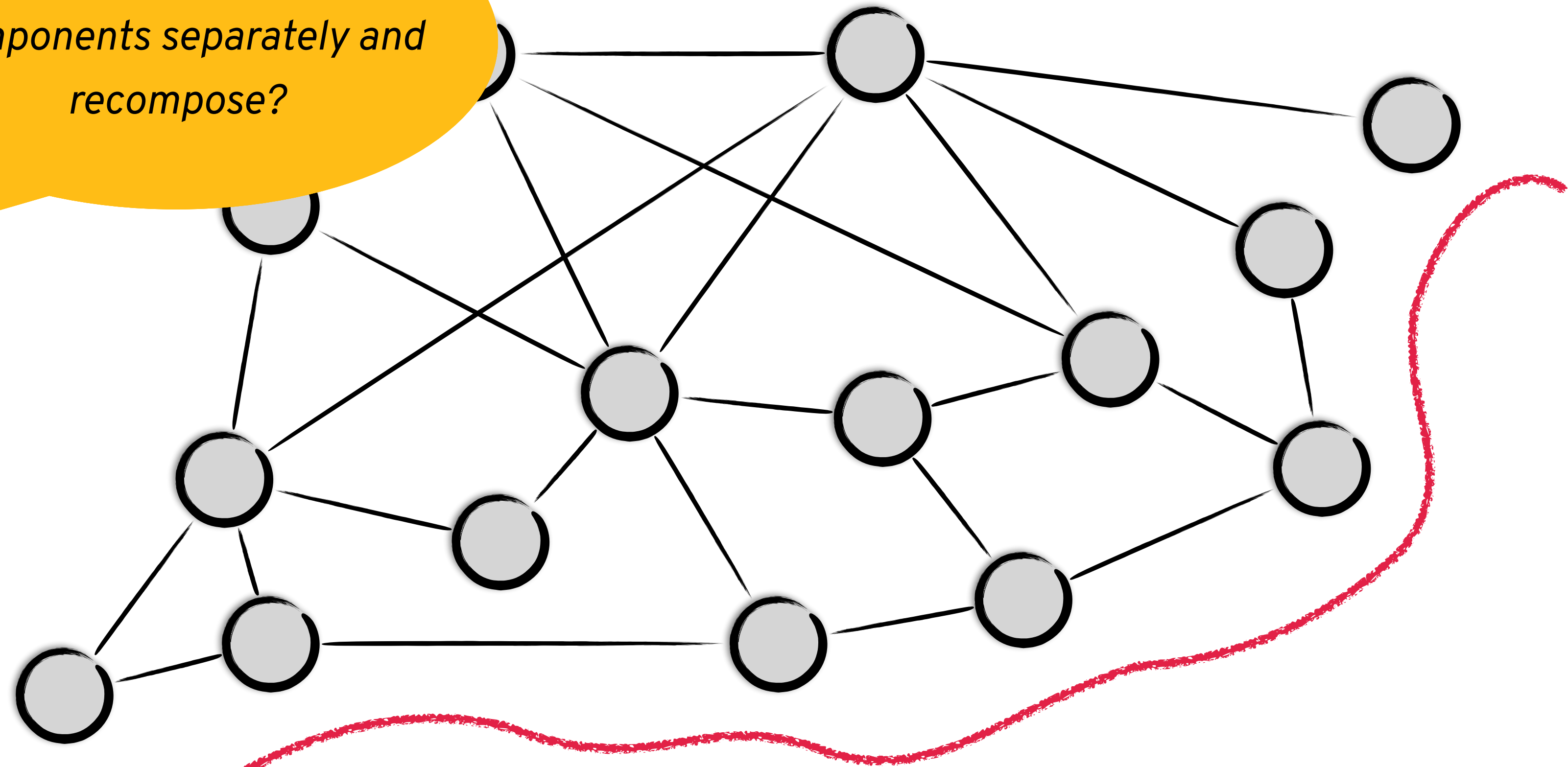
Is All Hope Lost?

*Couldn't I just make a cut
through the middle...*



Is All Hope Lost?

*And then verify the
components separately and
recompose?*



In other words...

Can we use the inherent
compositional design of networks
to *verify them compositionally*?

In other words...

Can we
compositional verification: verify
each component separately, such
that correctness of the monolithic
system is guaranteed?
compositionally?

Roadmap

Opening up the Stable Routing Problem

The Kirigami Algorithm

Implementing Kirigami in NV

Results and Future Directions

Opening up the Stable Routing Problem

The Stable Routing Problem (SRP)

(Prior Work)

[Beckett et al., SIGCOMM 2018]

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The Stable Routing Problem (SRP)

(Prior Work)

$$R = (\quad , \quad , \quad , \quad)$$

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topology of network


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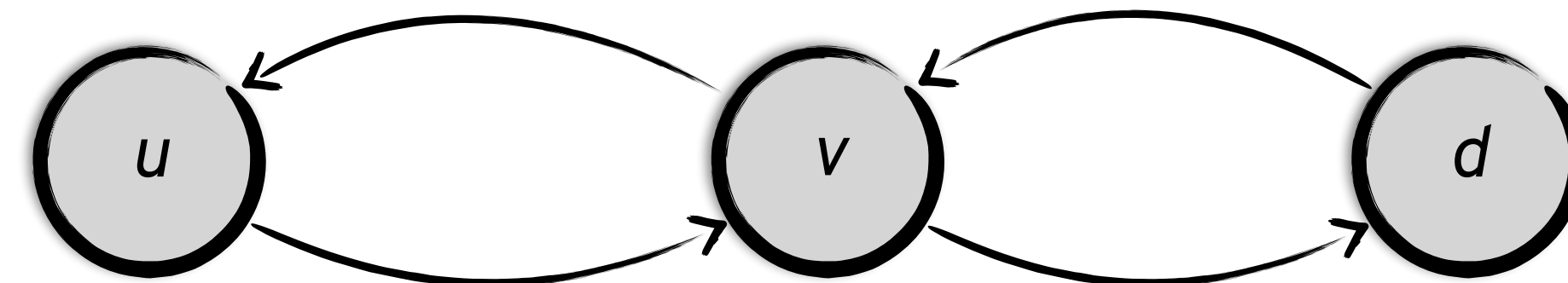
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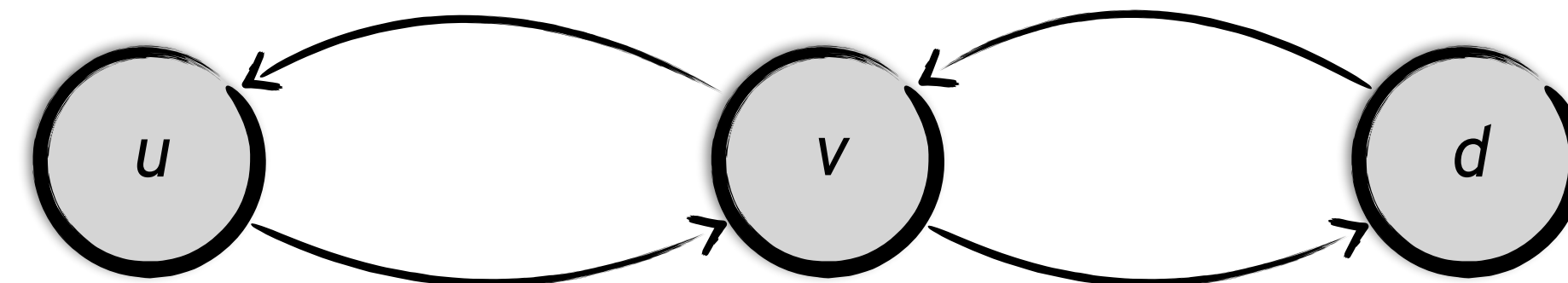
topology of network

$R = (G, A,$

set of attributes

,

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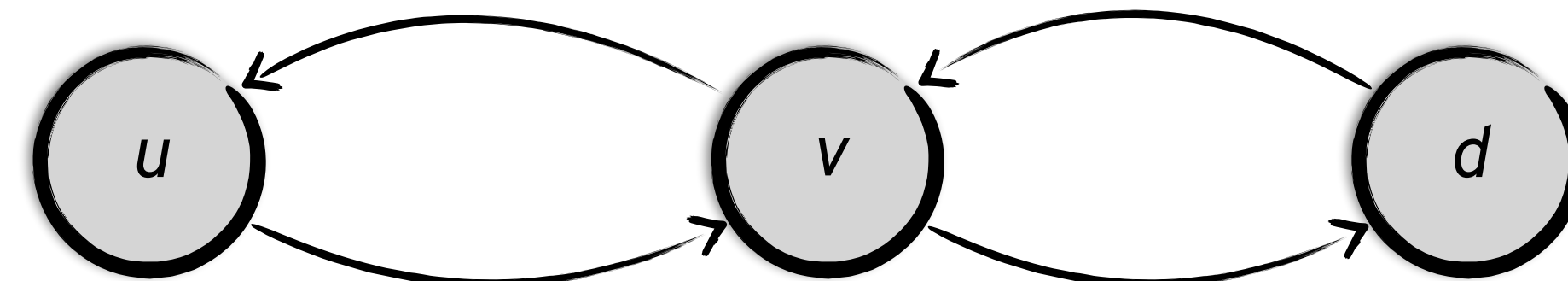
(Prior Work)

topology of network

$R = (G, A, \text{init}, \quad , \quad)$

set of attributes

initial attribute at each node



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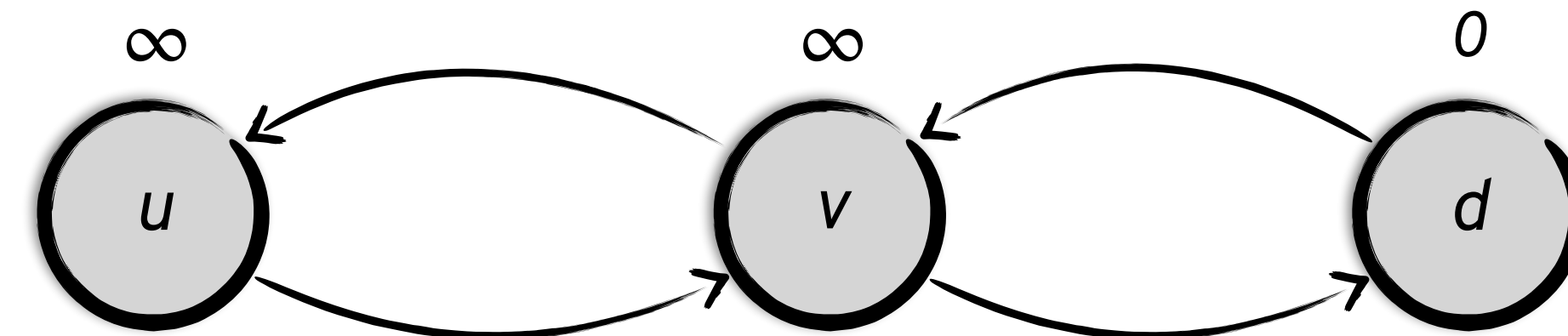
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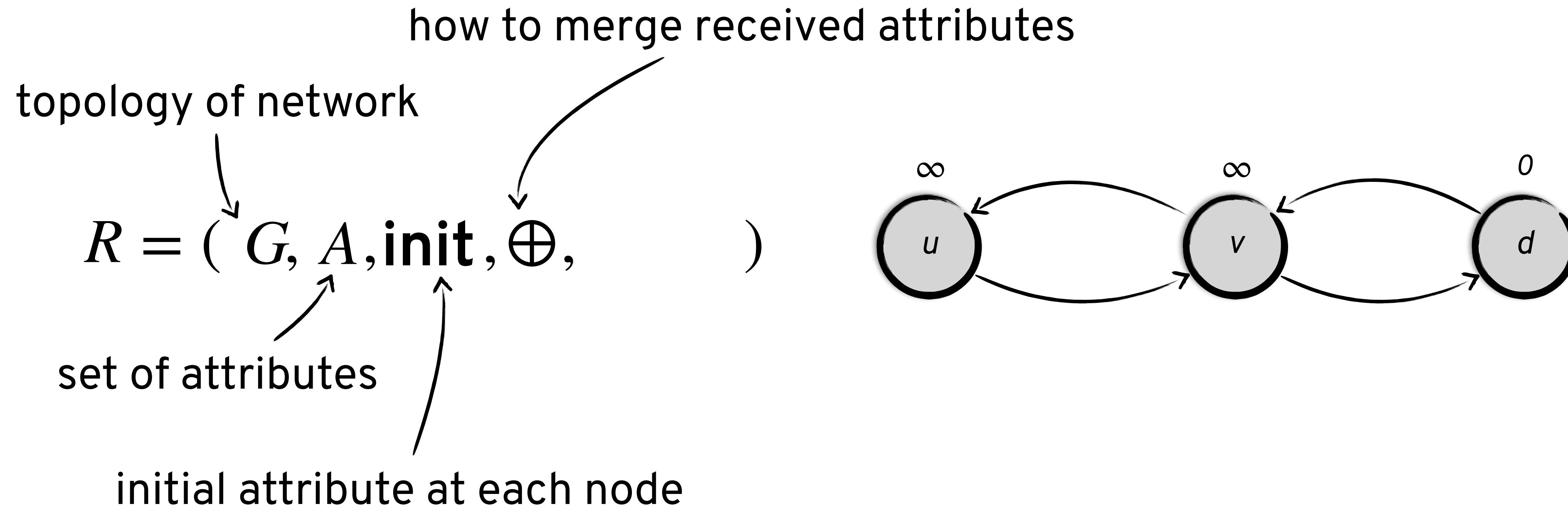
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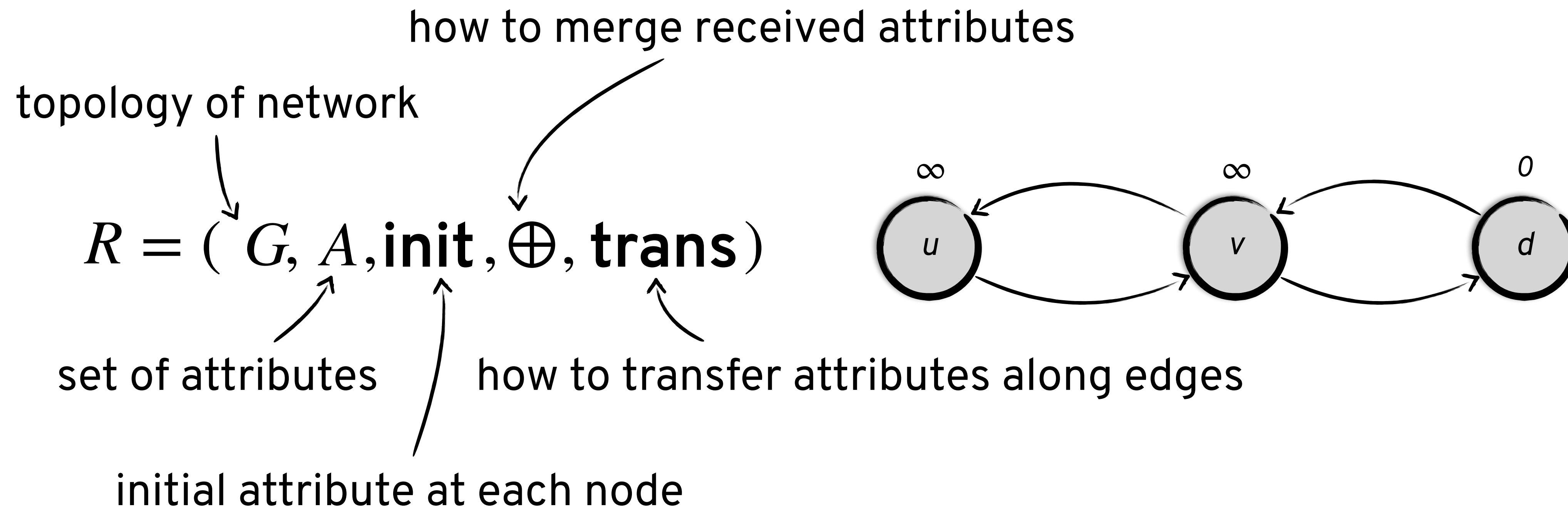
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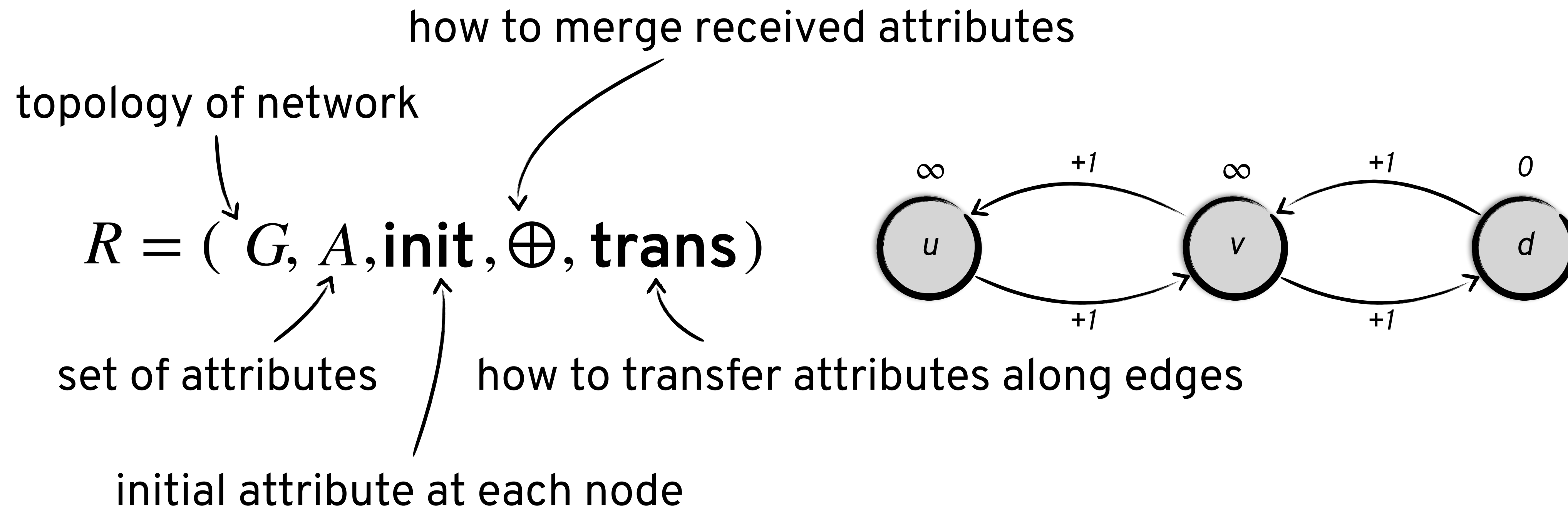
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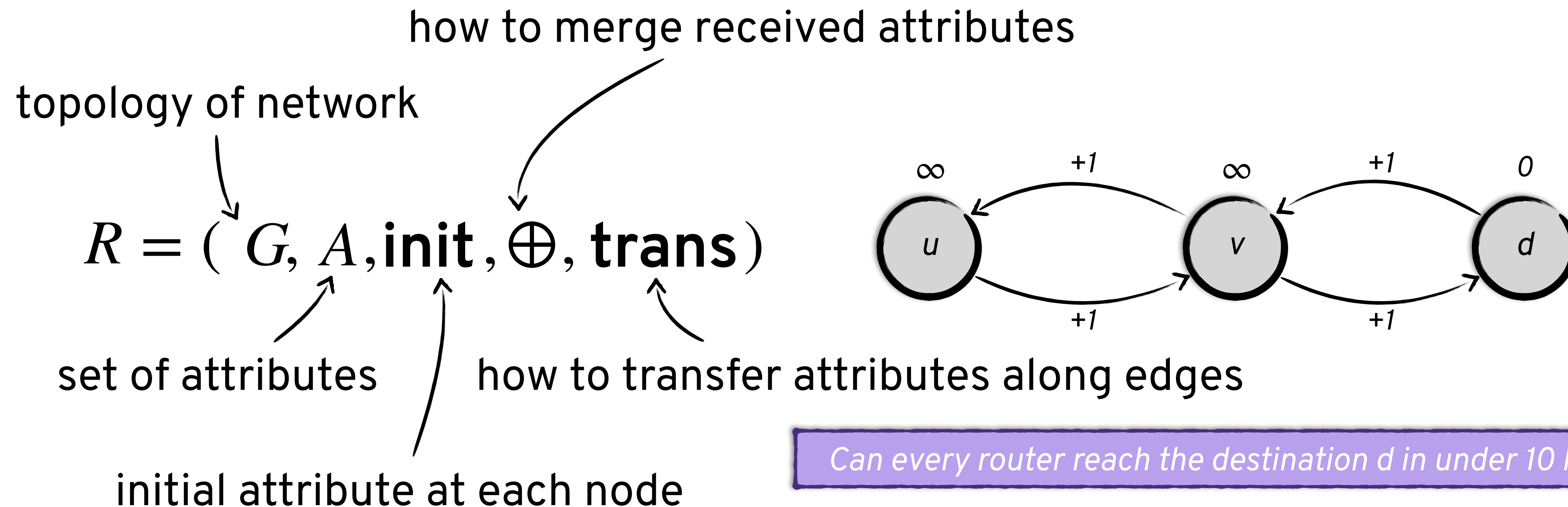
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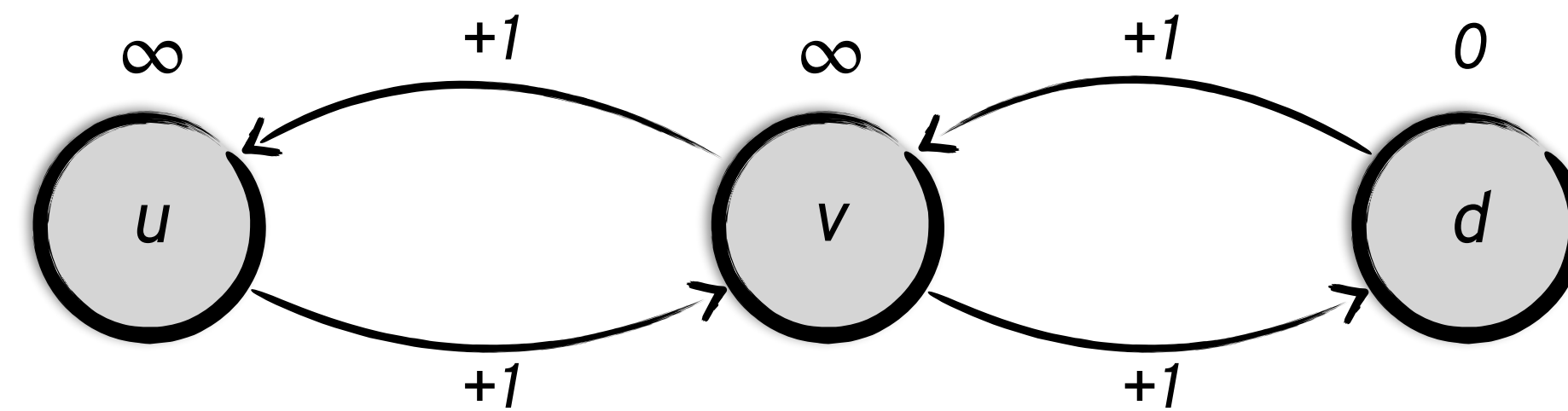
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Can every router reach the destination d in under 10 hops?

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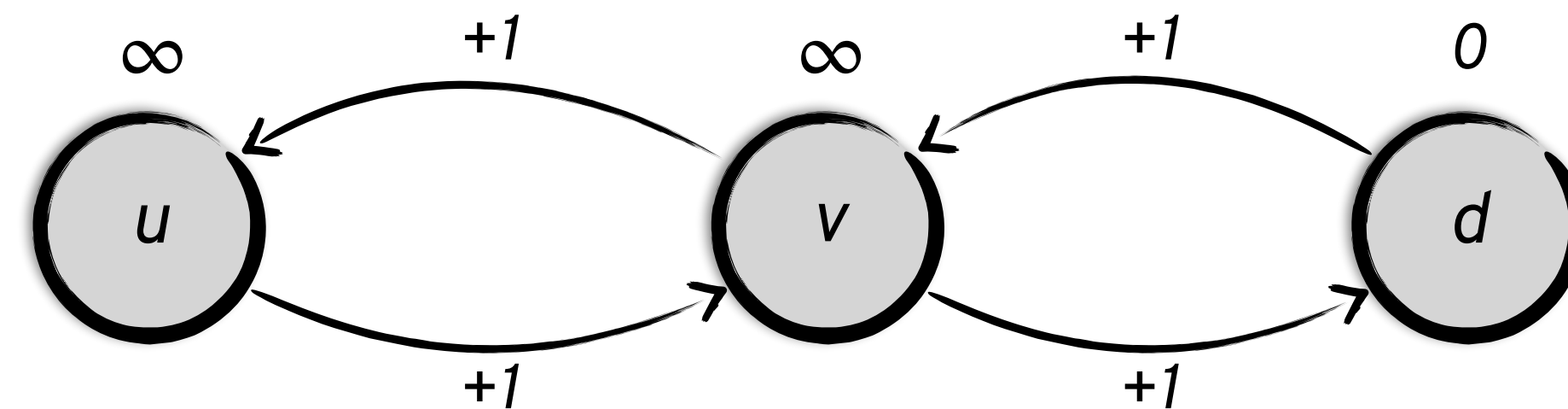
$$A = \mathbb{N}$$

$$\text{init}(n) = \text{if } n = d \text{ then } 0 \text{ else } \infty$$

$$a \oplus b = \min(a, b)$$

$$\text{trans}(e, x) = x + 1$$

Step: 0



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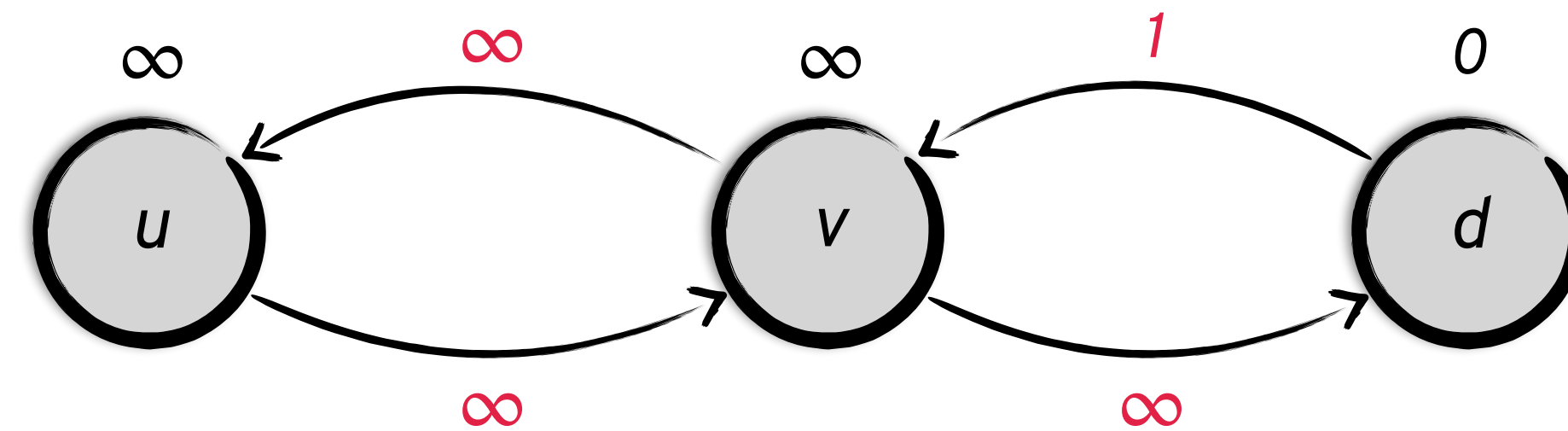
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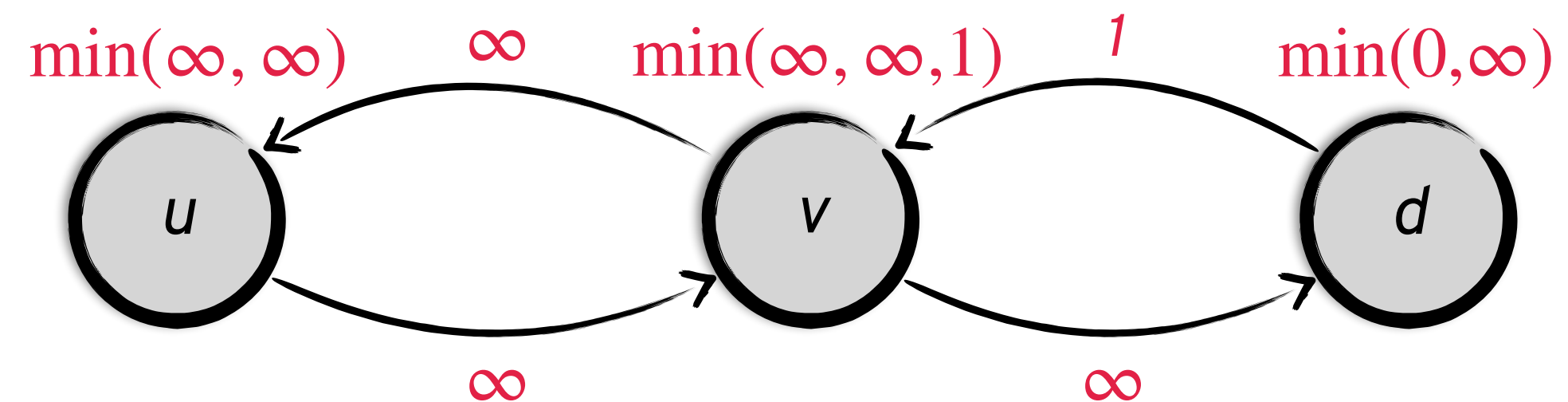
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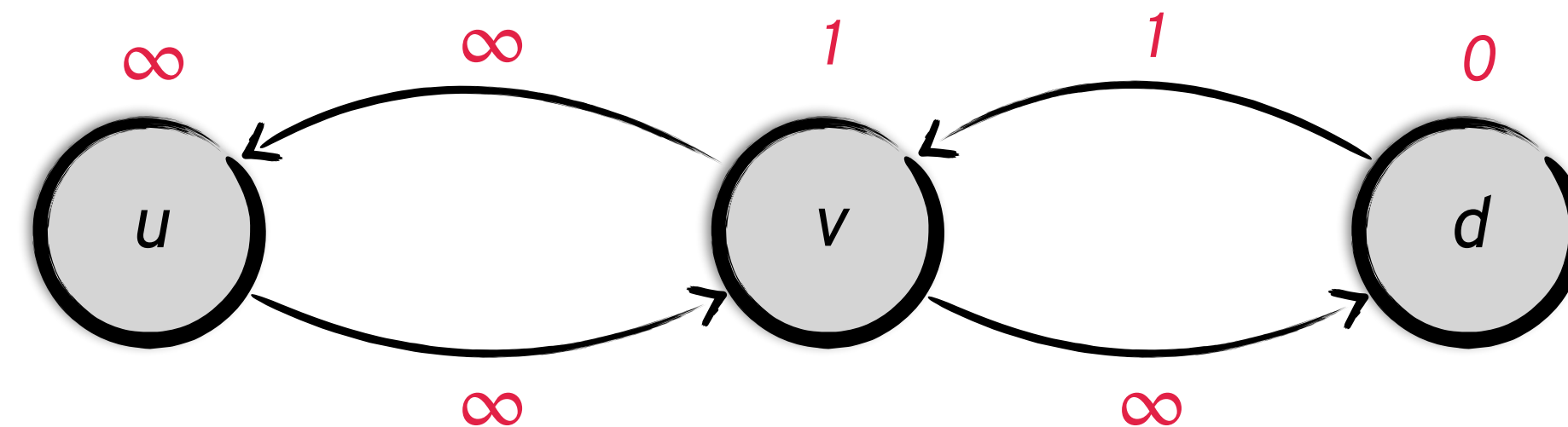
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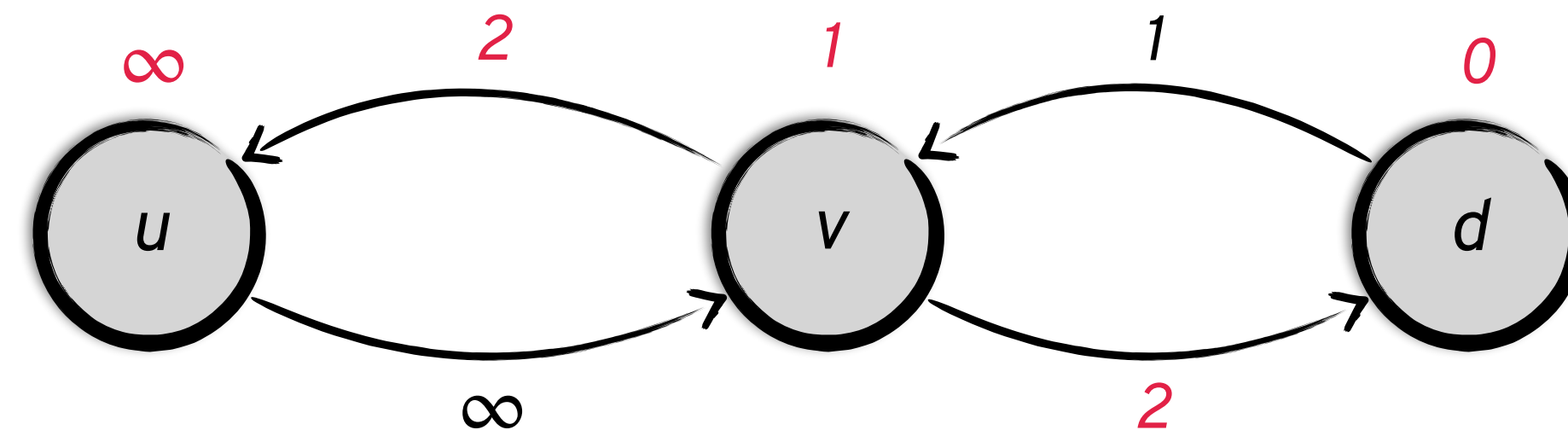
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Step: 2



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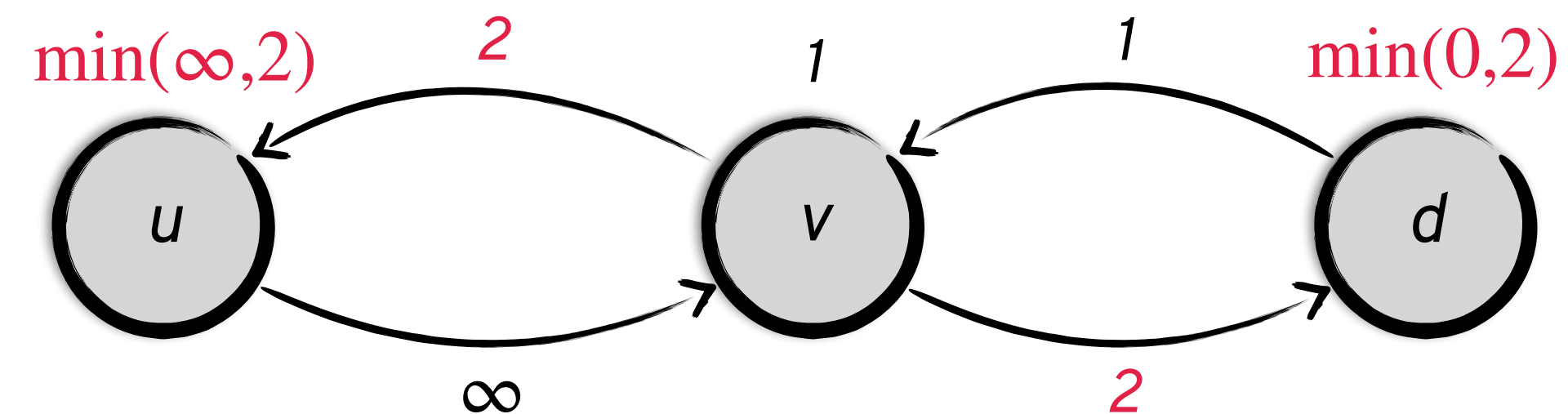
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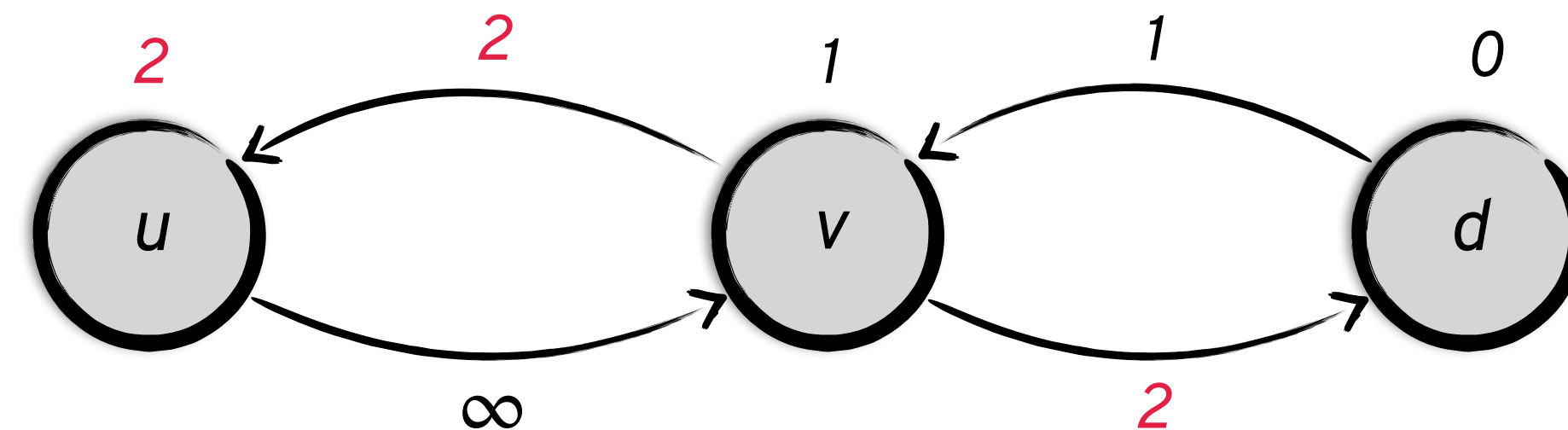
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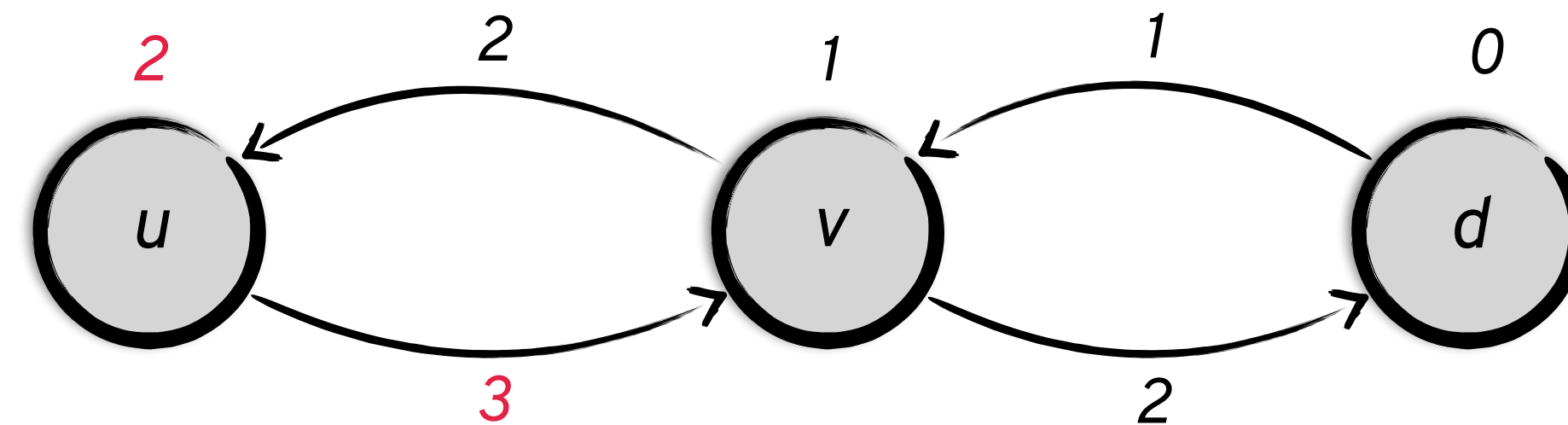
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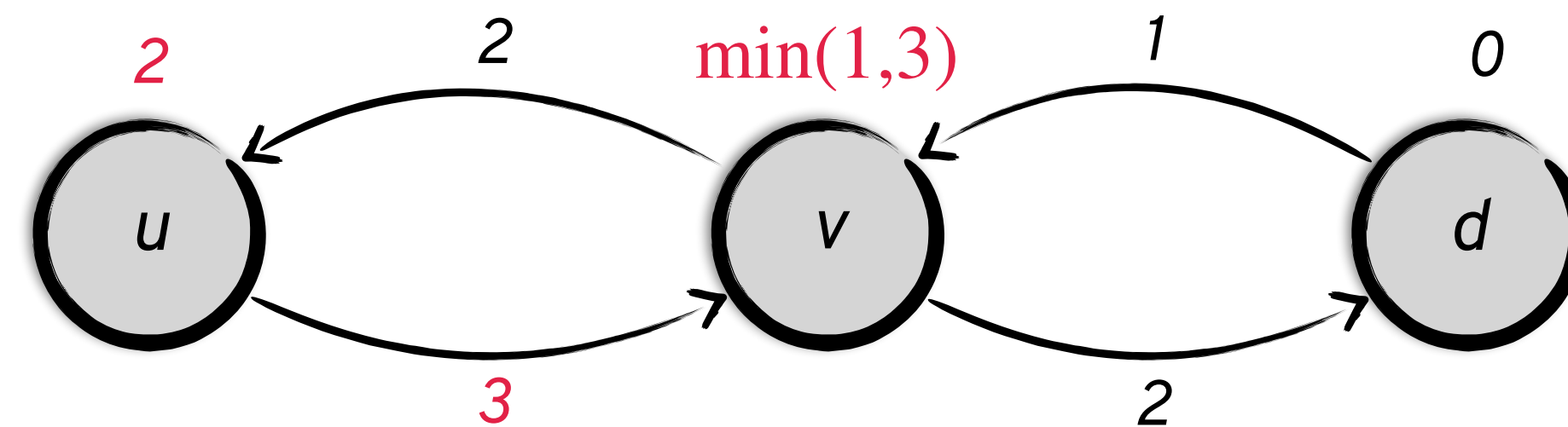
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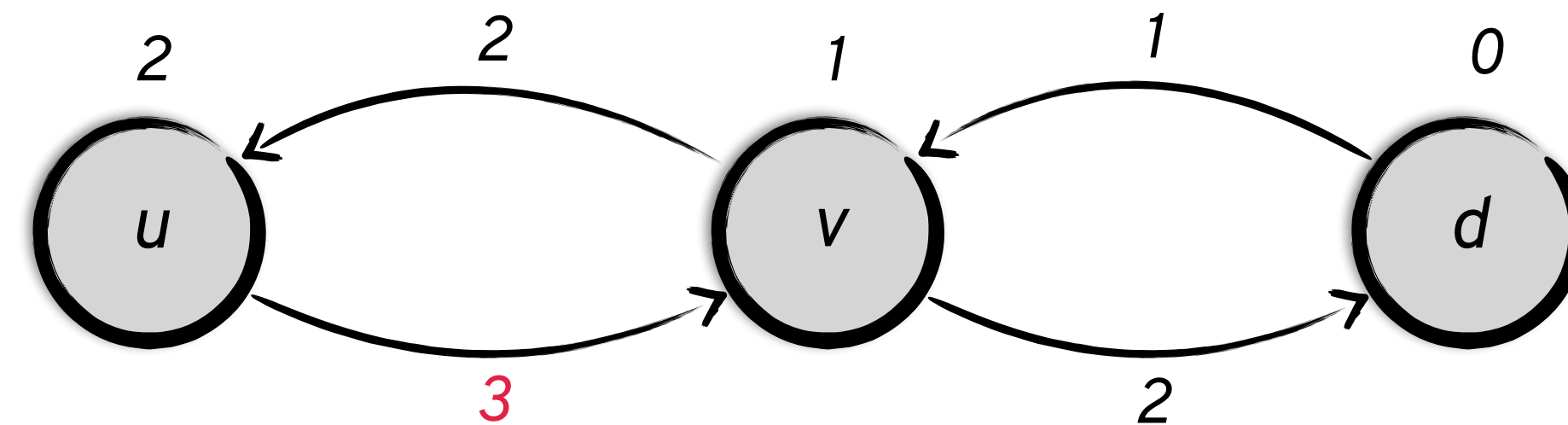
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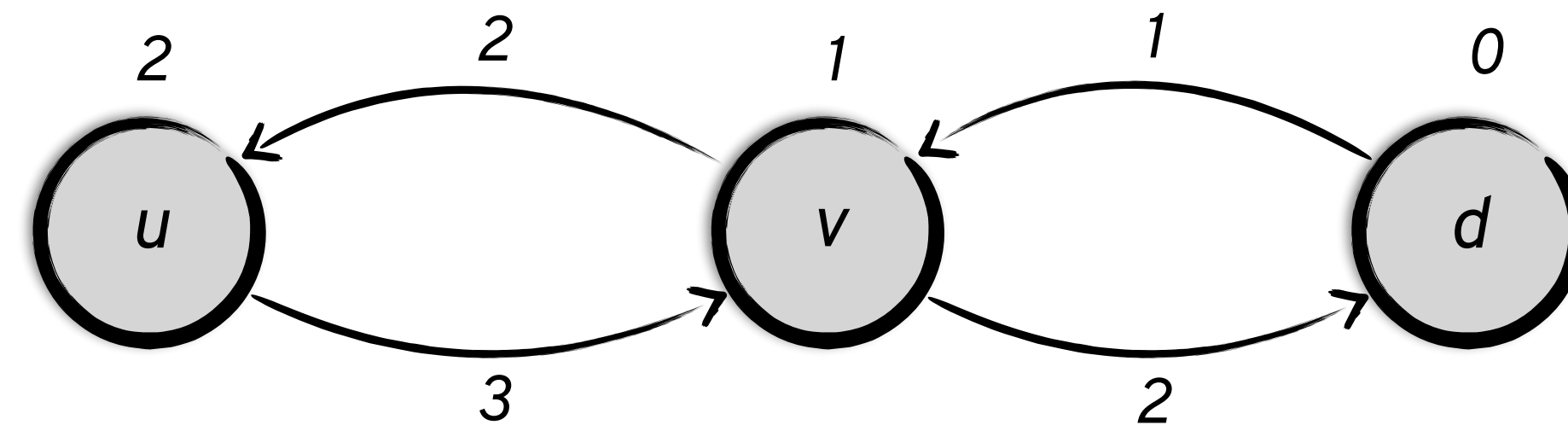
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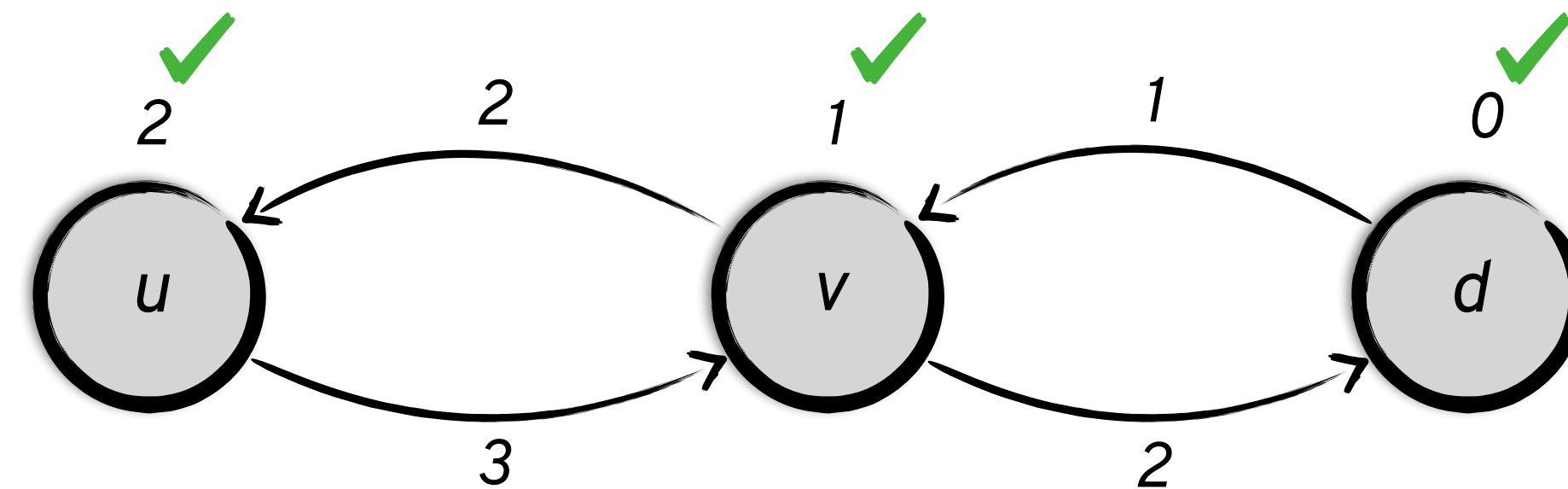
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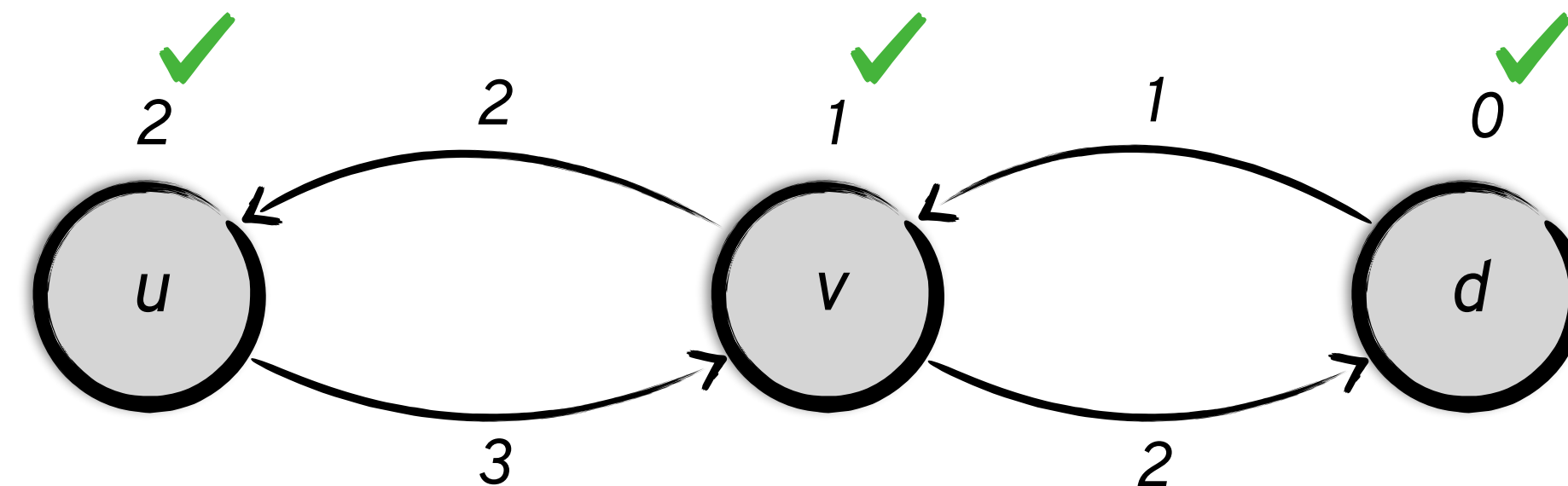
captures *converged* solutions:

$$\mathcal{L}(u) = 2$$

$$\mathcal{L}(v) = 1$$

$$\mathcal{L}(d) = 0$$

Step: 4



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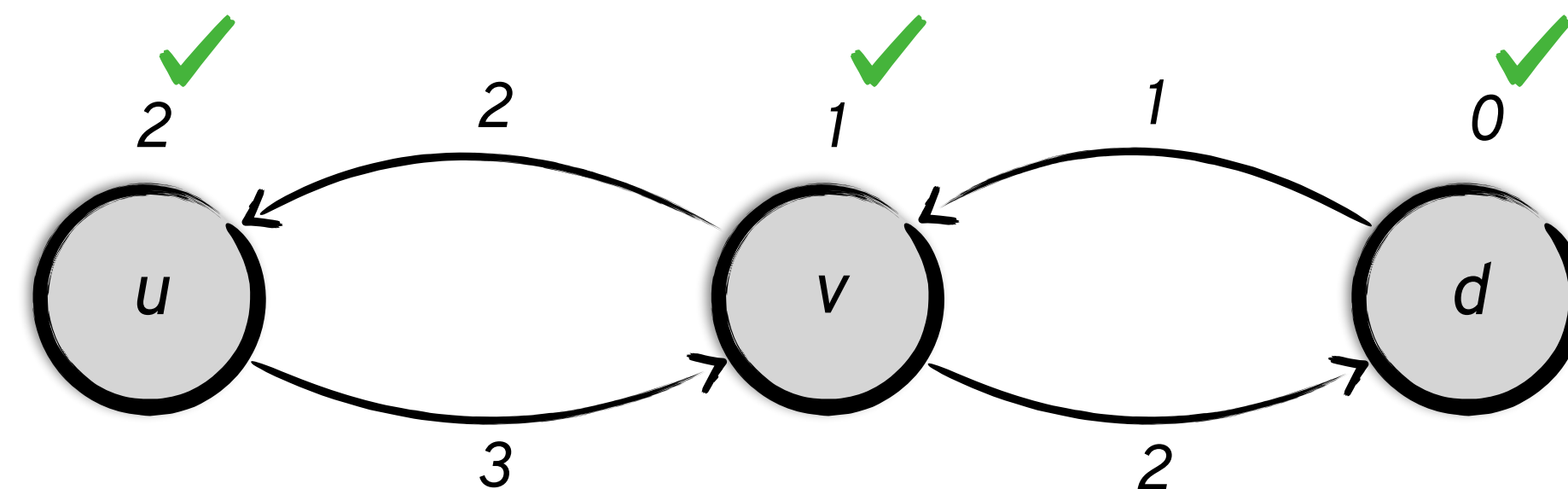
captures *converged* solutions:

$$\mathcal{L}(u) = \min(\infty, \mathcal{L}(v) + 1)$$

$$\mathcal{L}(v) = \min(\infty, \mathcal{L}(u) + 1, \mathcal{L}(d) + 1)$$

$$\mathcal{L}(d) = \min(0, \mathcal{L}(v) + 1)$$

Step: 4

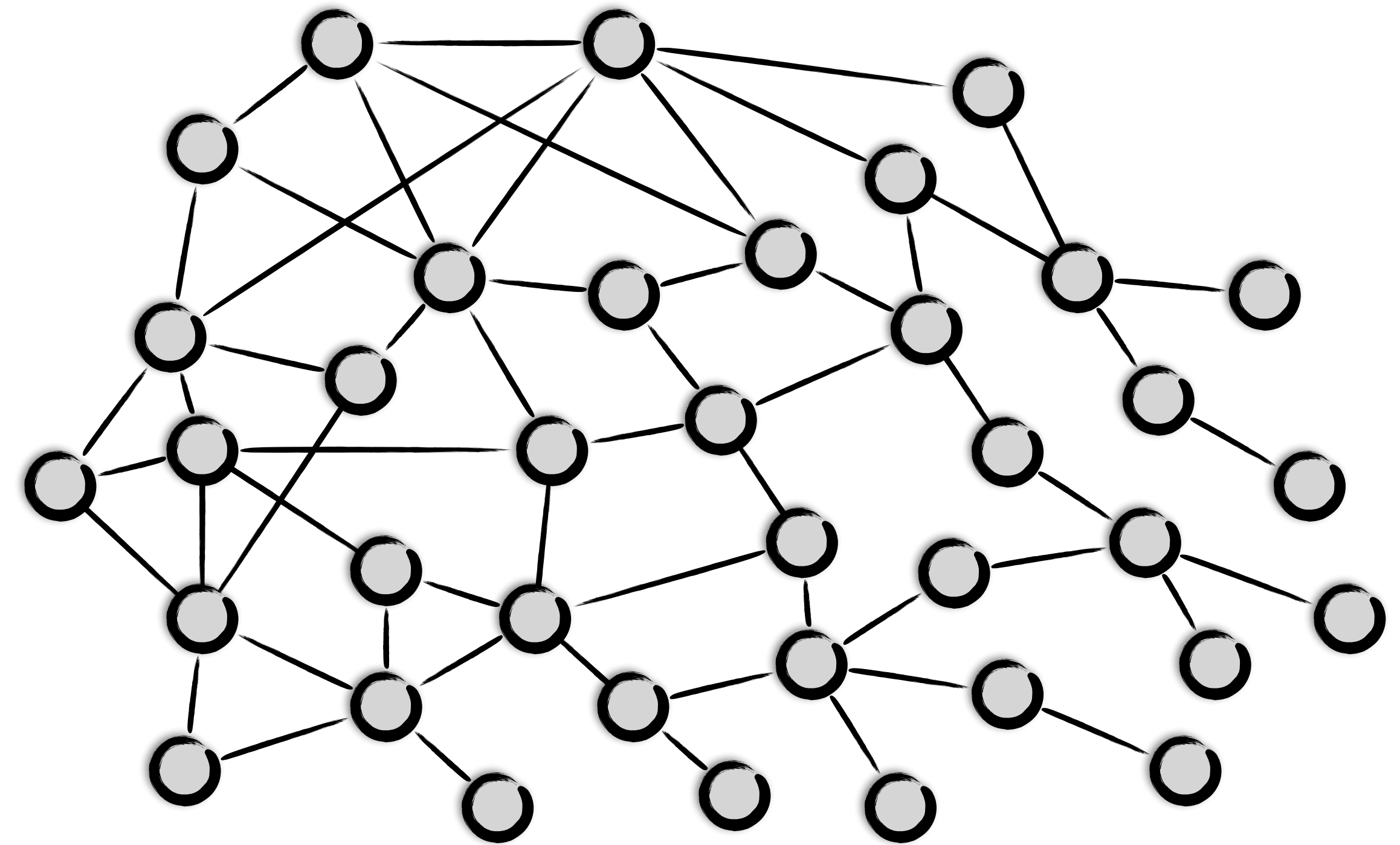


Can every router reach the destination d in under 10 hops?

[Beckett et al., SIGCOMM 2018]
[Griffin and Sobrinho, SIGCOMM 2005]
[Sobrinho, IEANEP 2005]
[Griffin et al., IEANEP 2002]

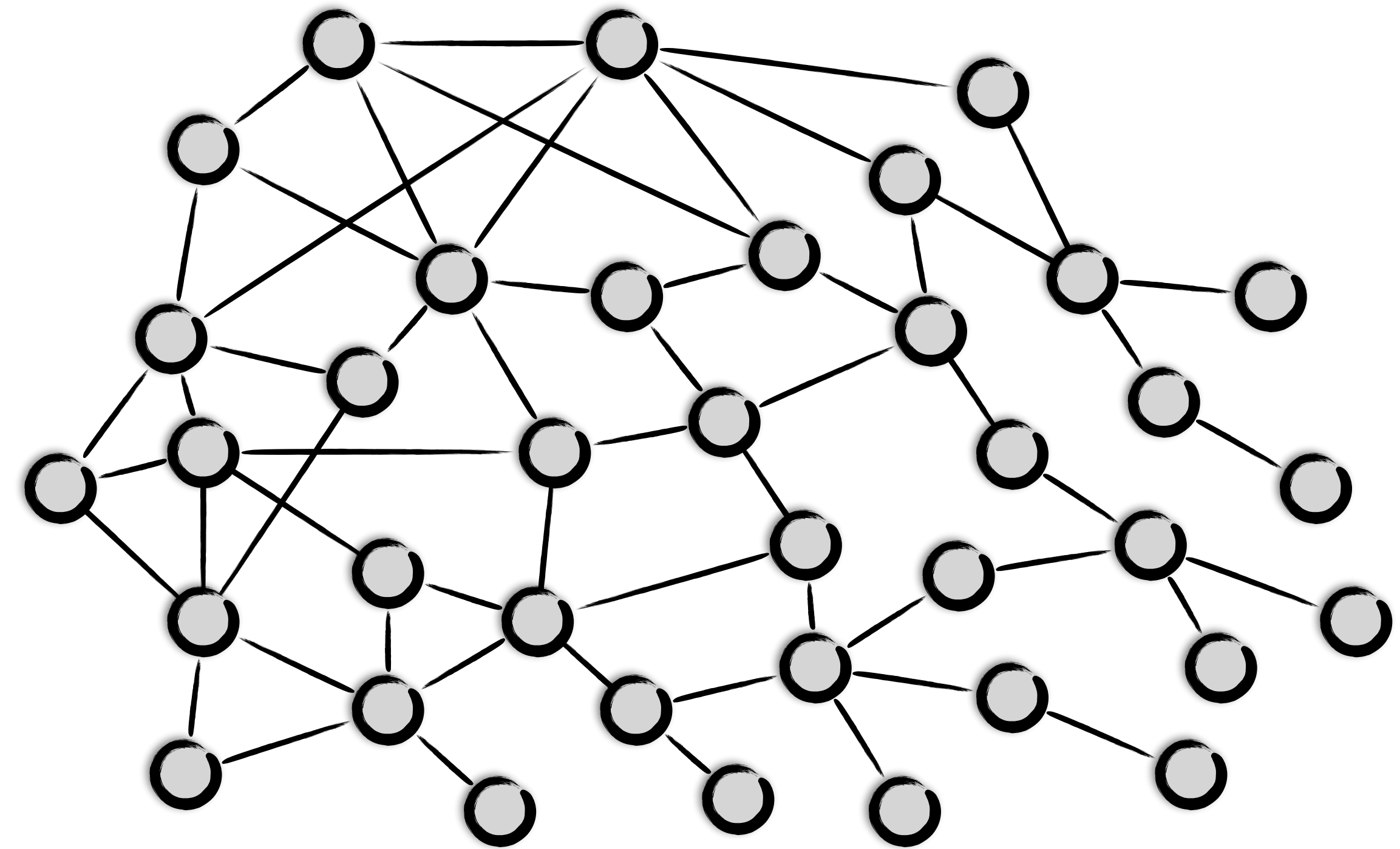
Verifying SRPs Compositionally

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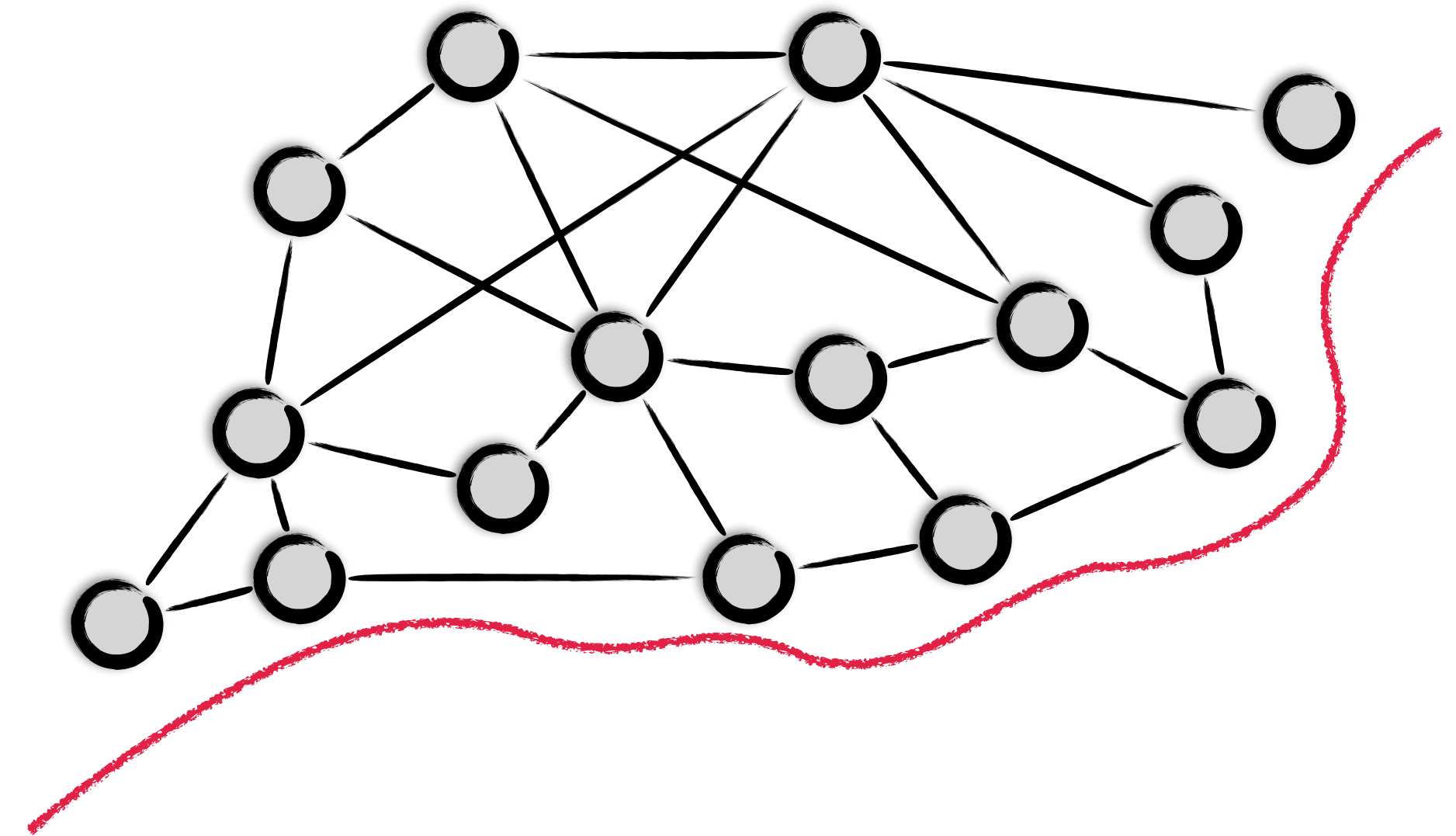
Verifying SRPs Compositionally

- Identify a cut-set of edges as forming an *interface*



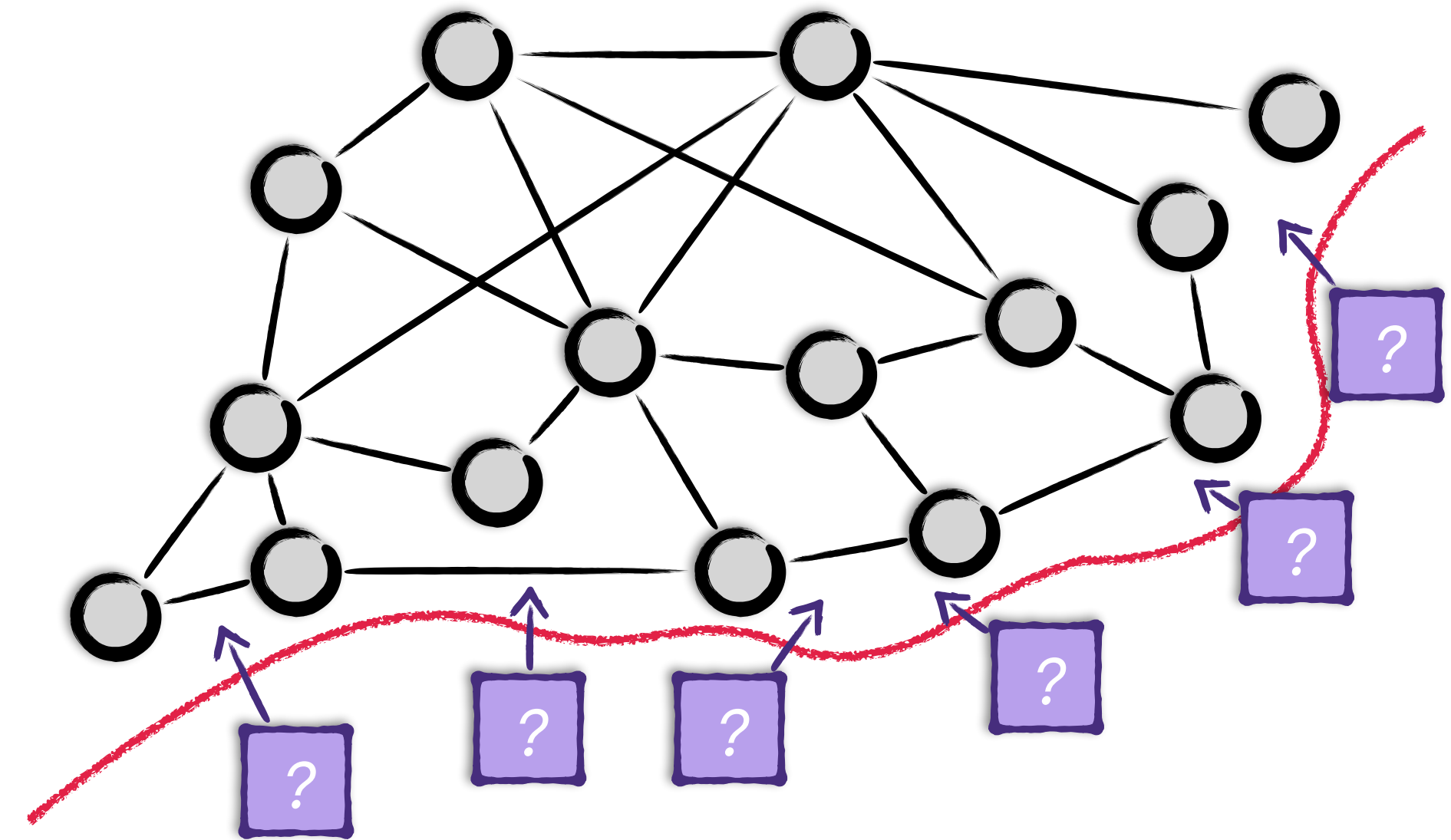
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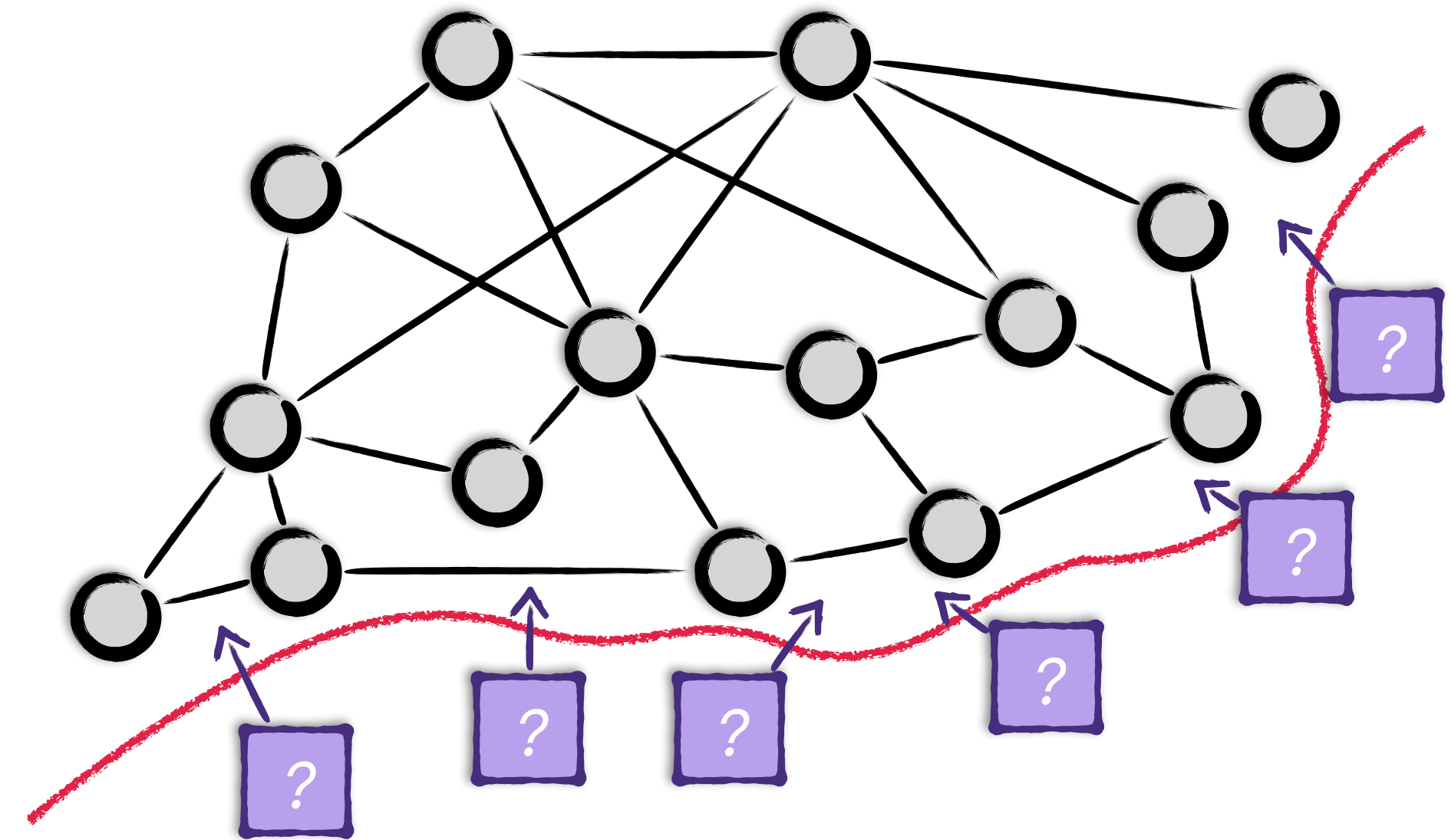
Verifying SRPs Compositionally

- Identify a cut-set of edges as forming an *interface*
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 - (We'll assume that the user provides these)

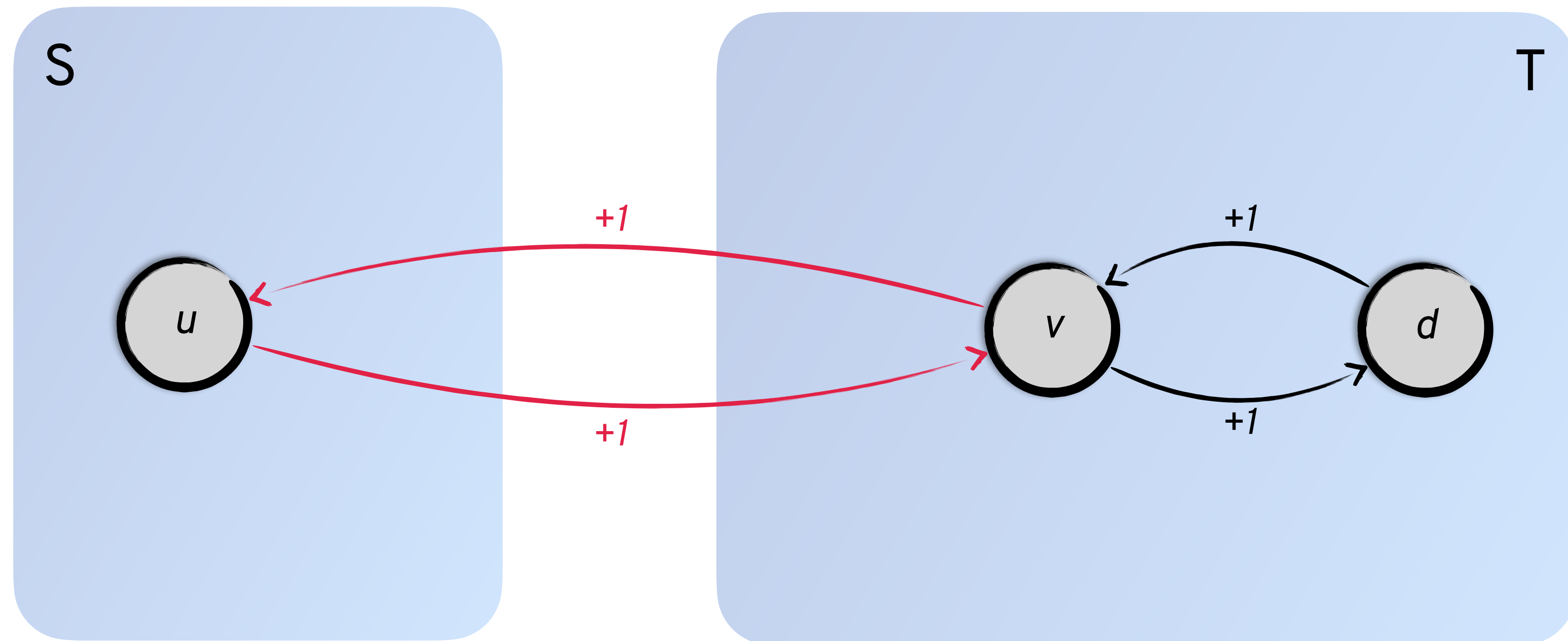


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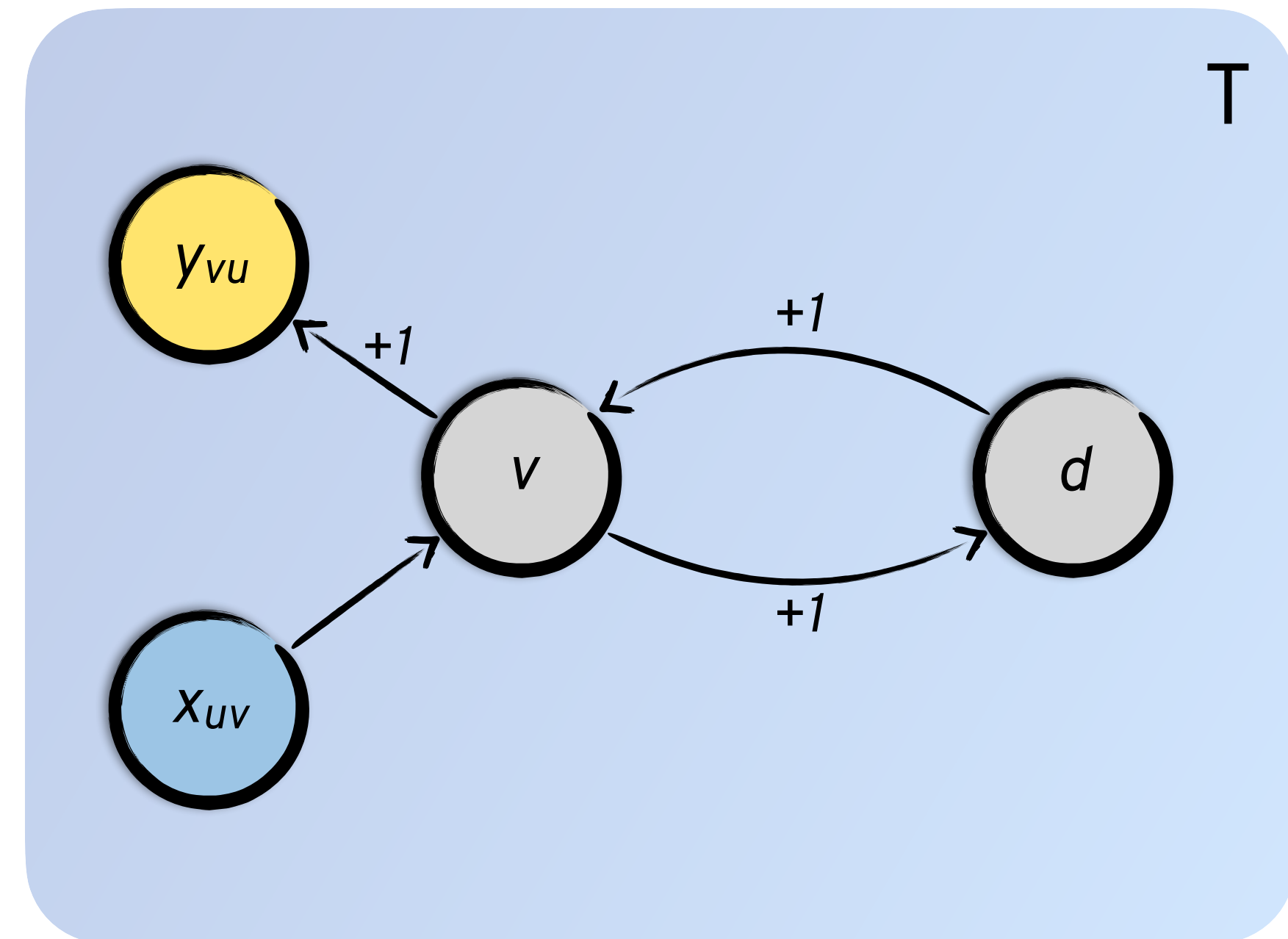
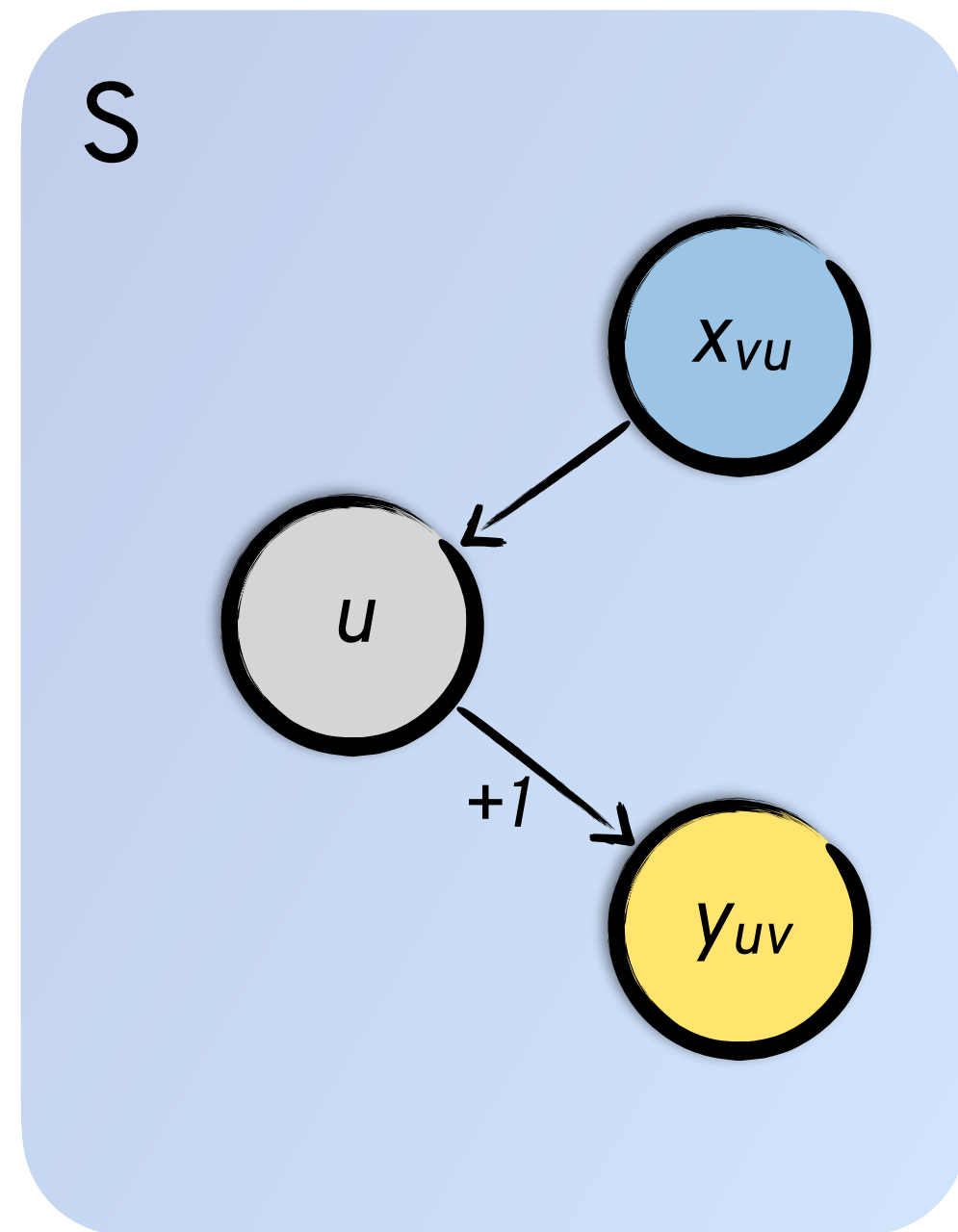
- Identify a cut-set of edges as forming an *interface*
- *Partition* the network along those edges
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 - (We'll assume that the user provides these)
- Verify the components using hypotheses, and then demonstrate that this suffices to prove the monolithic network correct



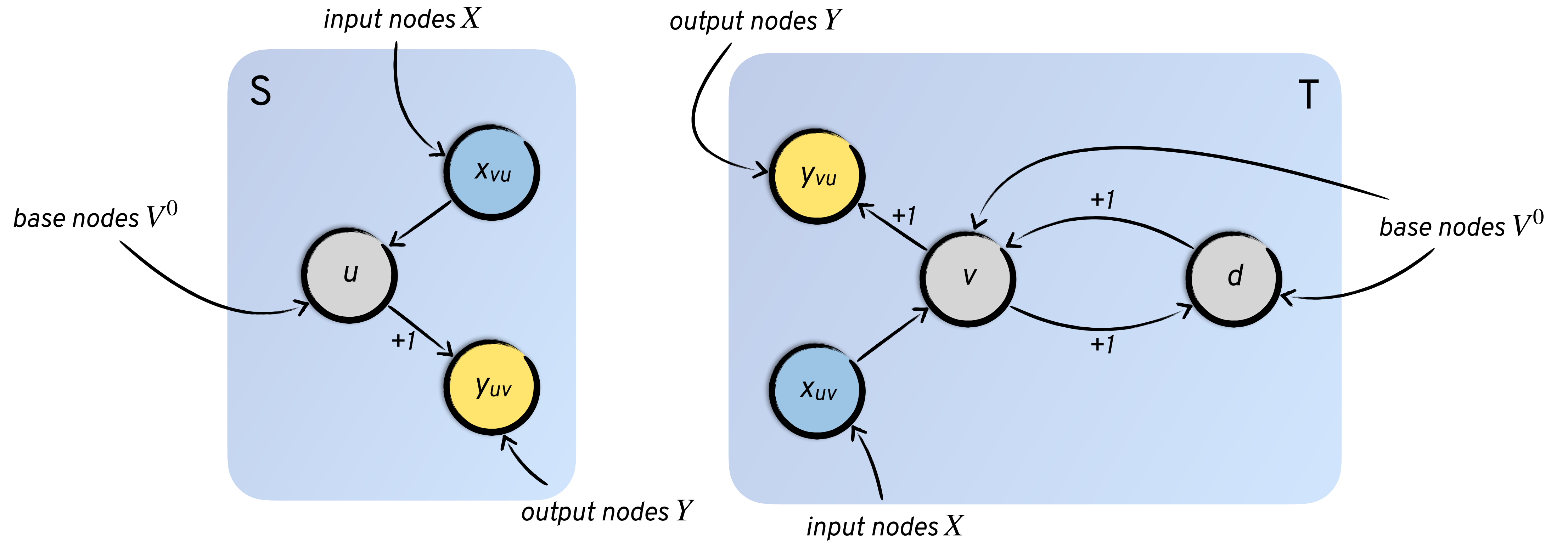
Open SRPs



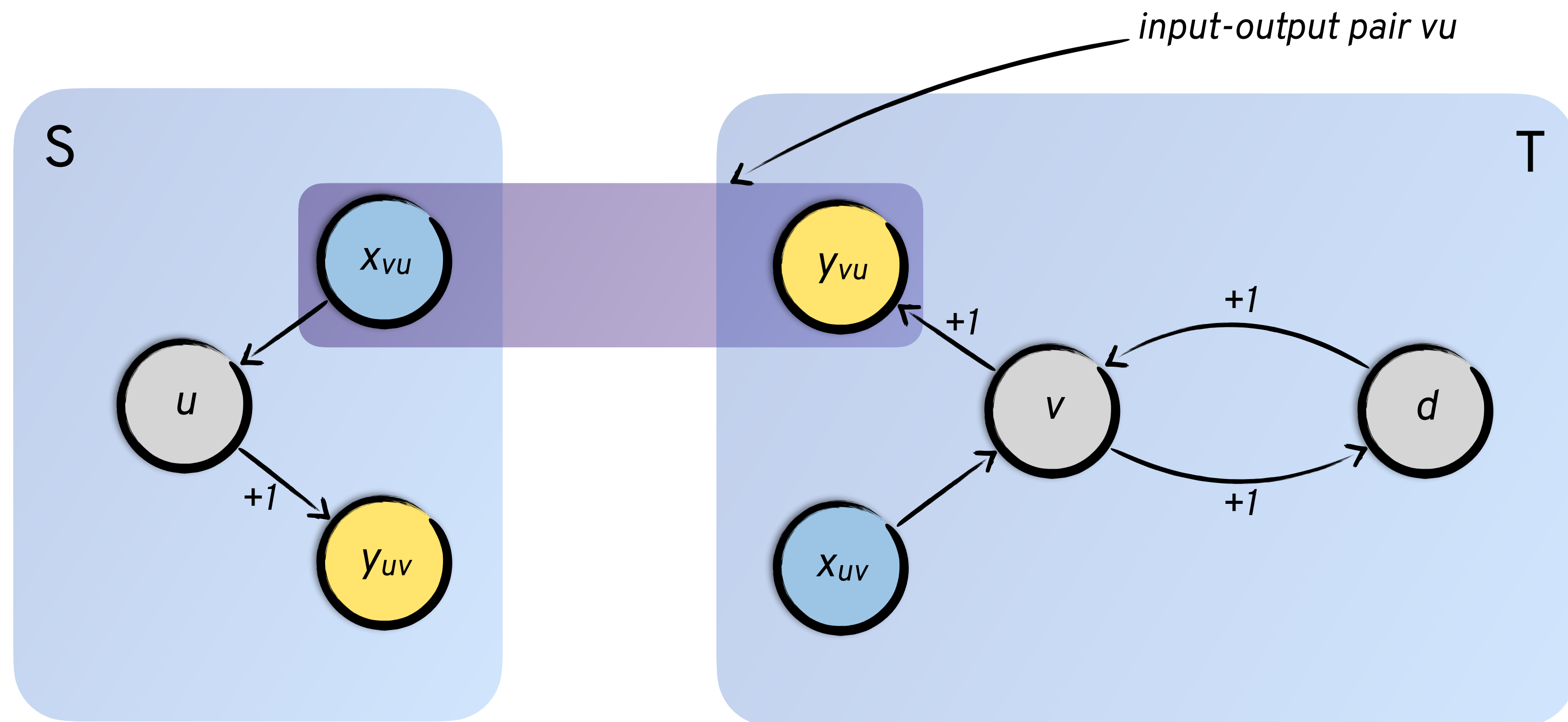
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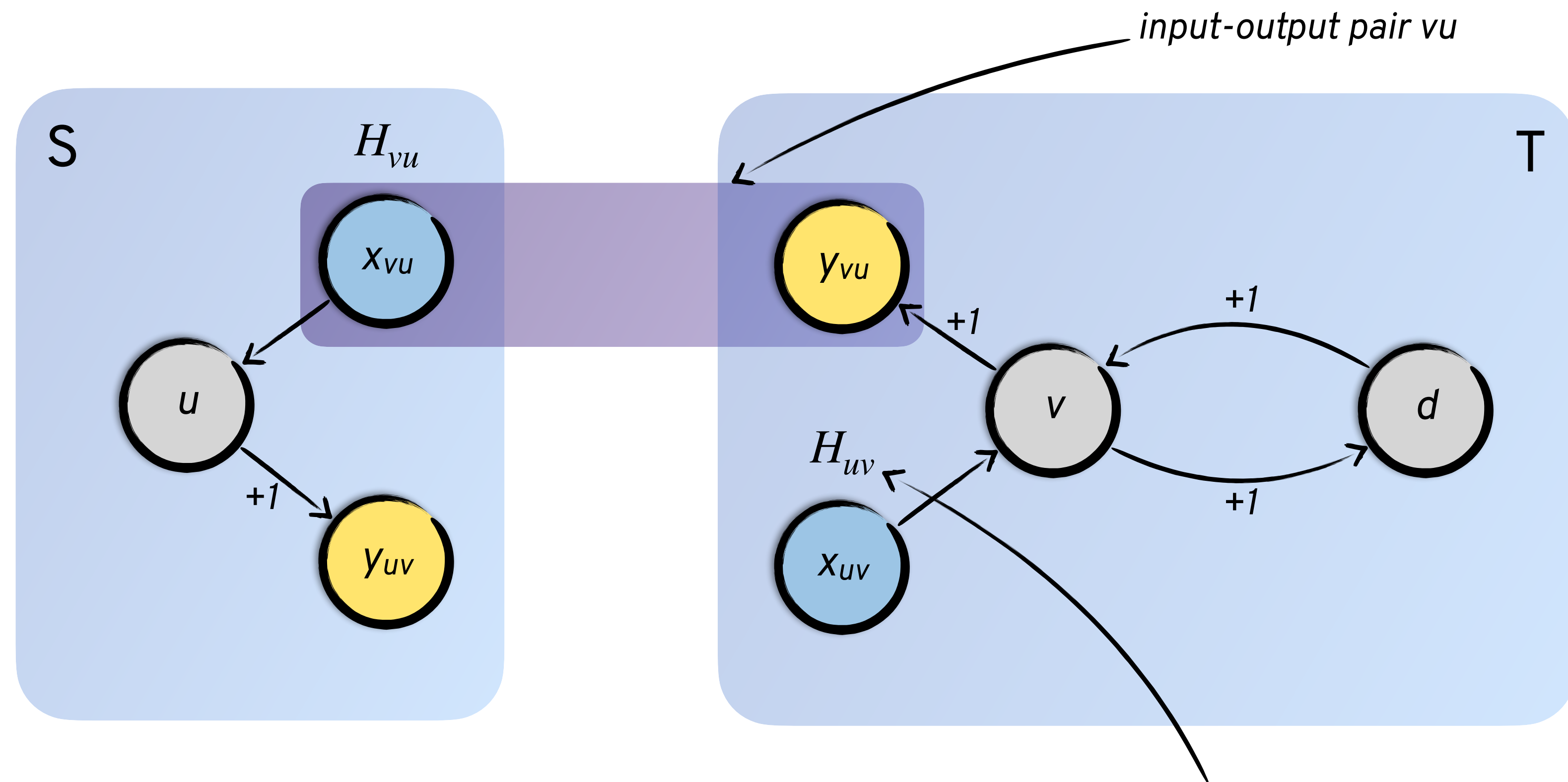
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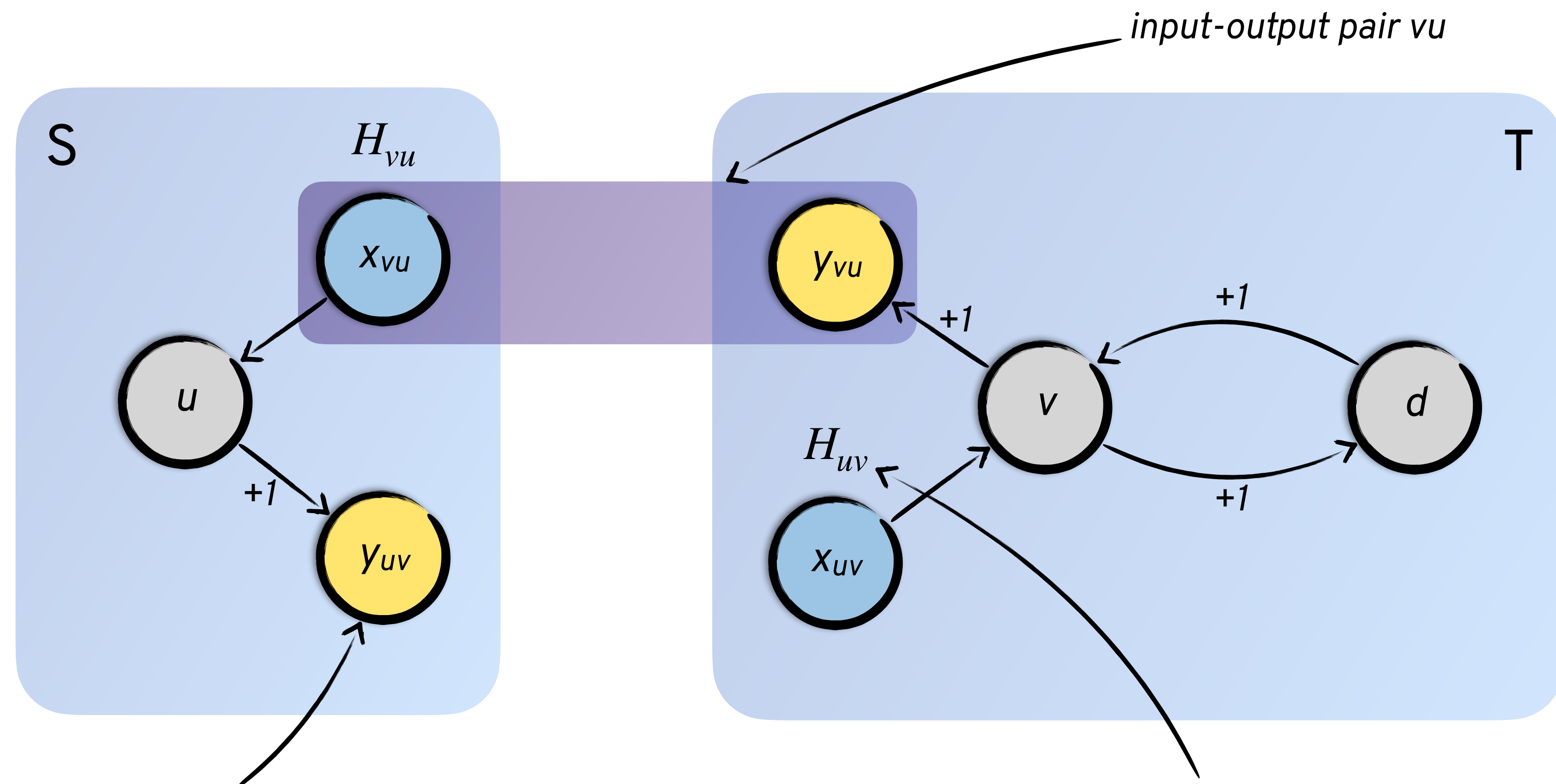


Open SRPs



hypotheses H are formulae over the solutions of inputs:
they provide **assumptions** about the other partition

Open SRPs

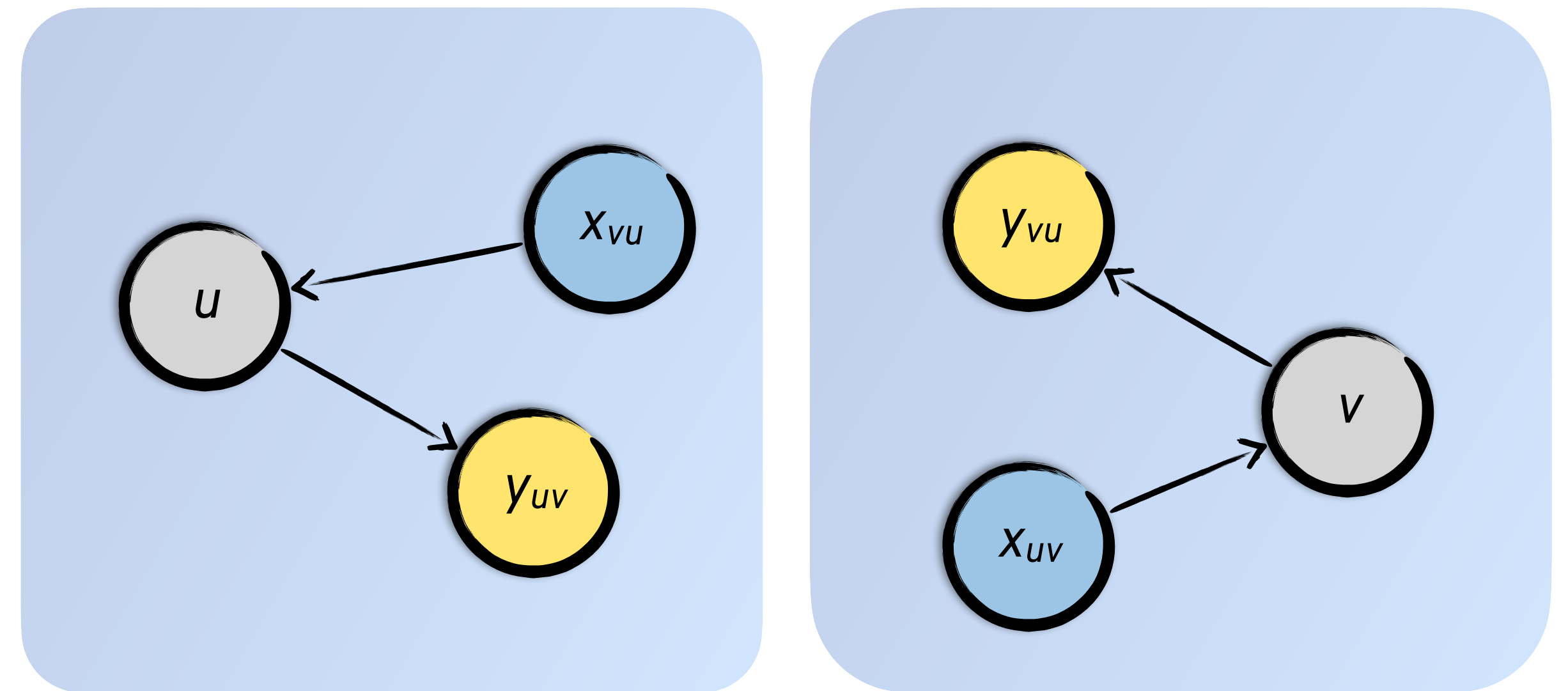


formulae over **output solutions** provide **guarantees** for the opposite partition

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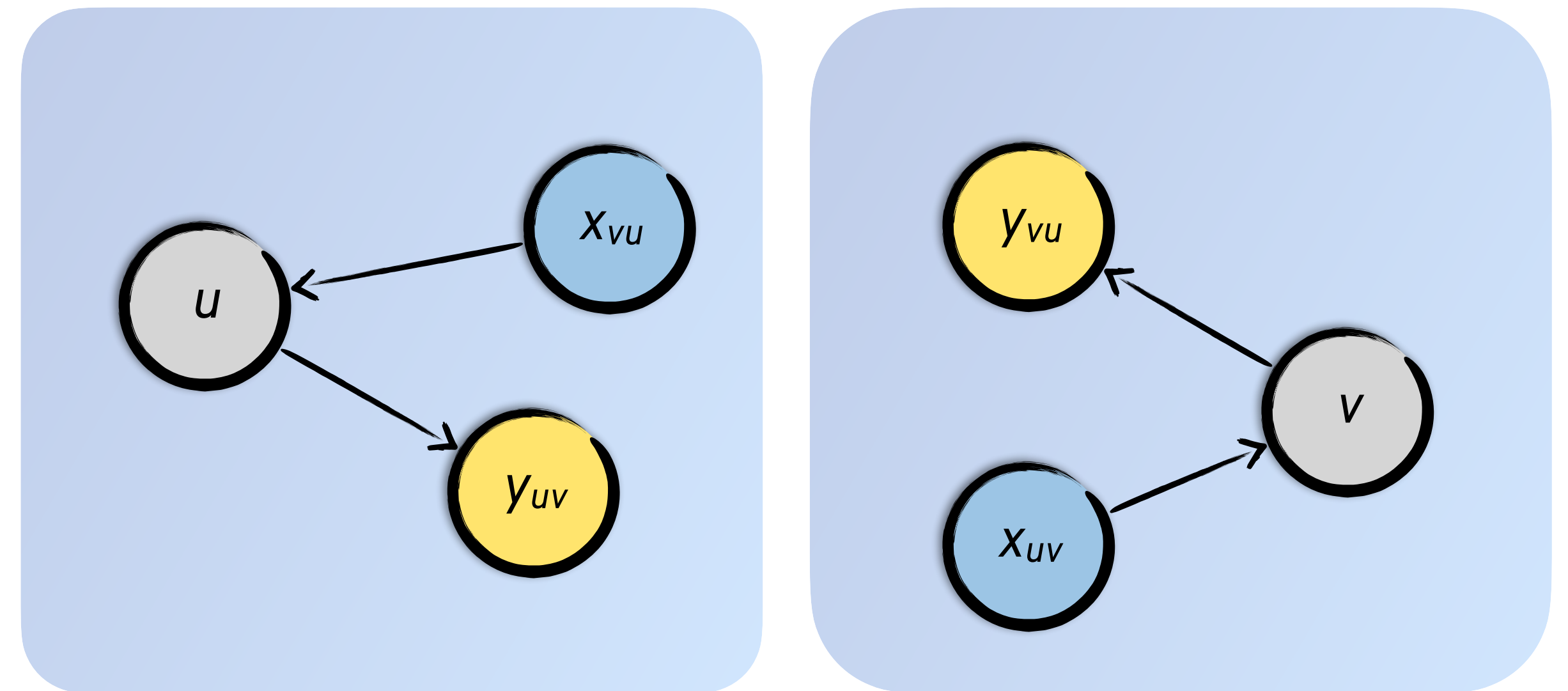
The Kirigami Algorithm

Interfaces



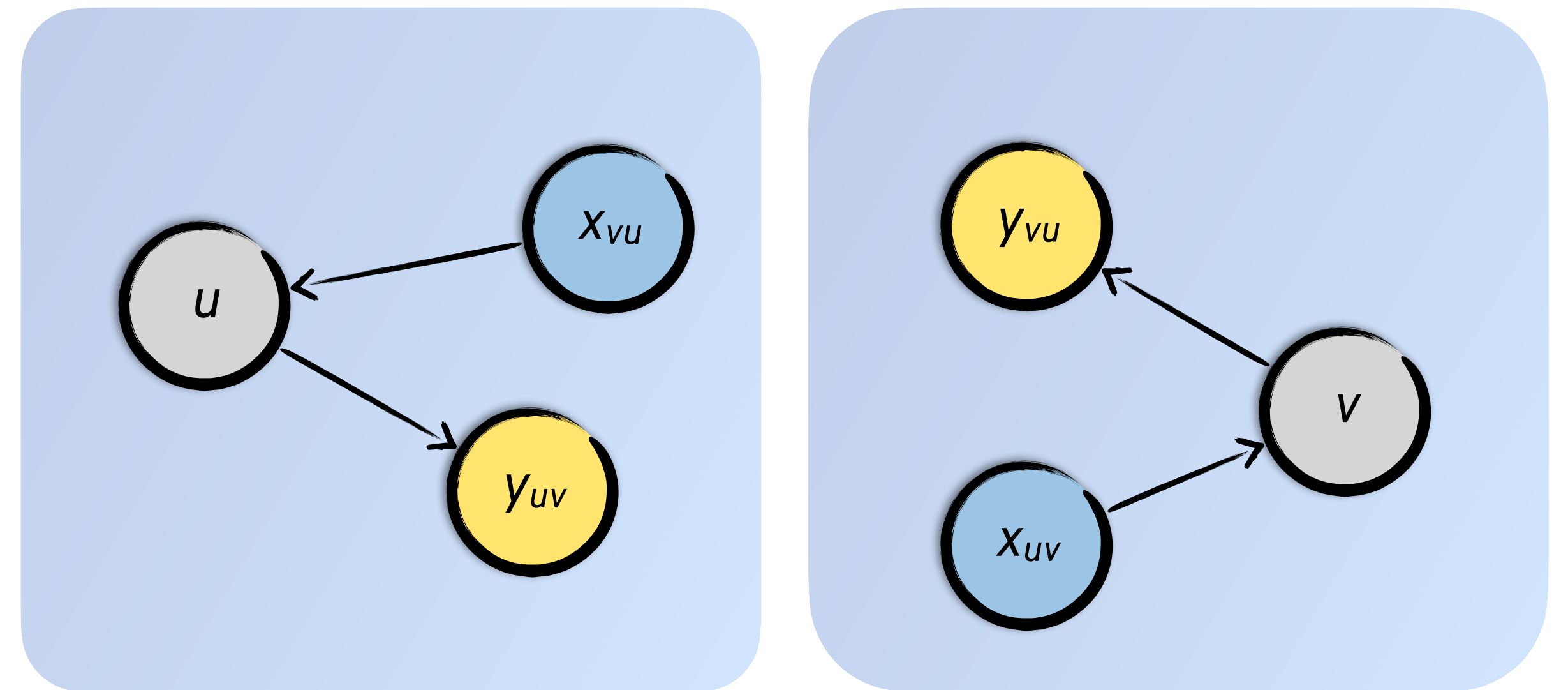
Interfaces

- To verify an SRP compositionally, let's define an *interface*!



Interfaces

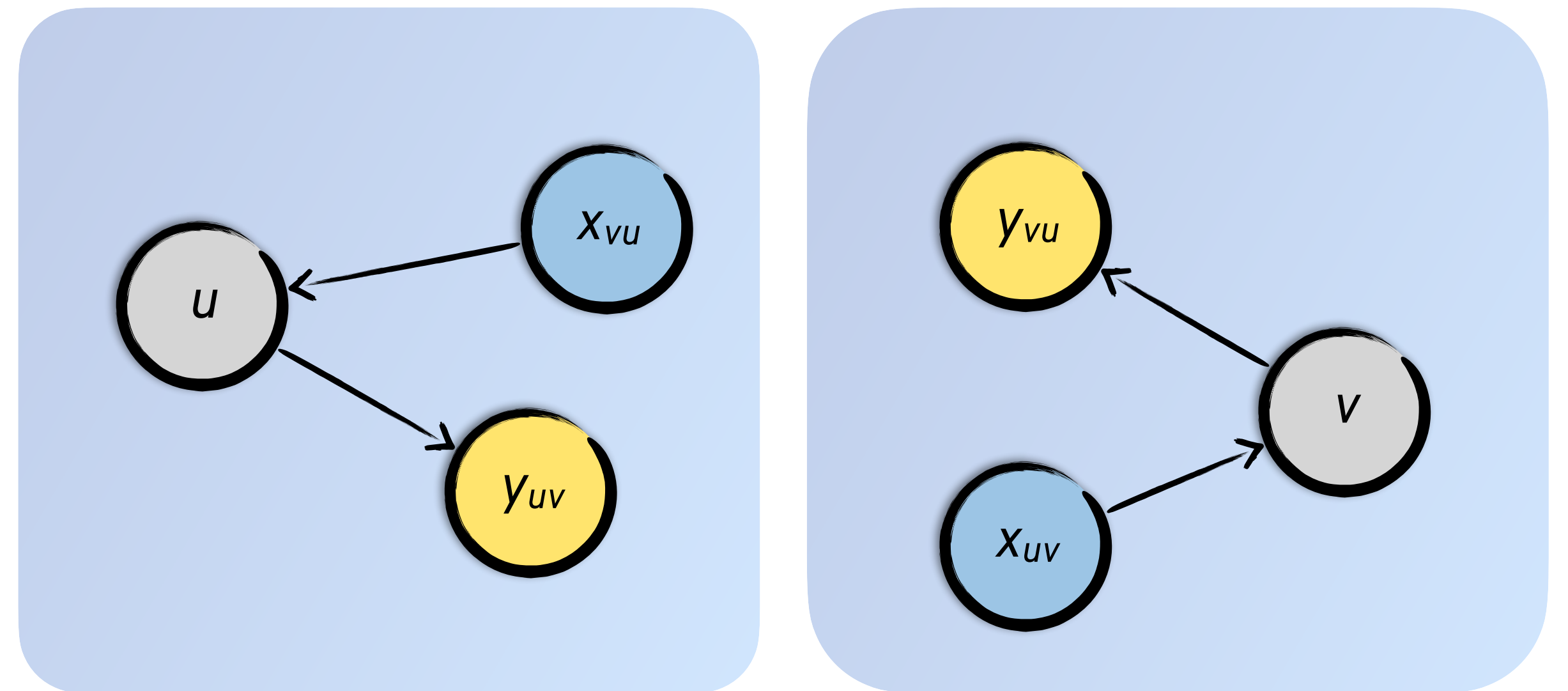
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$$\bullet \mathcal{I} = \left\{ \left(u_j, v_j, H_{u_j v_j} \right) \right\}$$

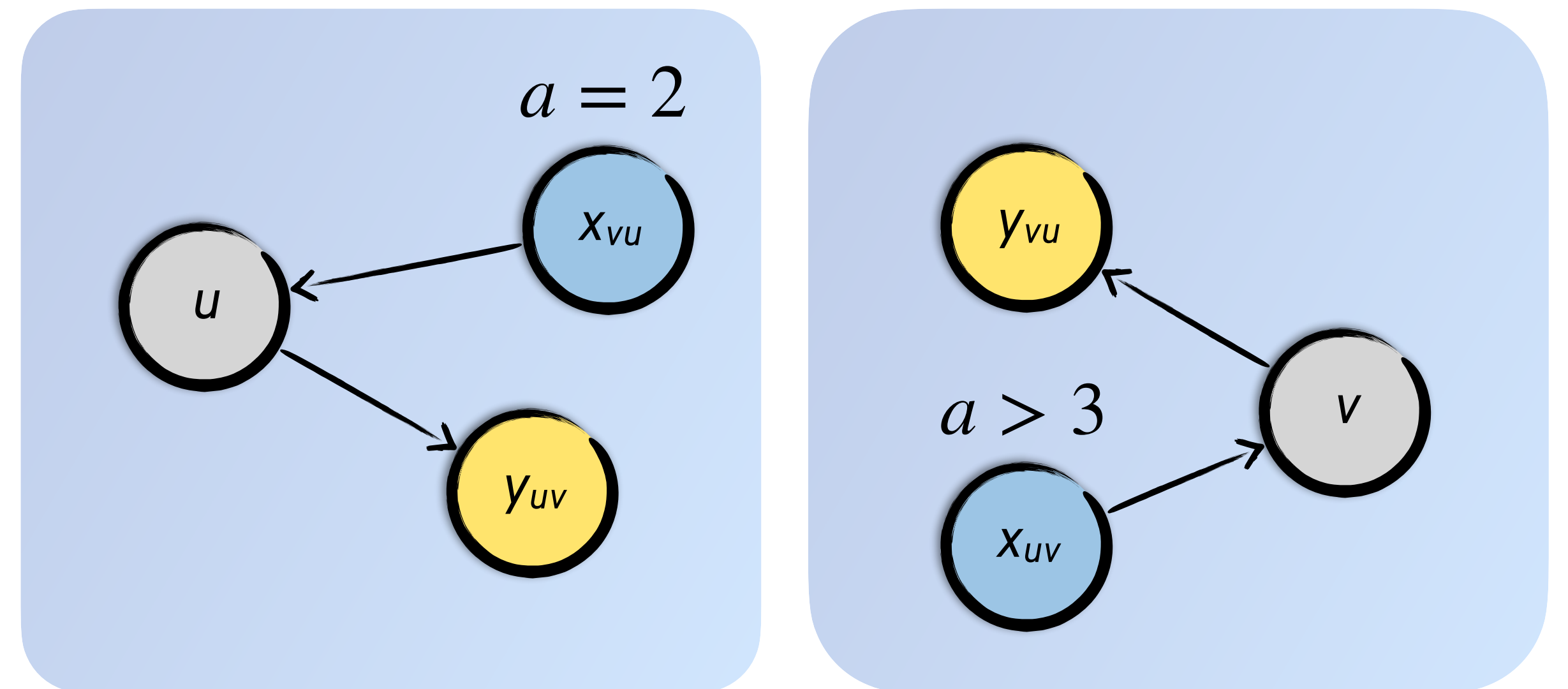


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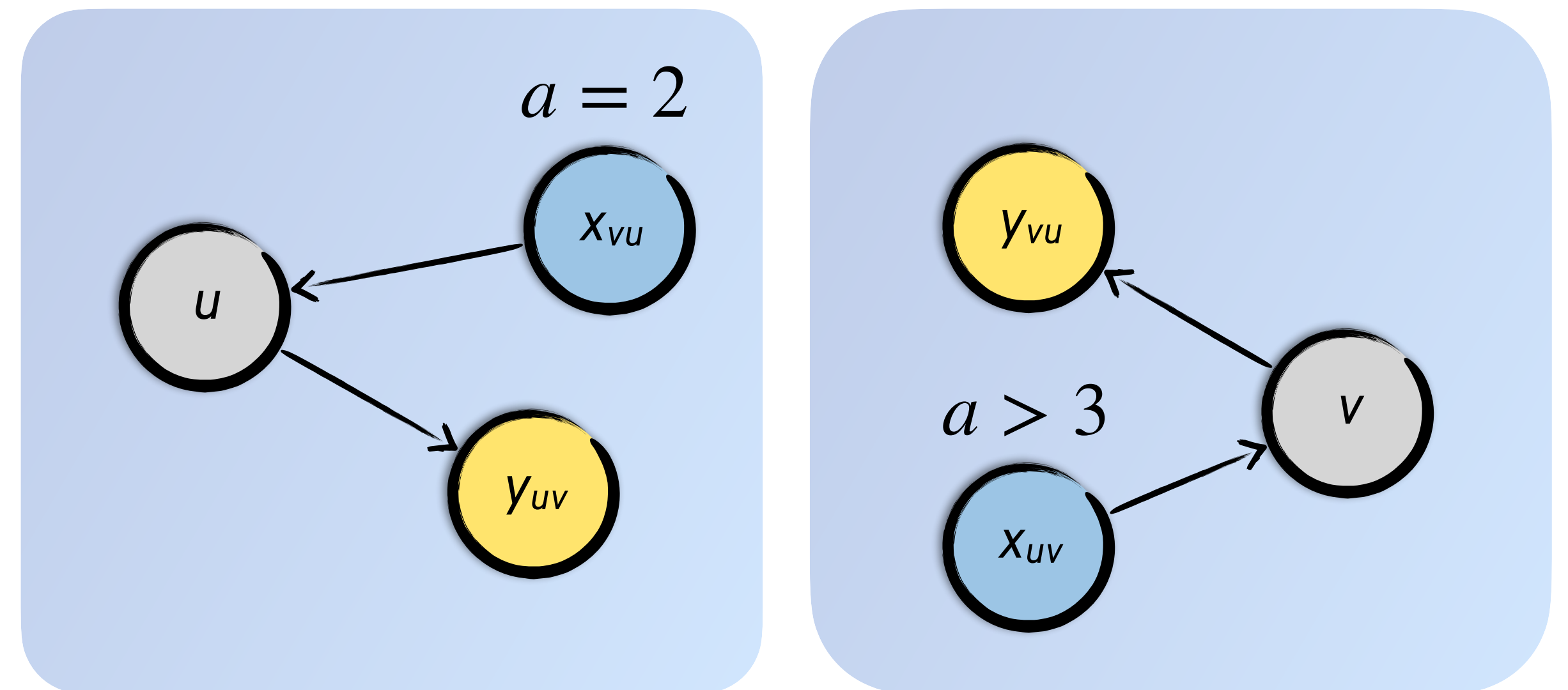
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 $(u, v, a > 3)$



Interfaces

- To verify an SRP compositionally, let's define an *interface*!
- An *interface* is a set of cut-set edges, each with an associated hypothesis
- $\mathcal{I} = \left\{ \left(u_j, v_j, H_{u_j v_j} \right) \right\}$
- Hypotheses needs to be *weak enough* to capture all behaviours of the original SRP, while *strong enough* to still allow us to prove our property

$(v, u, a = 2)$
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The Kirigami Algorithm

An algorithm for checking *interfaces*

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Input: a closed SRP R , a property P and an interface \mathcal{I}

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assuming A about M 's inputs,
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4. Return “TRUE” if the VCs hold, or “FALSE” with counterexample(s)

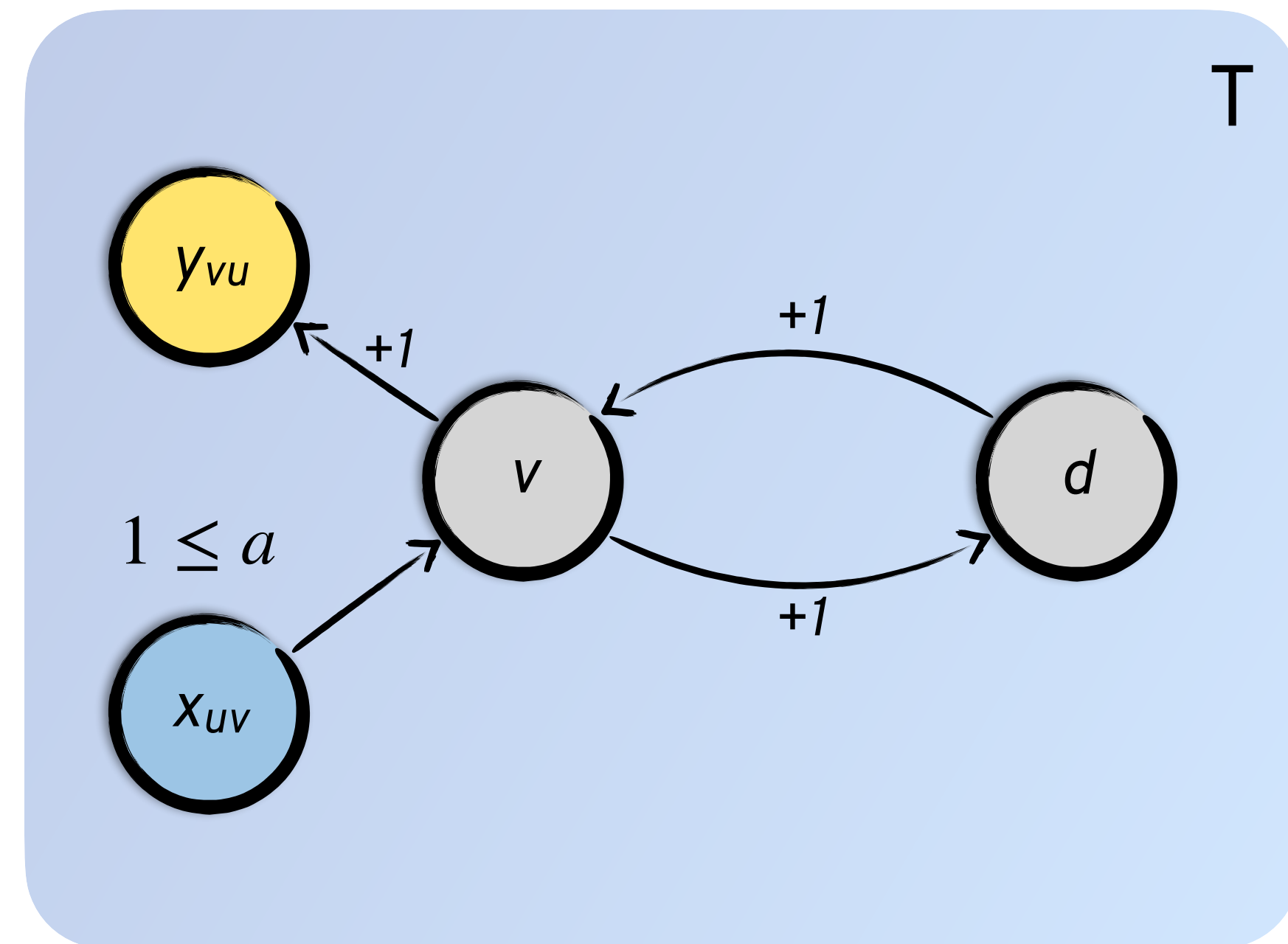
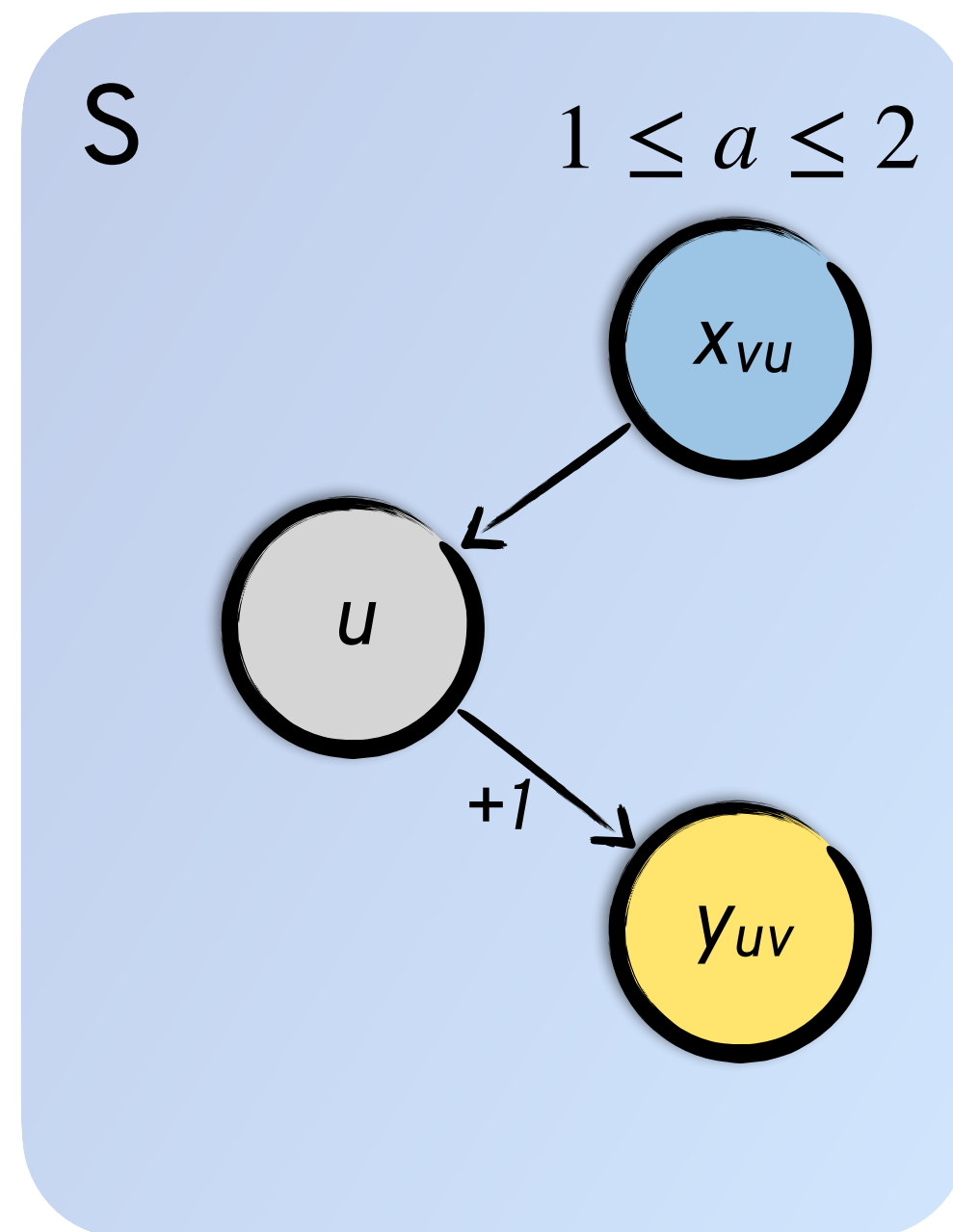
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[Beckett et al., SIGCOMM 2017]

Checking an Interface

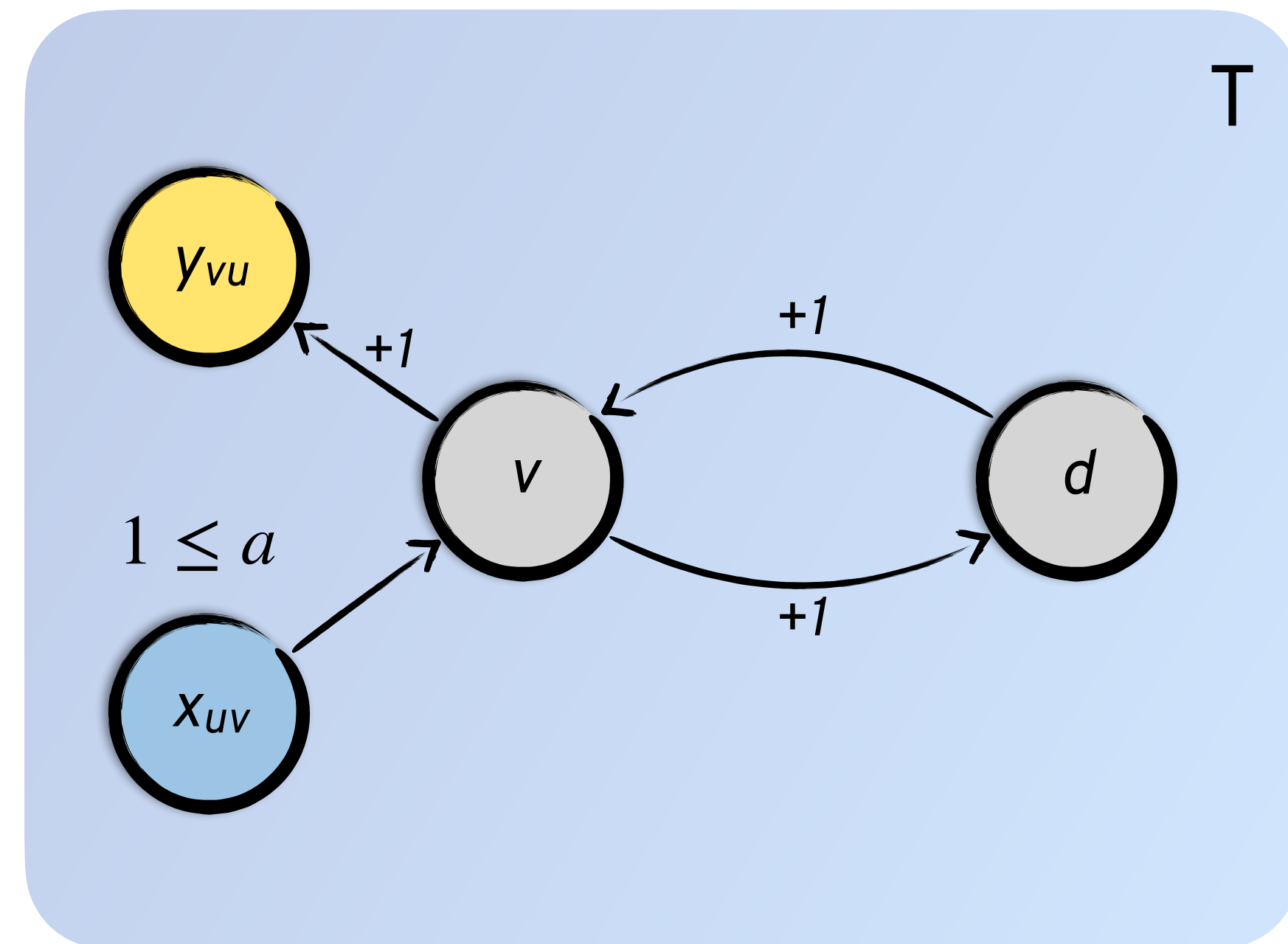
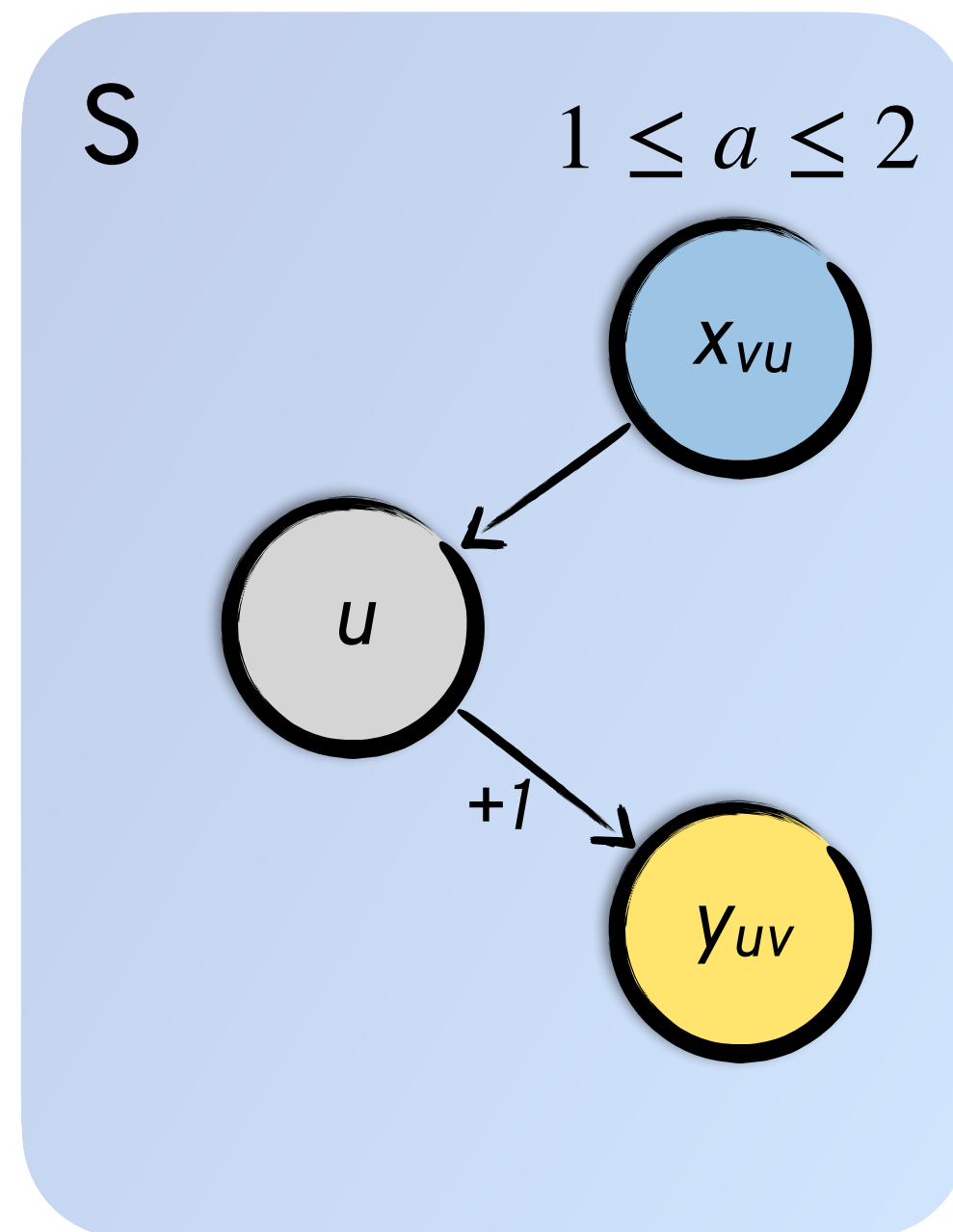
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Checking an Interface

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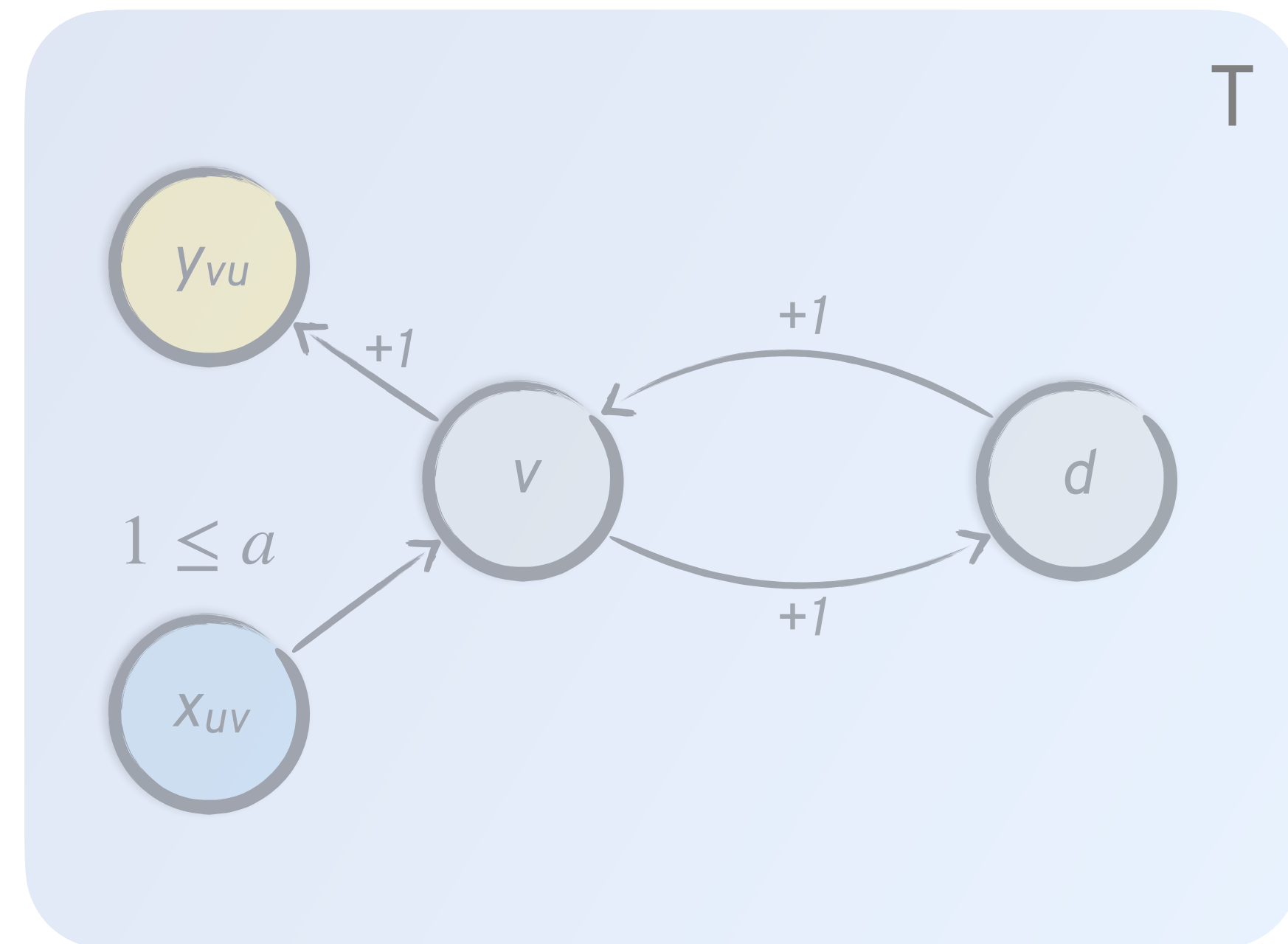
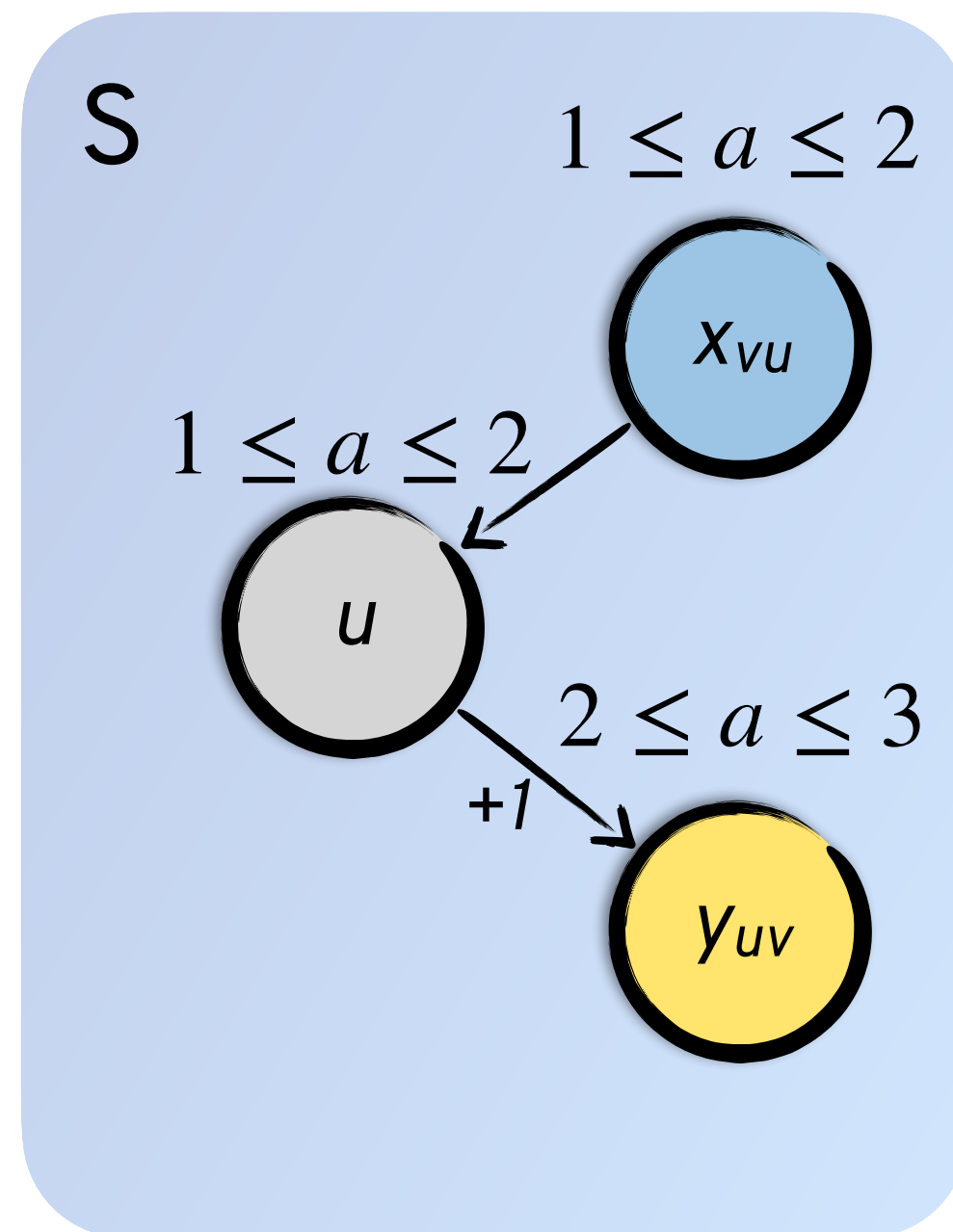
$(v, u, 1 \leq a \leq 2)$
 $(u, v, 1 \leq a)$



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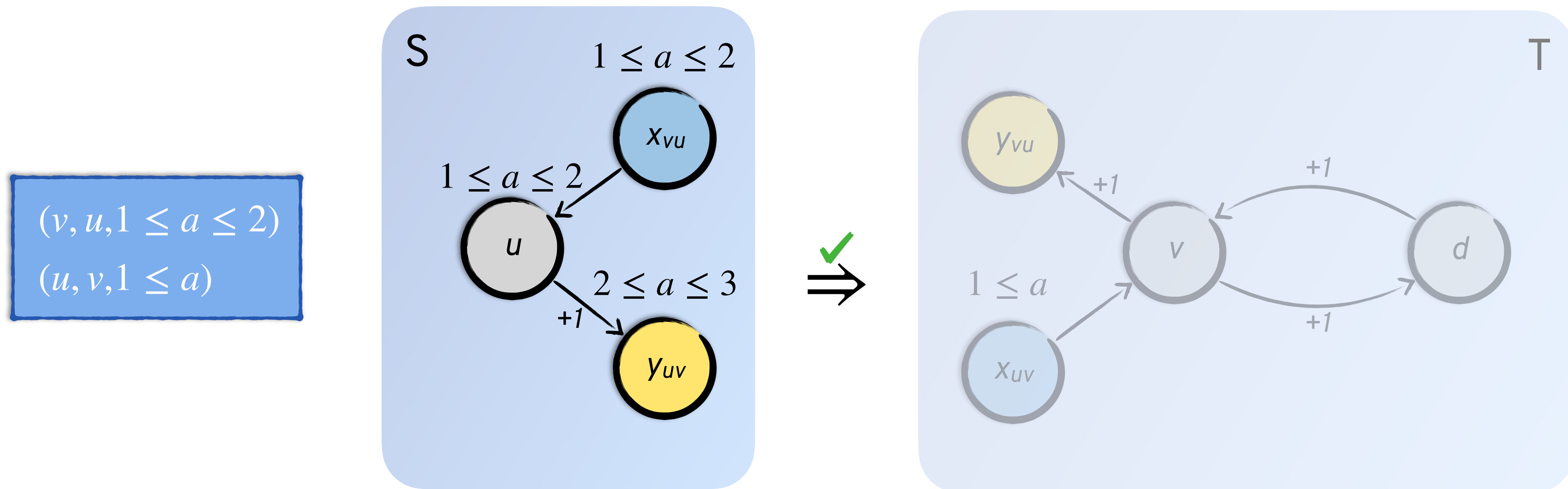
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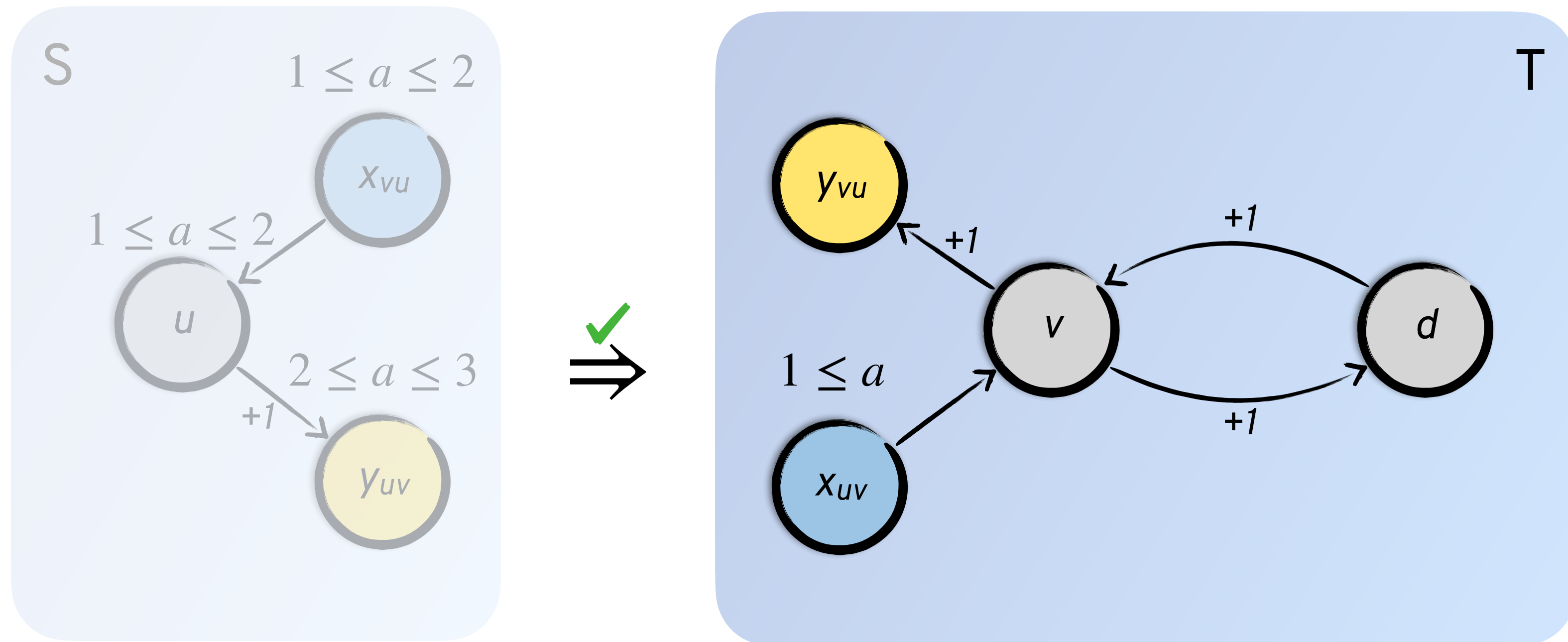
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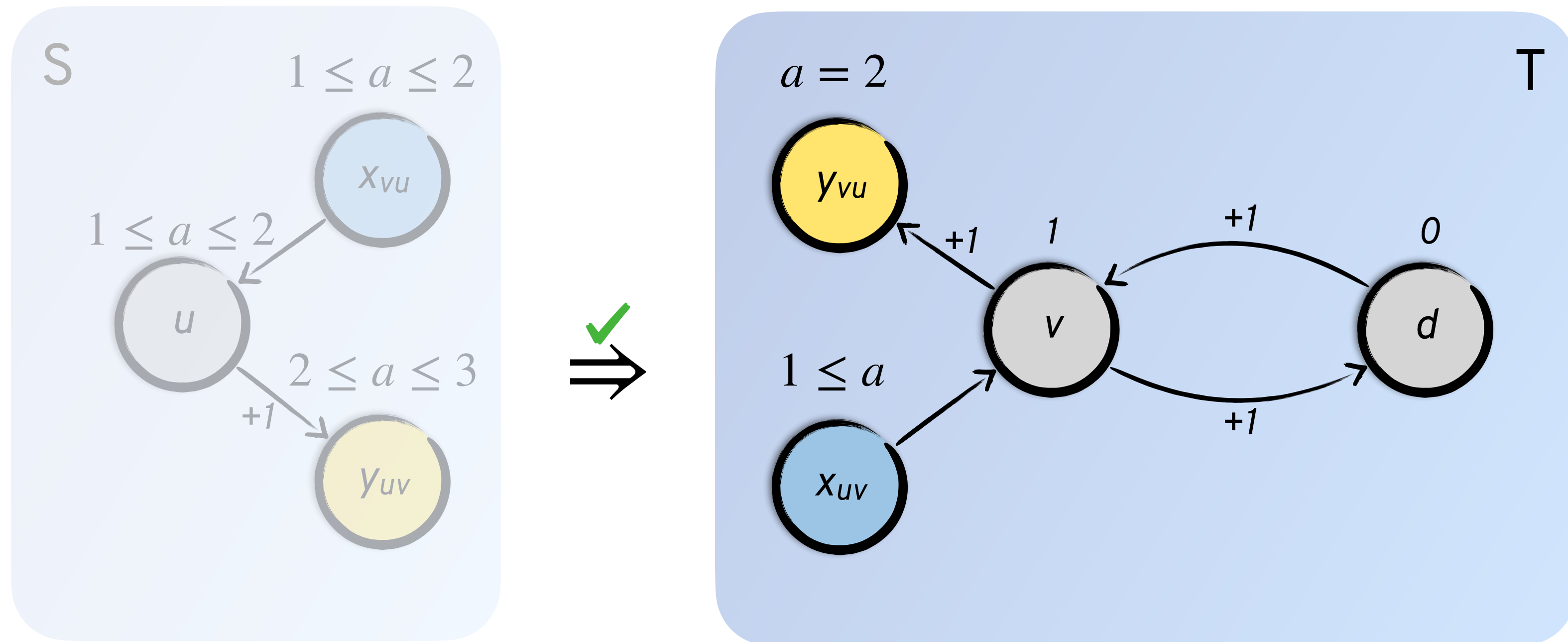
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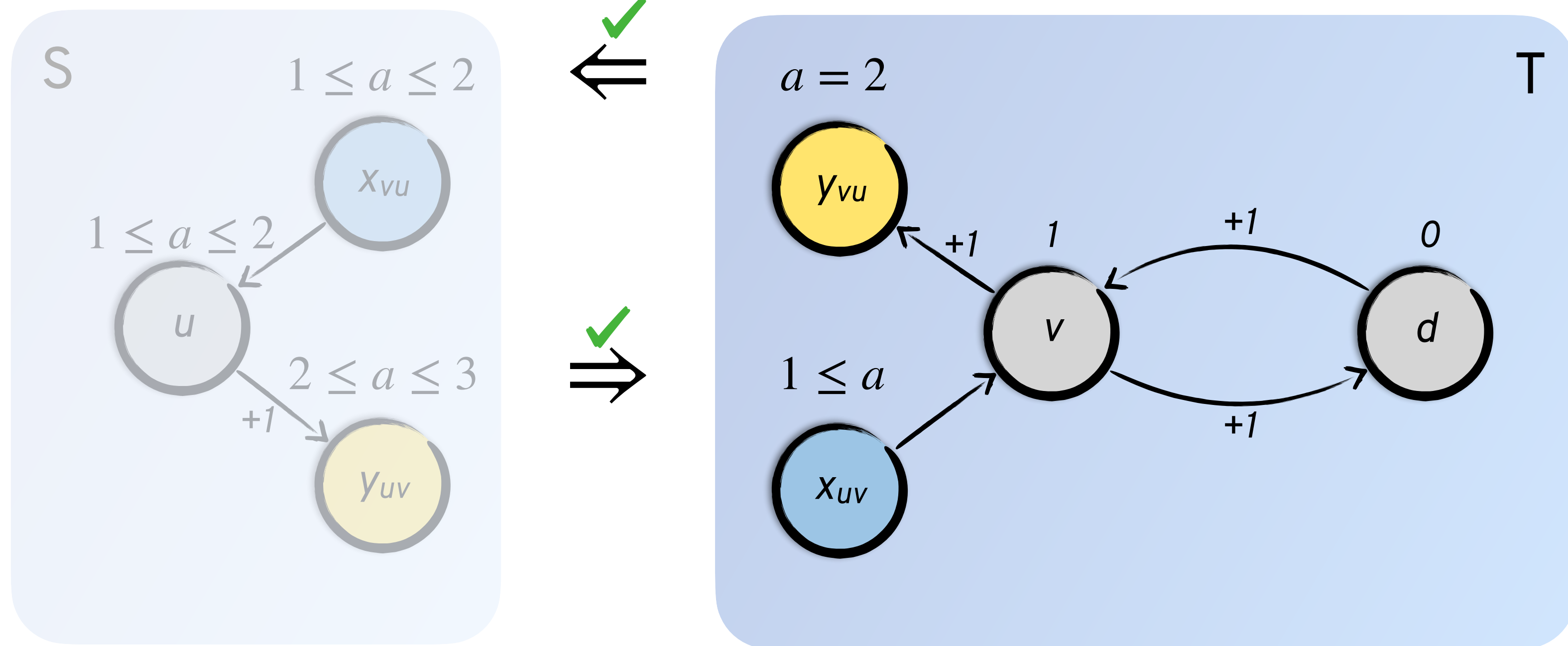
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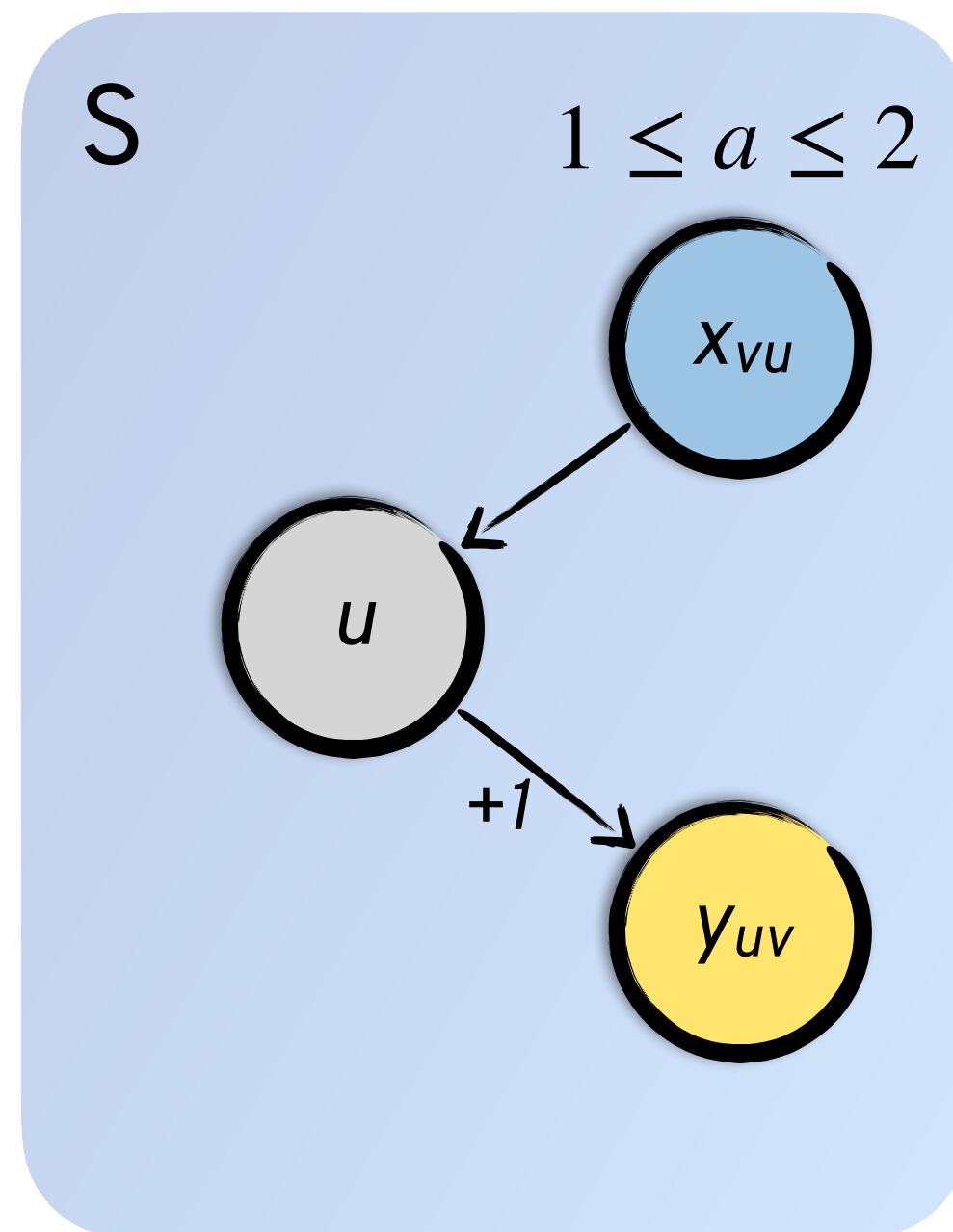
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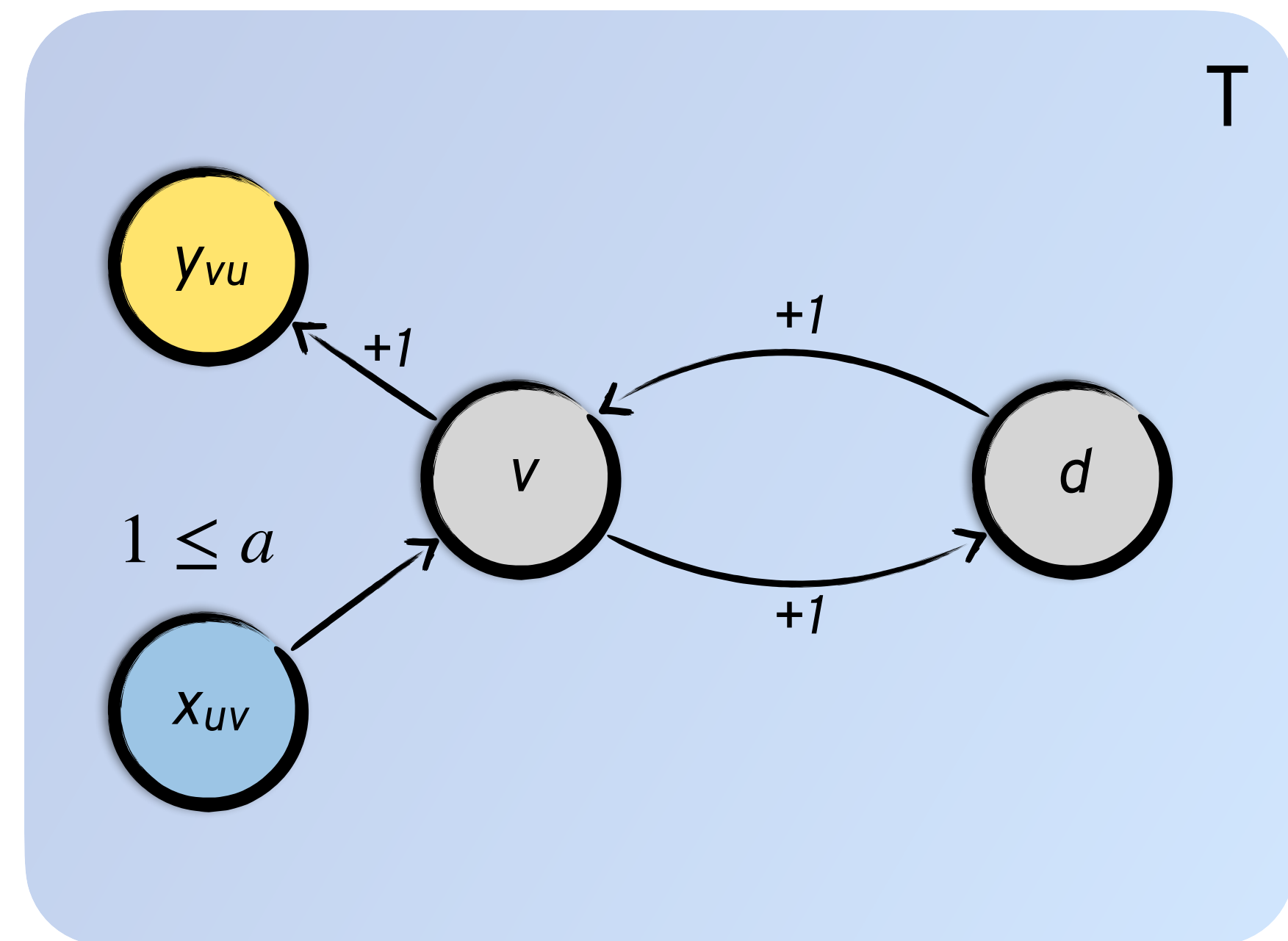
Checking an Interface

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$P_S = \mathcal{L}(u) < 10$

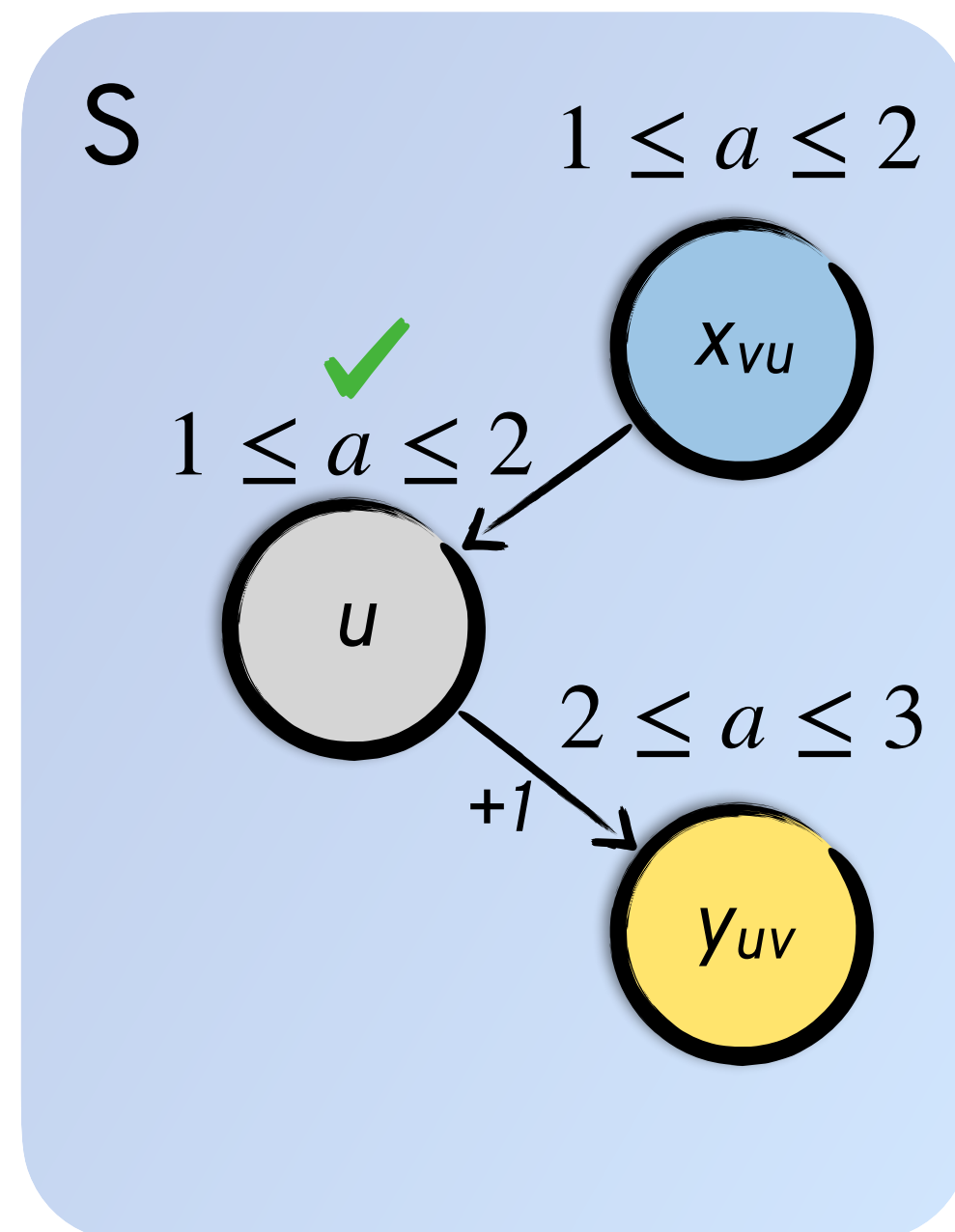


$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$

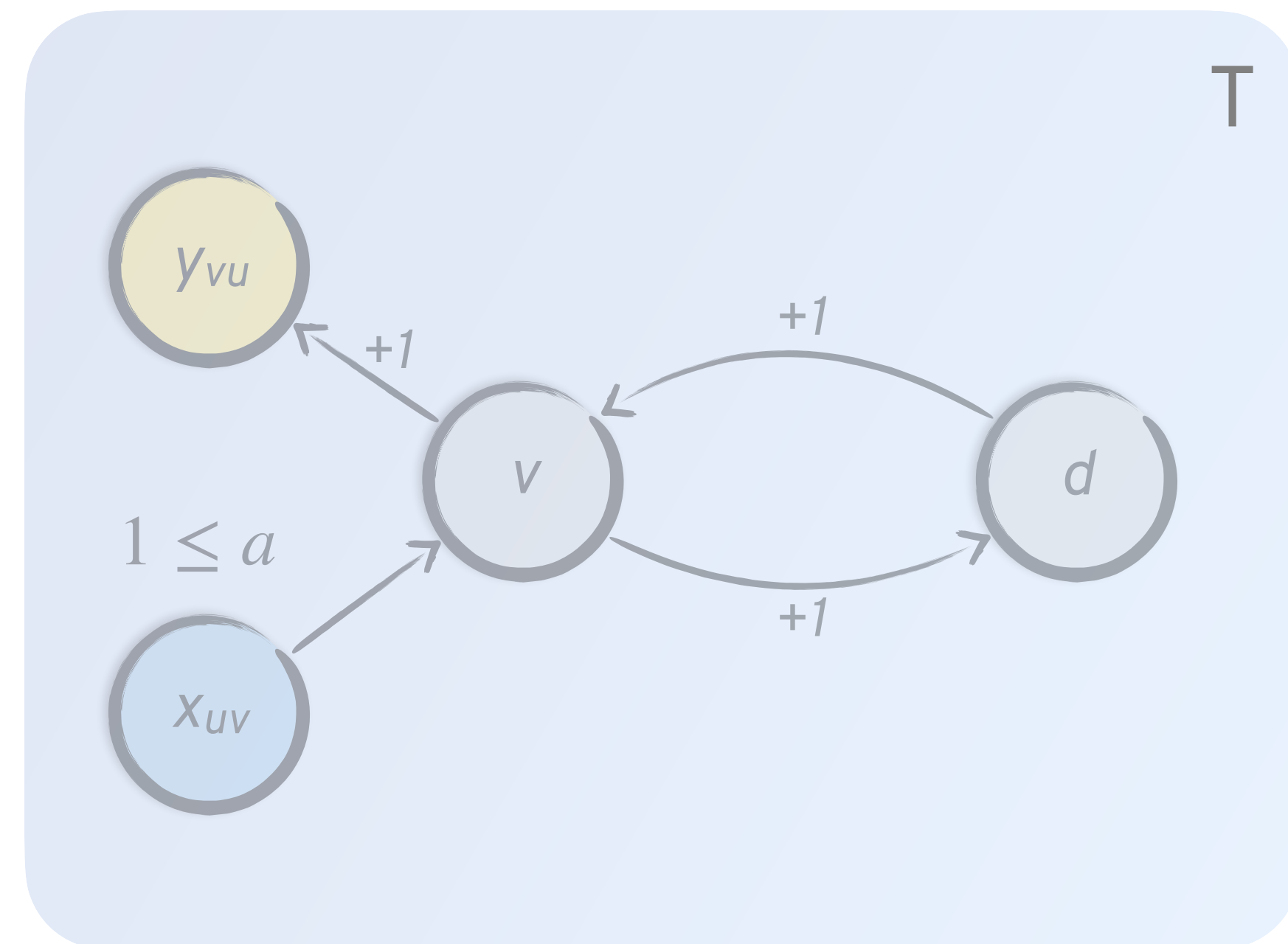
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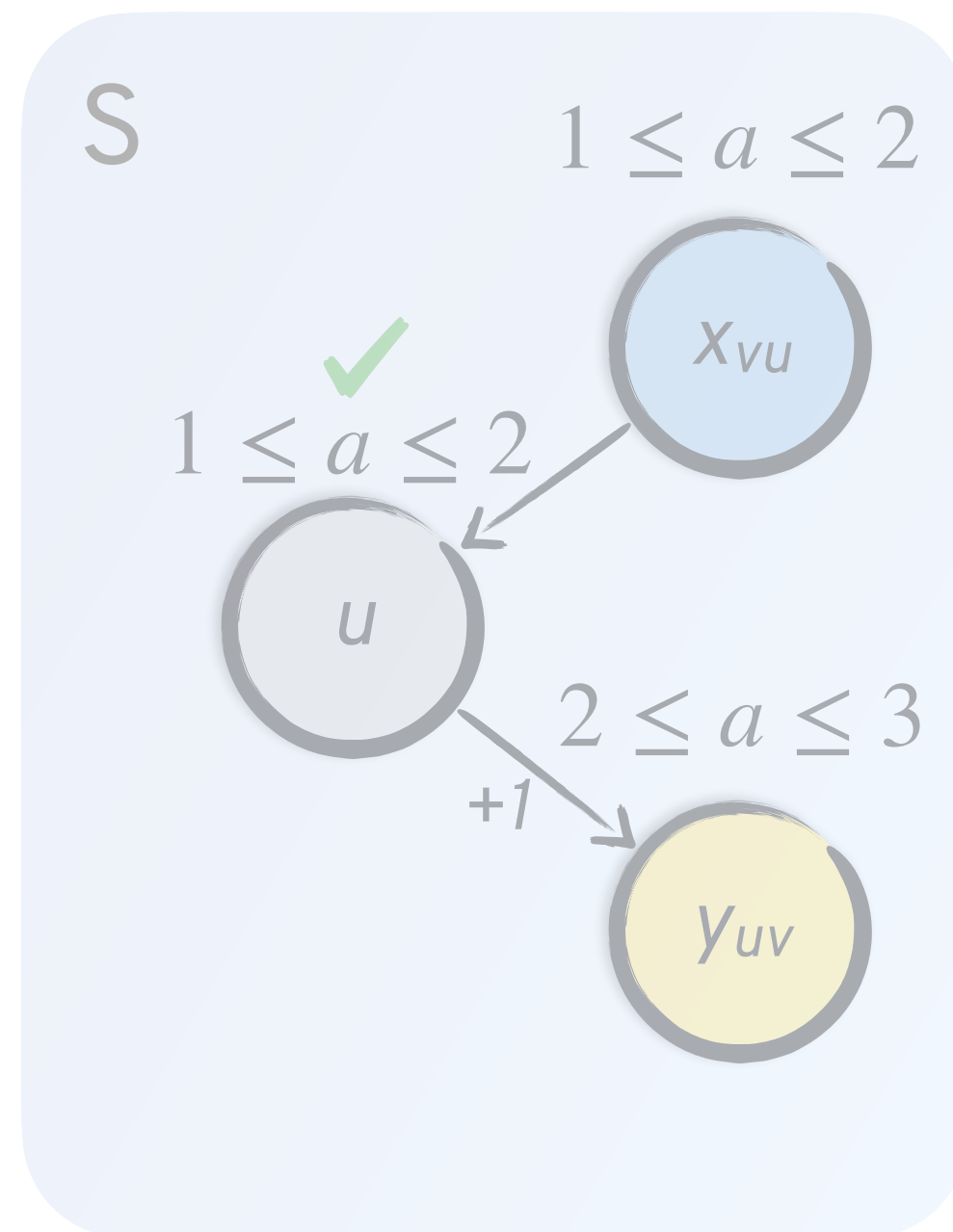


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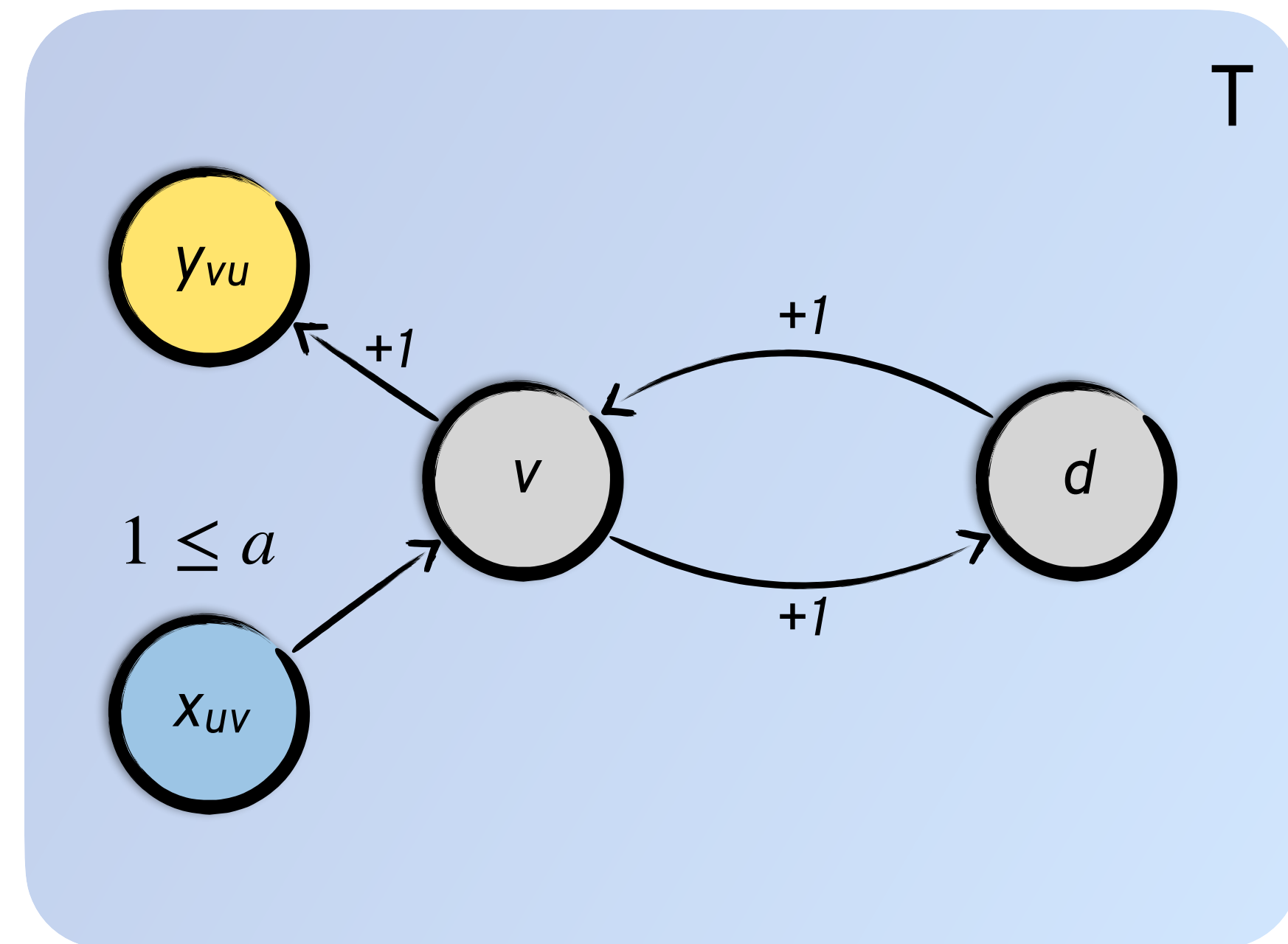
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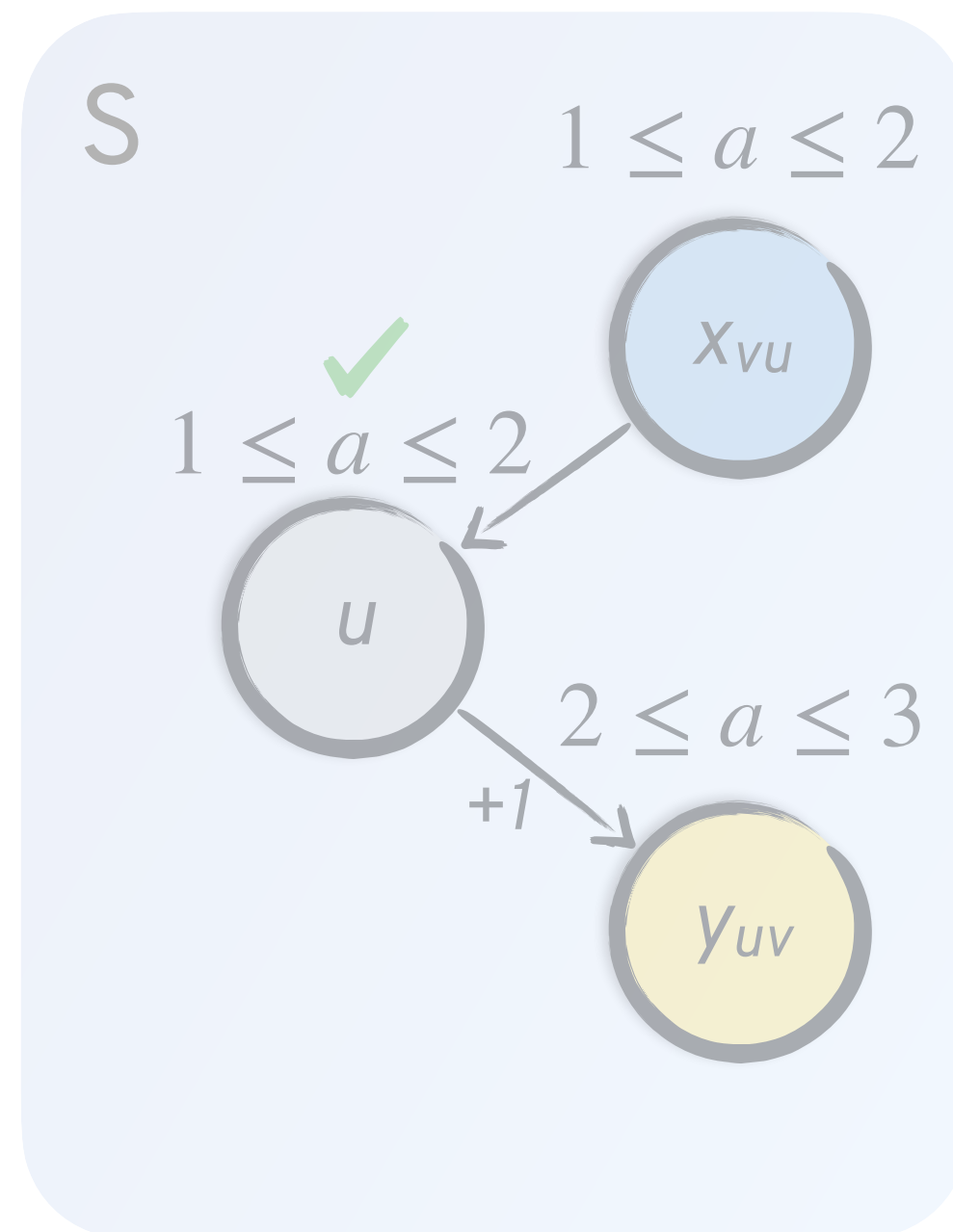


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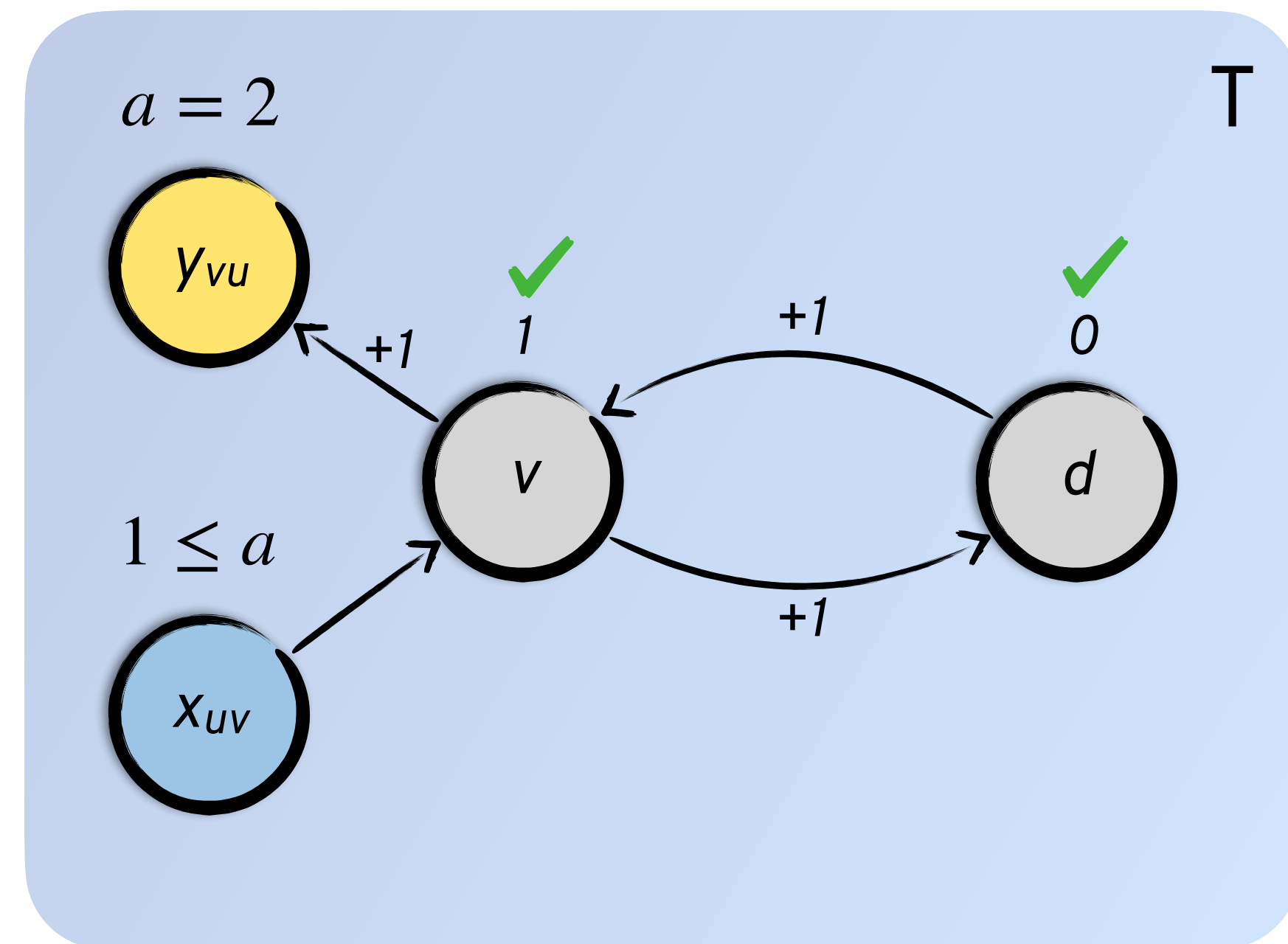
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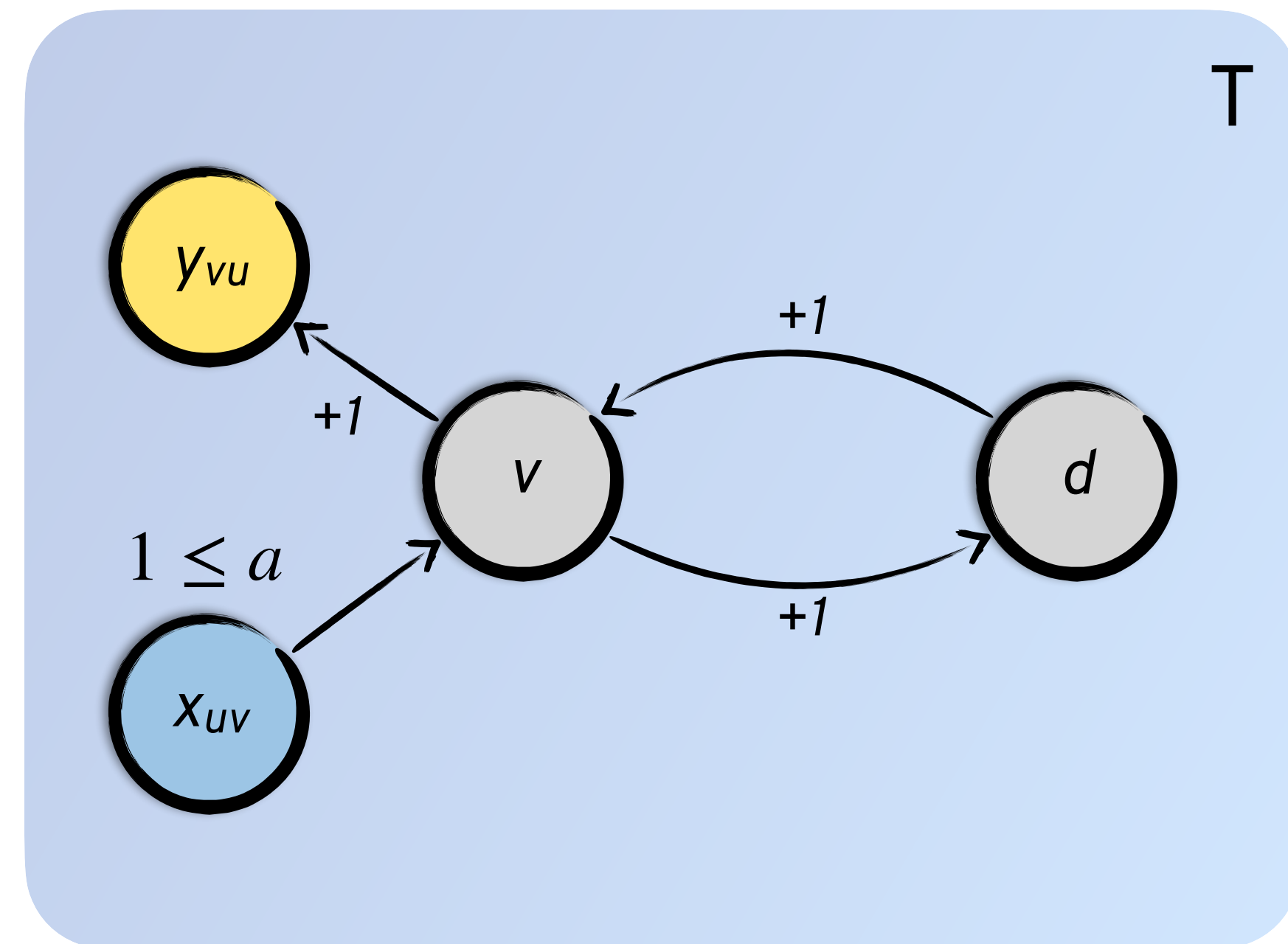
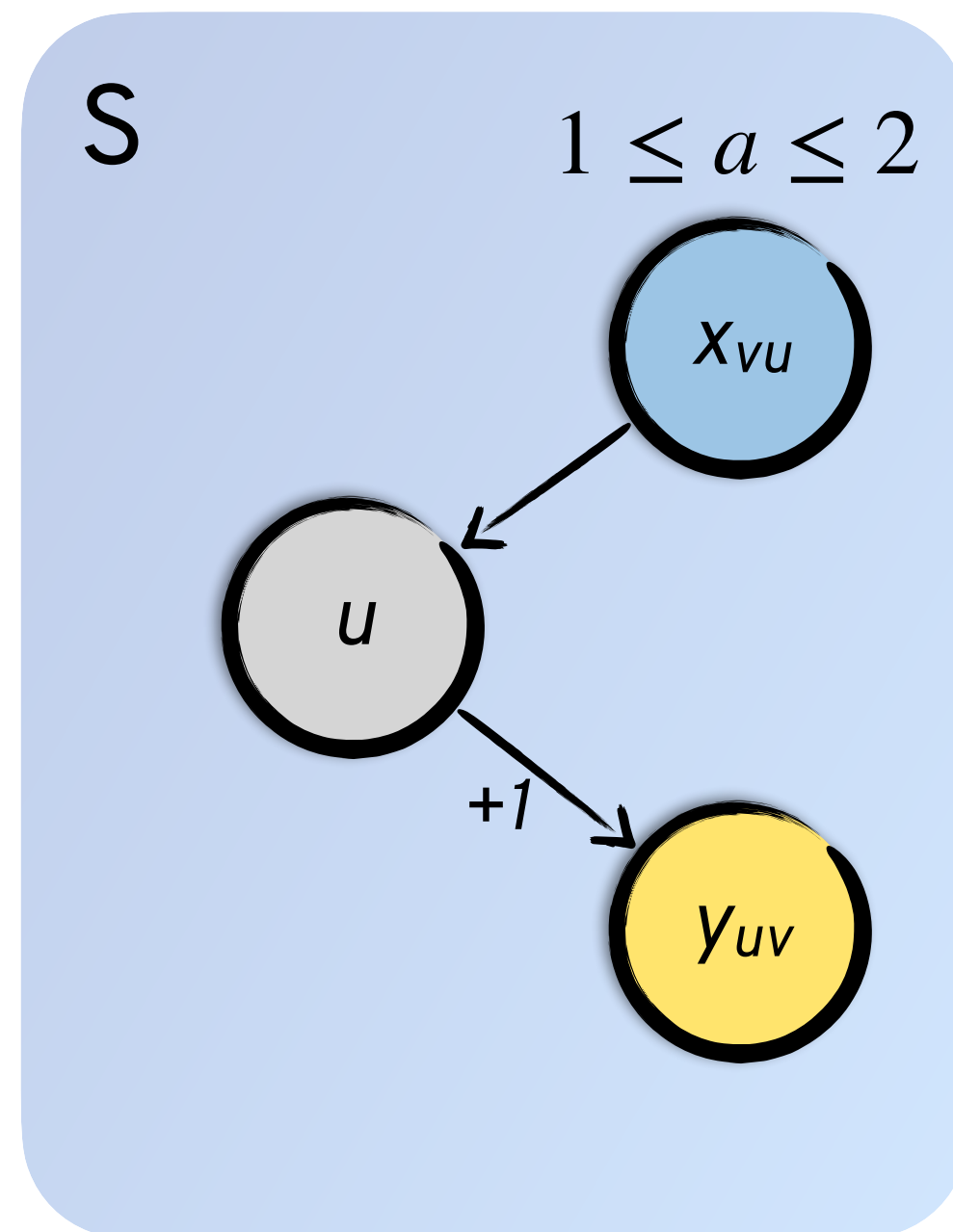


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Checking an Interface

C: Initial: $\langle \text{true} \rangle \mathcal{M}_S \langle G_S \rangle \wedge G_S \Rightarrow \mathbf{H}_T, \langle \text{true} \rangle \mathcal{M}_T \langle G_T \rangle \wedge G_T \Rightarrow \mathbf{H}_S$

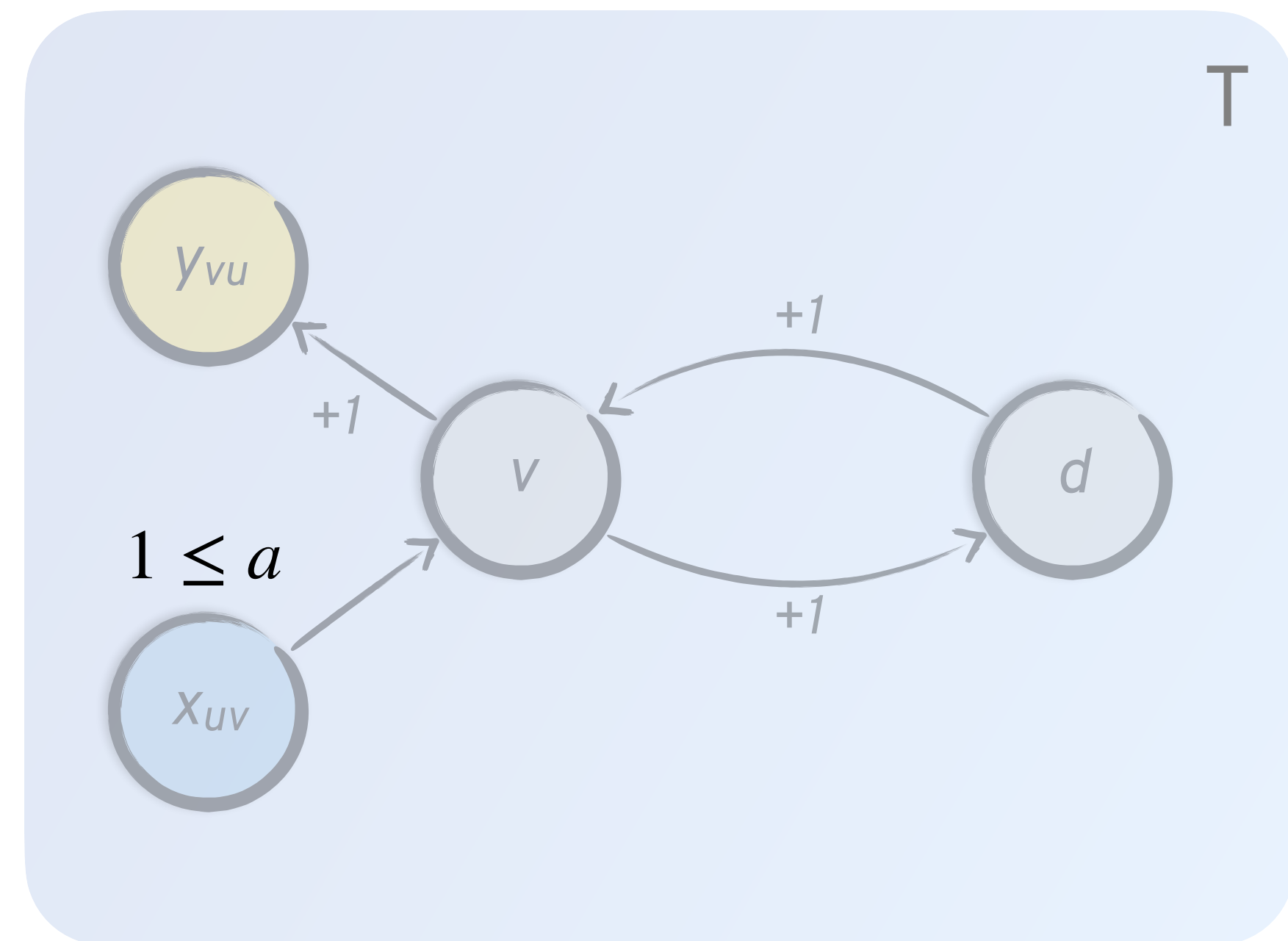
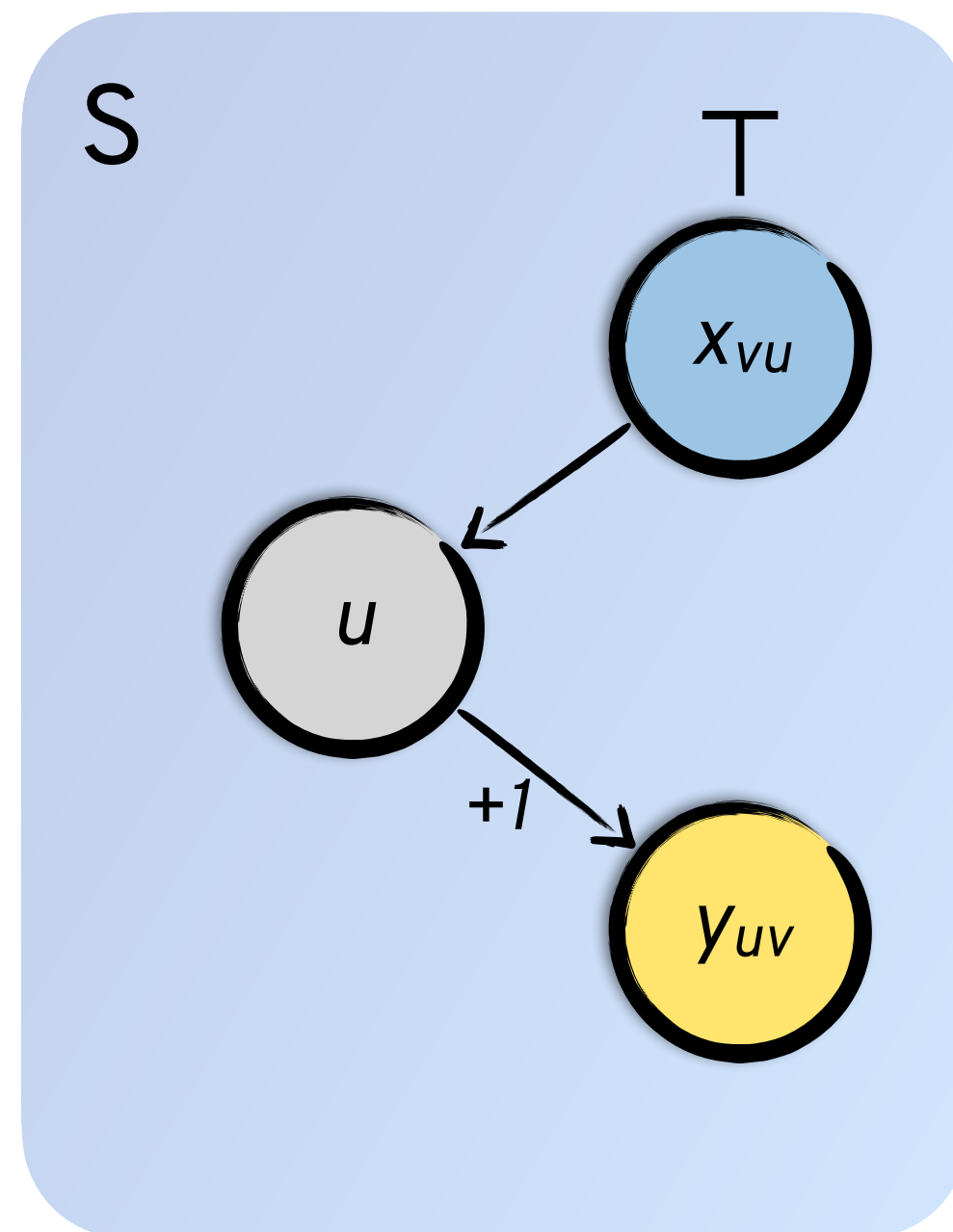
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C: Initial: $\langle \text{true} \rangle \mathcal{M}_S \langle G_S \rangle \wedge G_S \Rightarrow \mathbf{H}_T, \langle \text{true} \rangle \mathcal{M}_T \langle G_T \rangle \wedge G_T \Rightarrow \mathbf{H}_S$

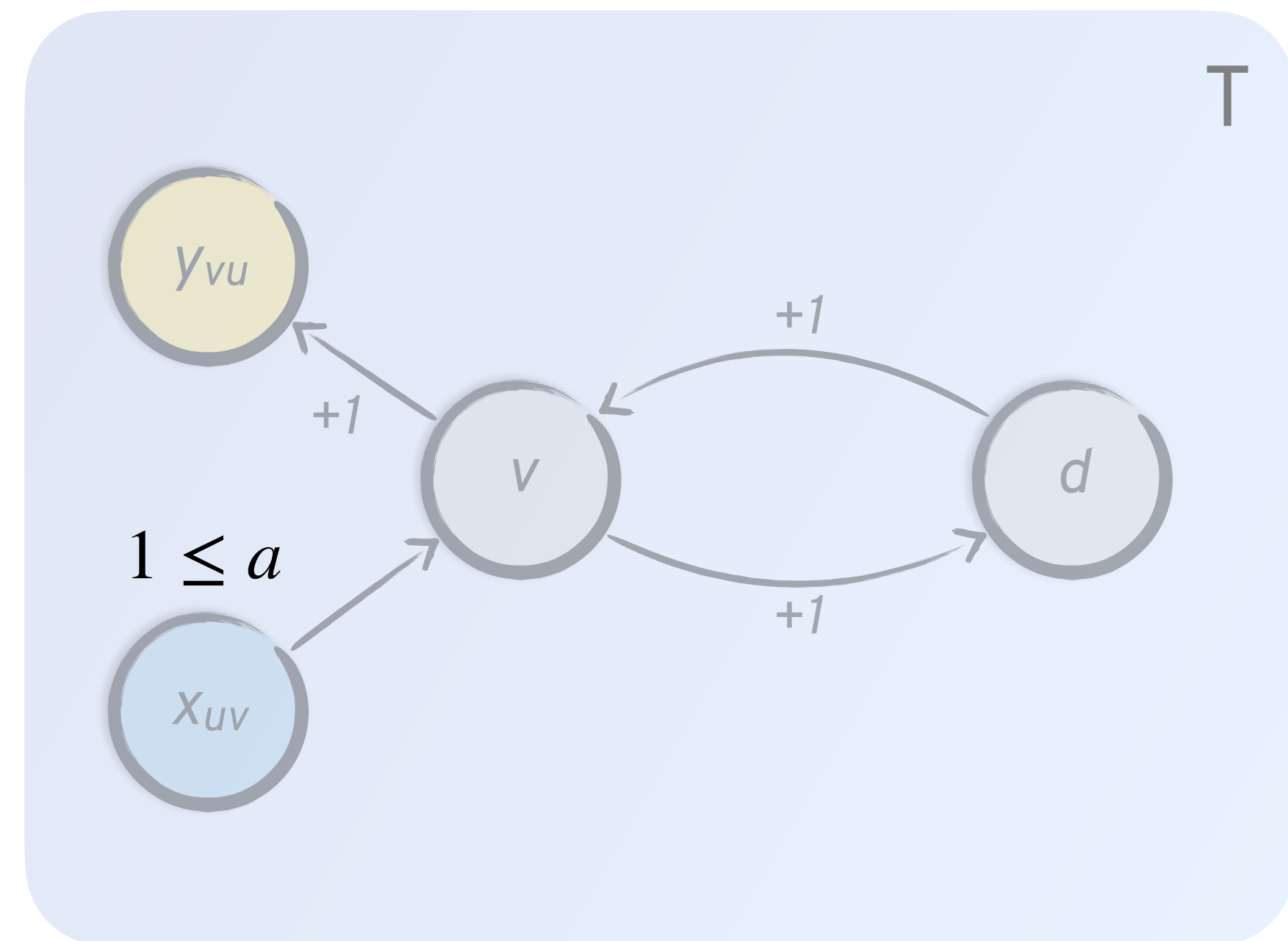
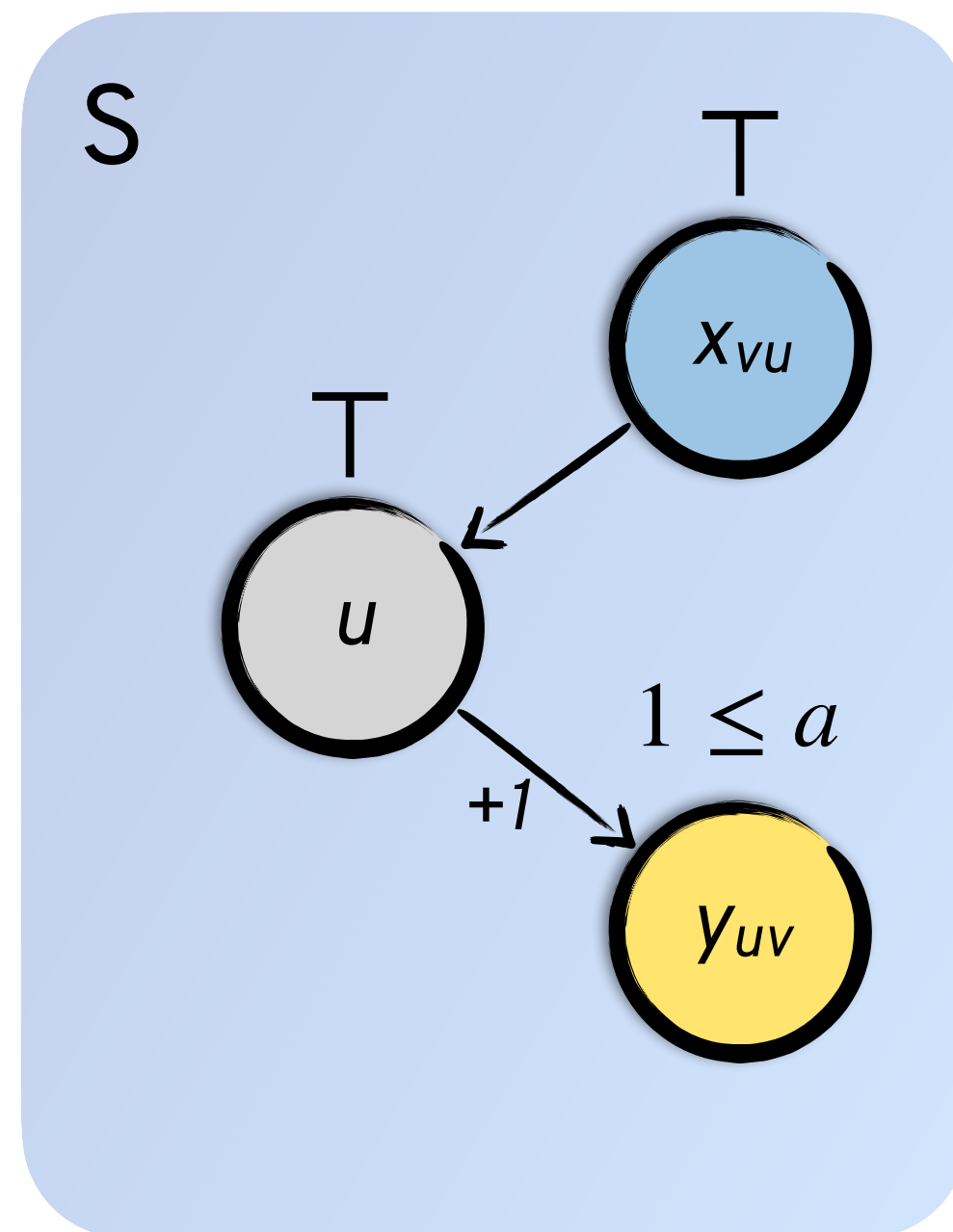
$(v, u, 1 \leq a \leq 2)$
 $(u, v, 1 \leq a)$



Checking an Interface

C: Initial: $\langle \text{true} \rangle \mathcal{M}_S \langle G_S \rangle \wedge G_S \Rightarrow \mathbf{H}_T, \langle \text{true} \rangle \mathcal{M}_T \langle G_T \rangle \wedge G_T \Rightarrow \mathbf{H}_S$

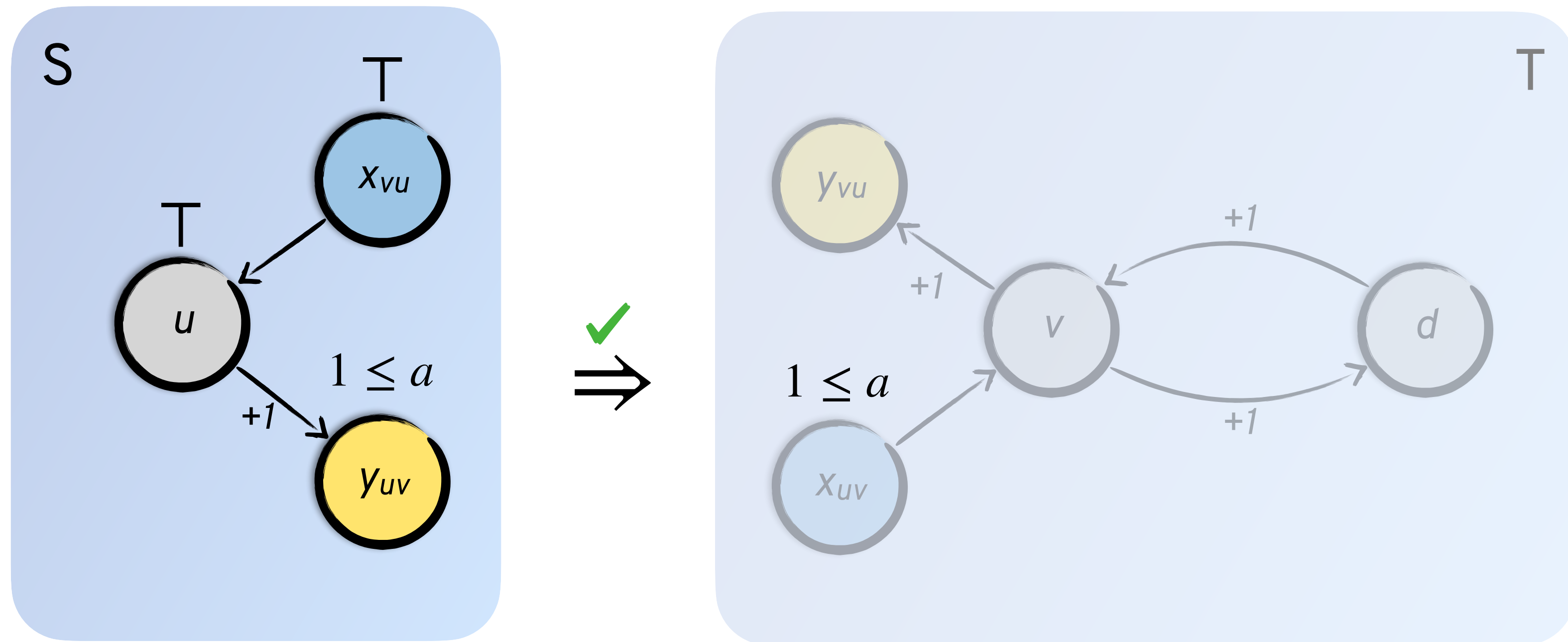
$(v, u, 1 \leq a \leq 2)$
 $(u, v, 1 \leq a)$



Checking an Interface

C: Initial: $\langle \text{true} \rangle \mathcal{M}_S \langle G_S \rangle \wedge G_S \Rightarrow \mathbf{H}_T, \langle \text{true} \rangle \mathcal{M}_T \langle G_T \rangle \wedge G_T \Rightarrow \mathbf{H}_S$

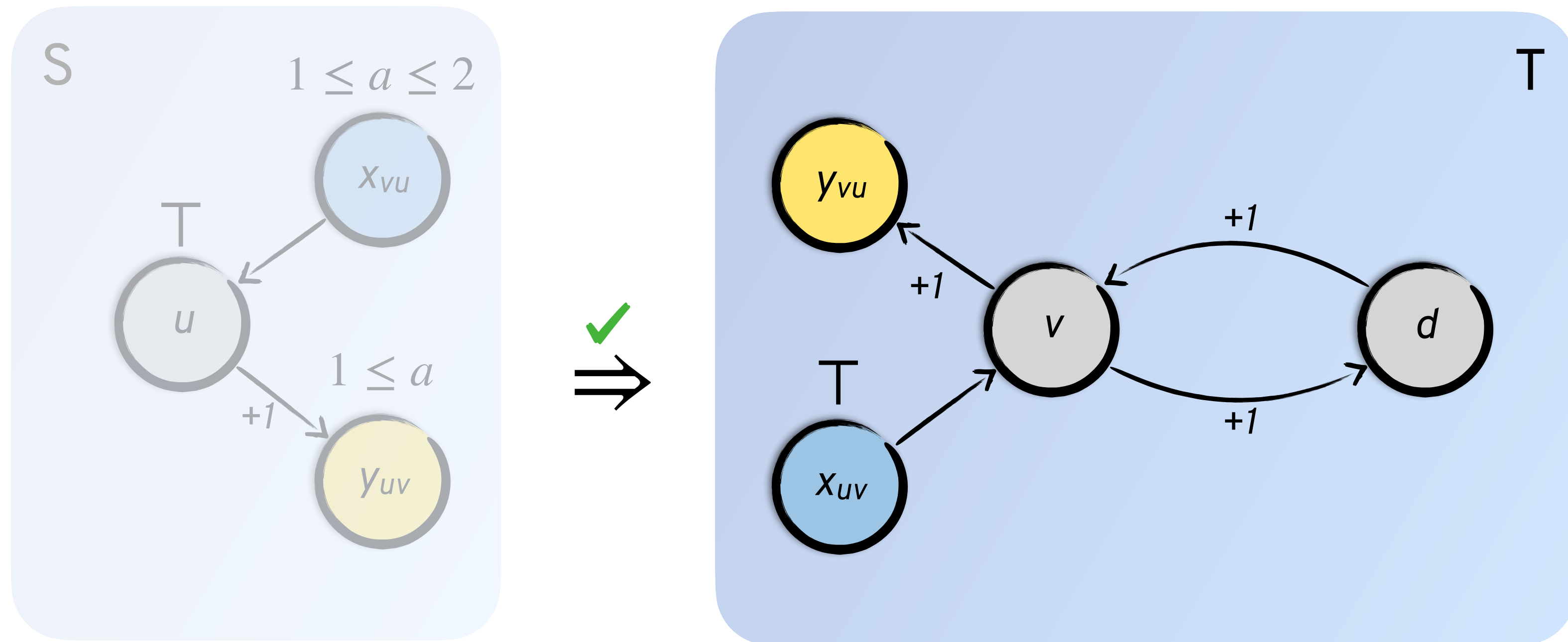
$(v, u, 1 \leq a \leq 2)$
 $(u, v, 1 \leq a)$



Checking an Interface

C: Initial: $\langle \text{true} \rangle \mathcal{M}_S \langle G_S \rangle \wedge G_S \Rightarrow \mathbf{H}_T, \langle \text{true} \rangle \mathcal{M}_T \langle G_T \rangle \wedge G_T \Rightarrow \mathbf{H}_S$

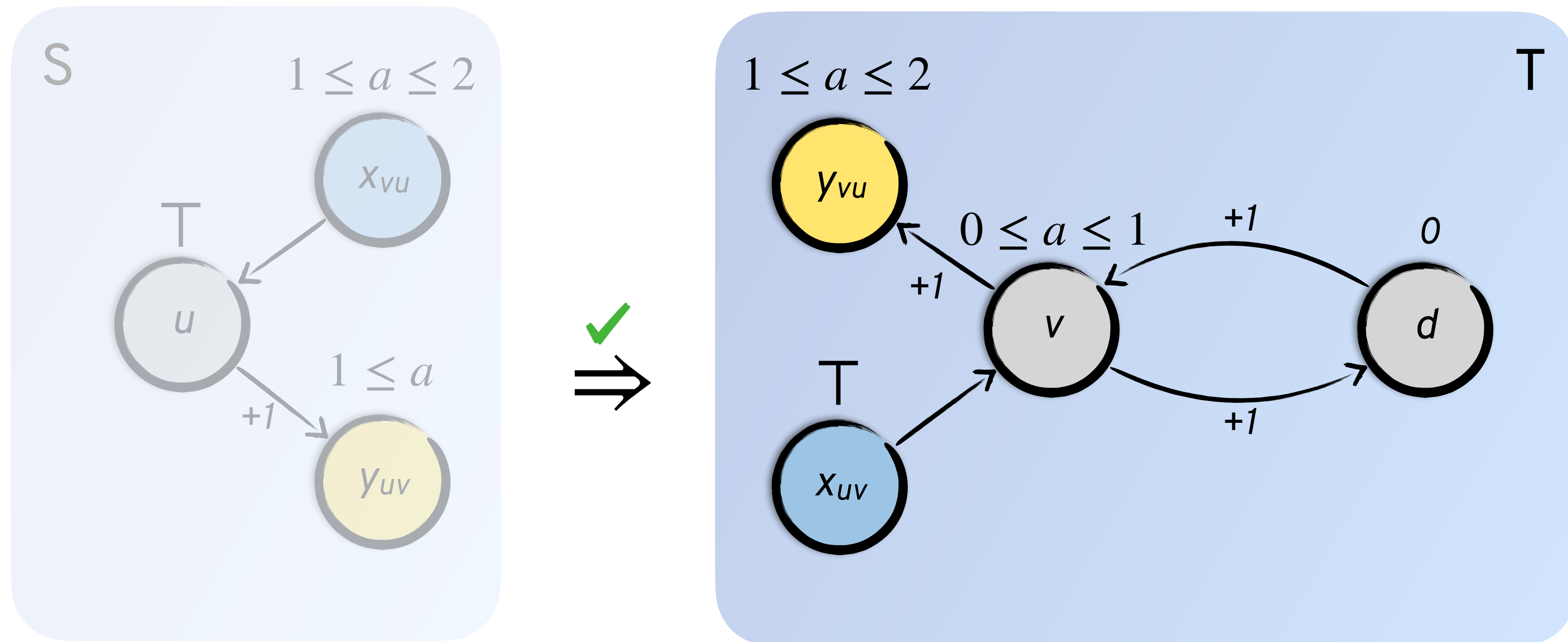
$(v, u, 1 \leq a \leq 2)$
 $(u, v, 1 \leq a)$



Checking an Interface

C: Initial: $\langle \text{true} \rangle \mathcal{M}_S \langle G_S \rangle \wedge G_S \Rightarrow \mathbf{H}_T, \langle \text{true} \rangle \mathcal{M}_T \langle G_T \rangle \wedge G_T \Rightarrow \mathbf{H}_S$

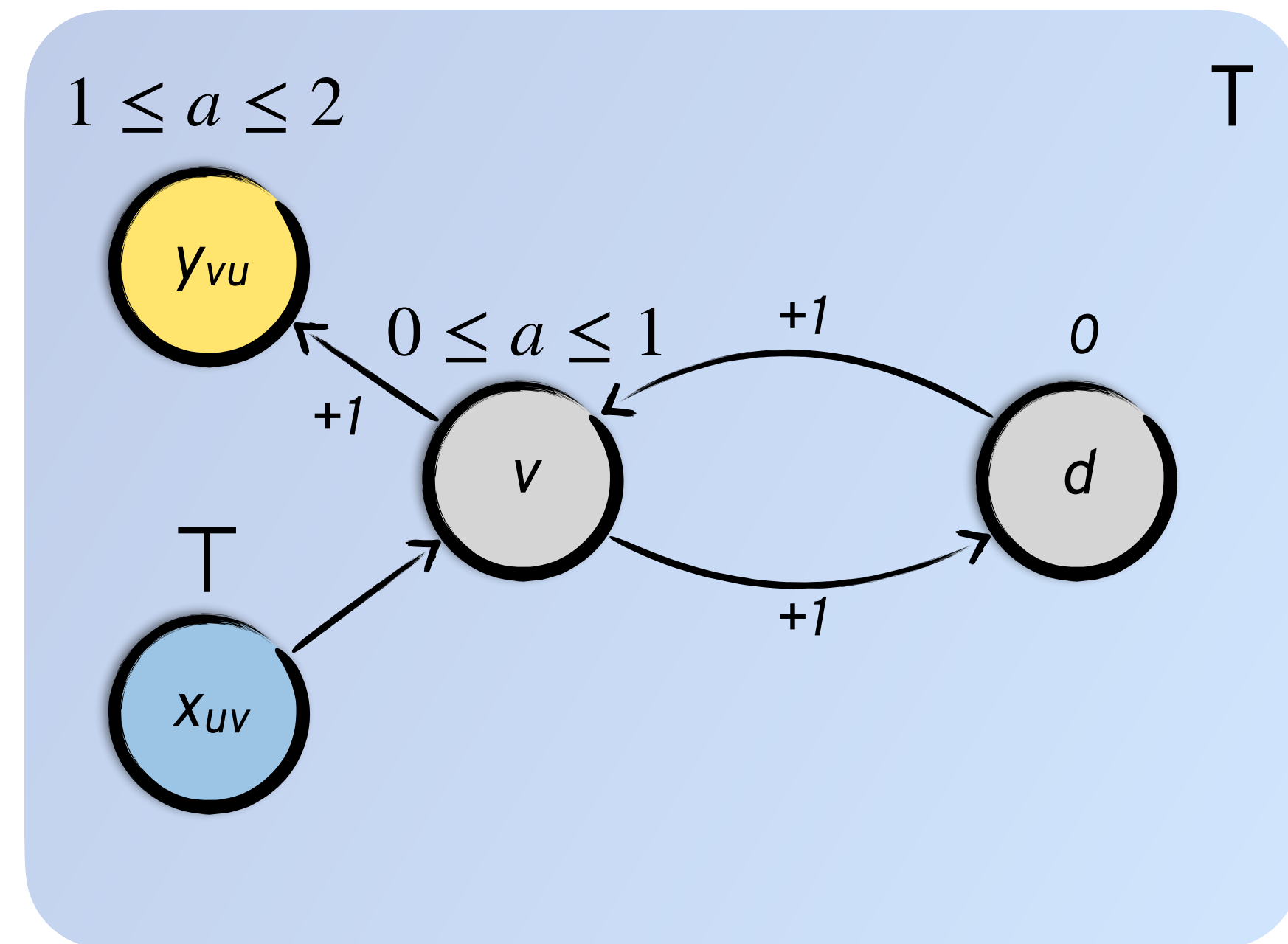
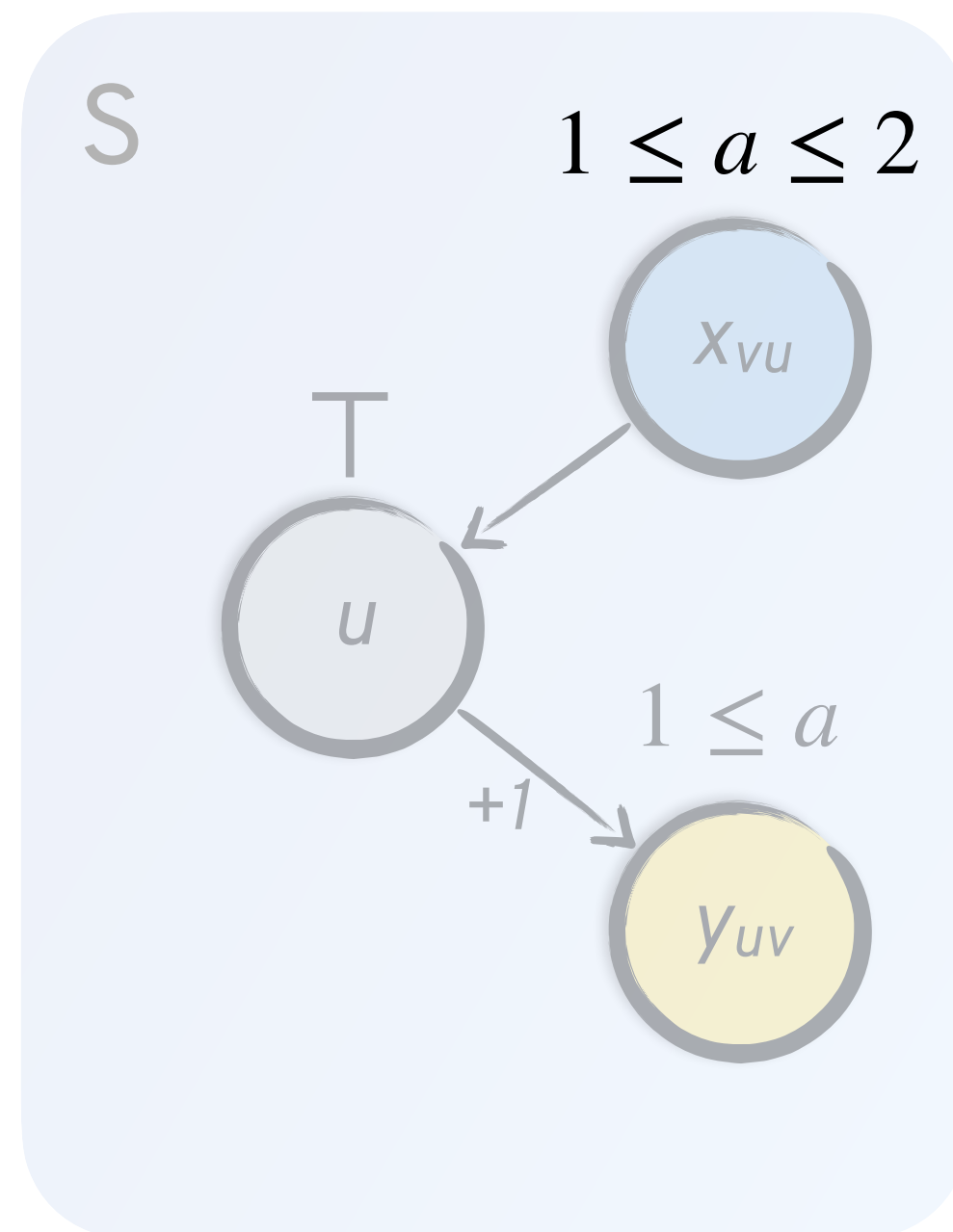
$(v, u, 1 \leq a \leq 2)$
 $(u, v, 1 \leq a)$



Checking an Interface

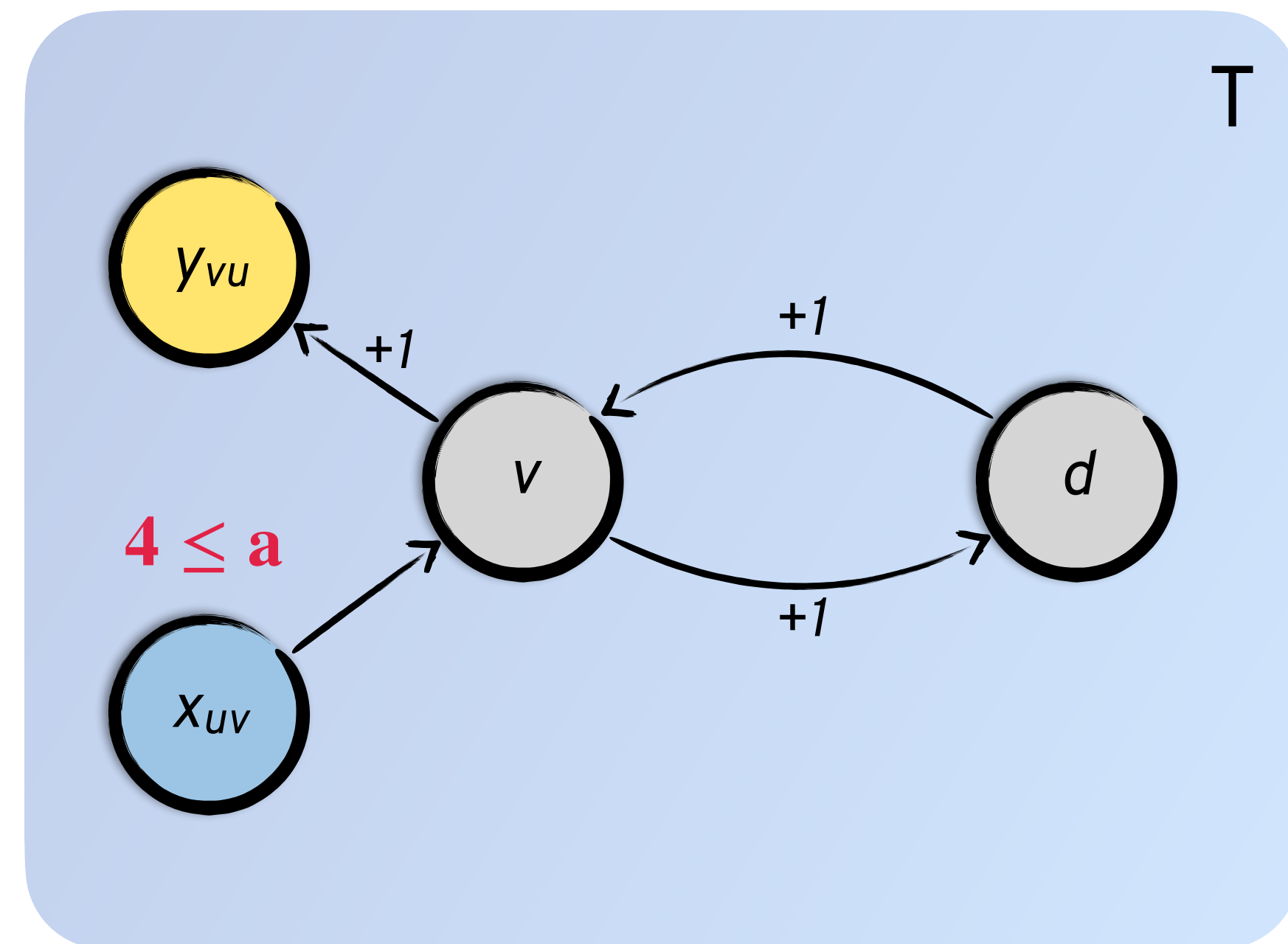
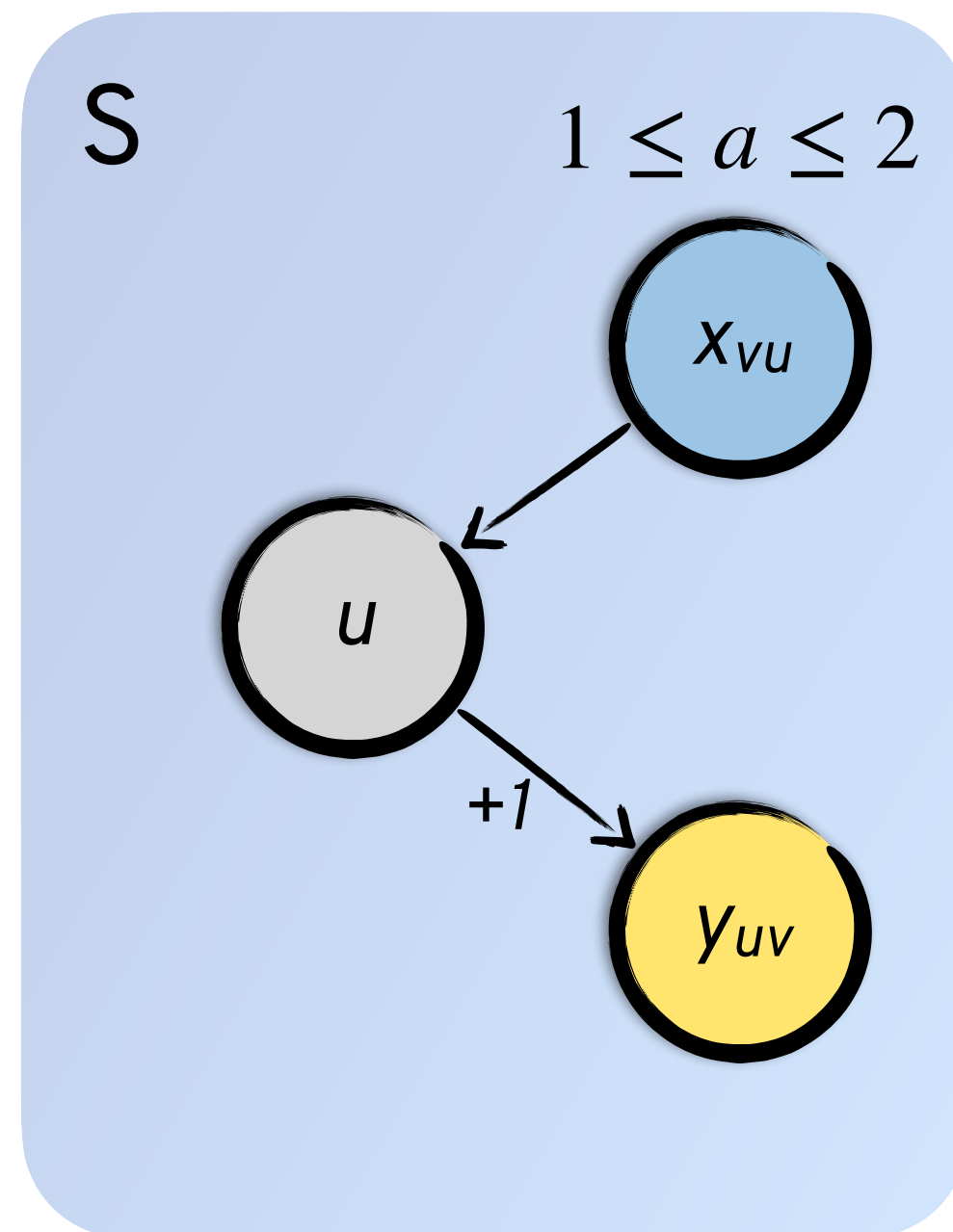
C: Initial: $\langle \text{true} \rangle \mathcal{M}_S \langle G_S \rangle \wedge G_S \Rightarrow \mathbf{H}_T, \langle \text{true} \rangle \mathcal{M}_T \langle G_T \rangle \wedge G_T \Rightarrow \mathbf{H}_S$

$(v, u, 1 \leq a \leq 2)$
 $(u, v, 1 \leq a)$



Checking an Interface

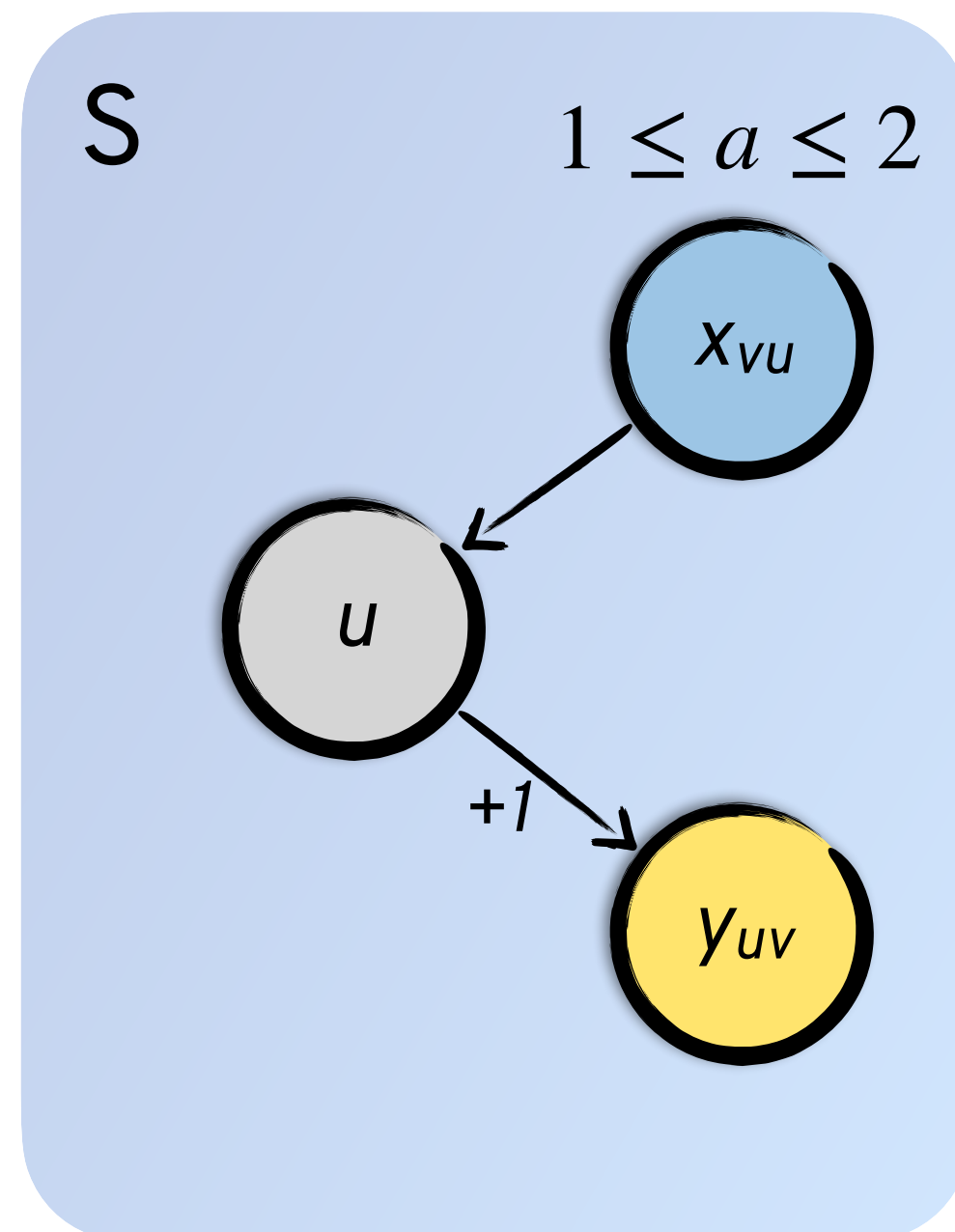
Inductiveness VC?



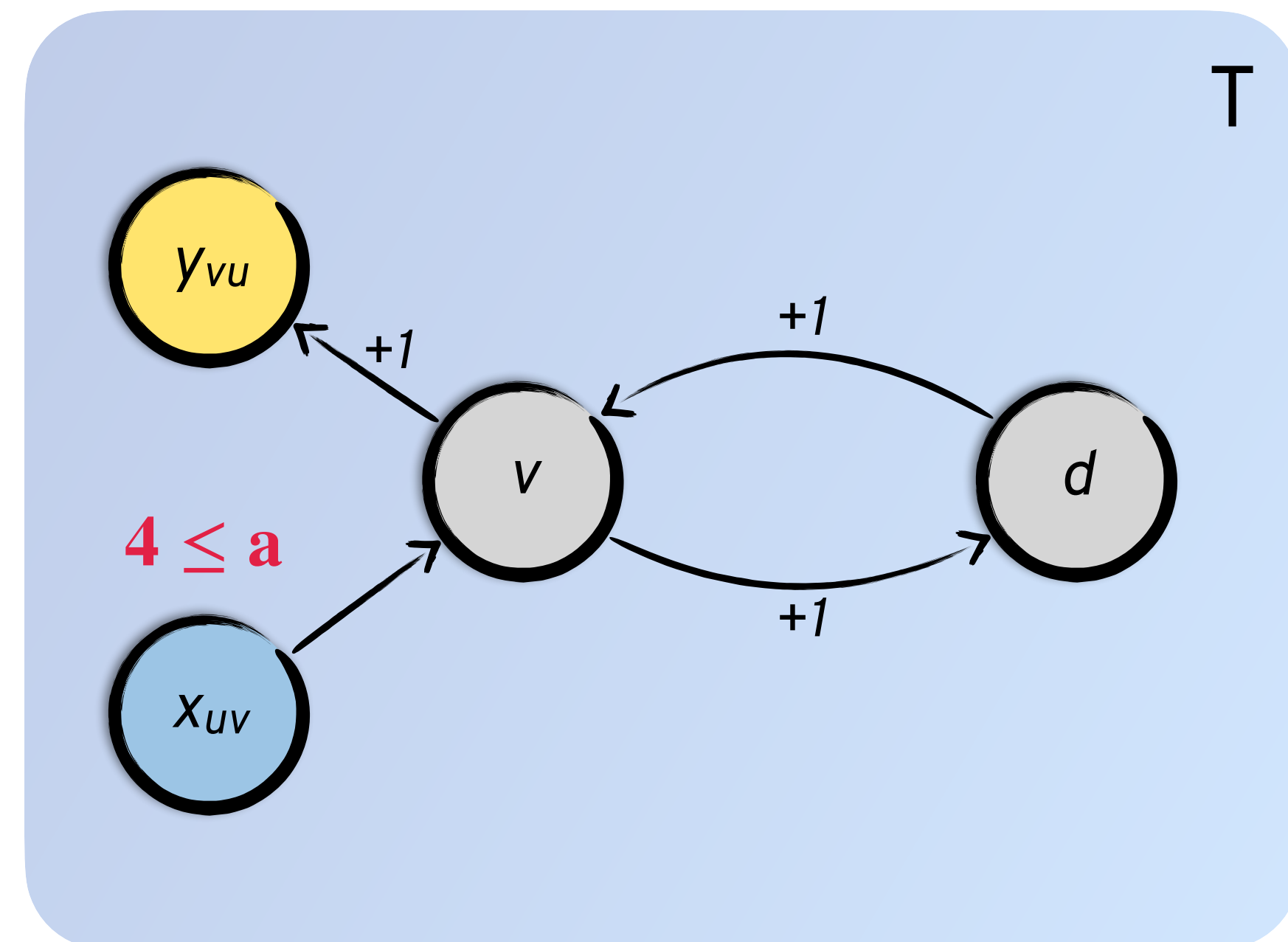
Checking an Interface

Inductiveness VC?

$(v, u, 1 \leq a \leq 2)$
 $(u, v, 4 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$

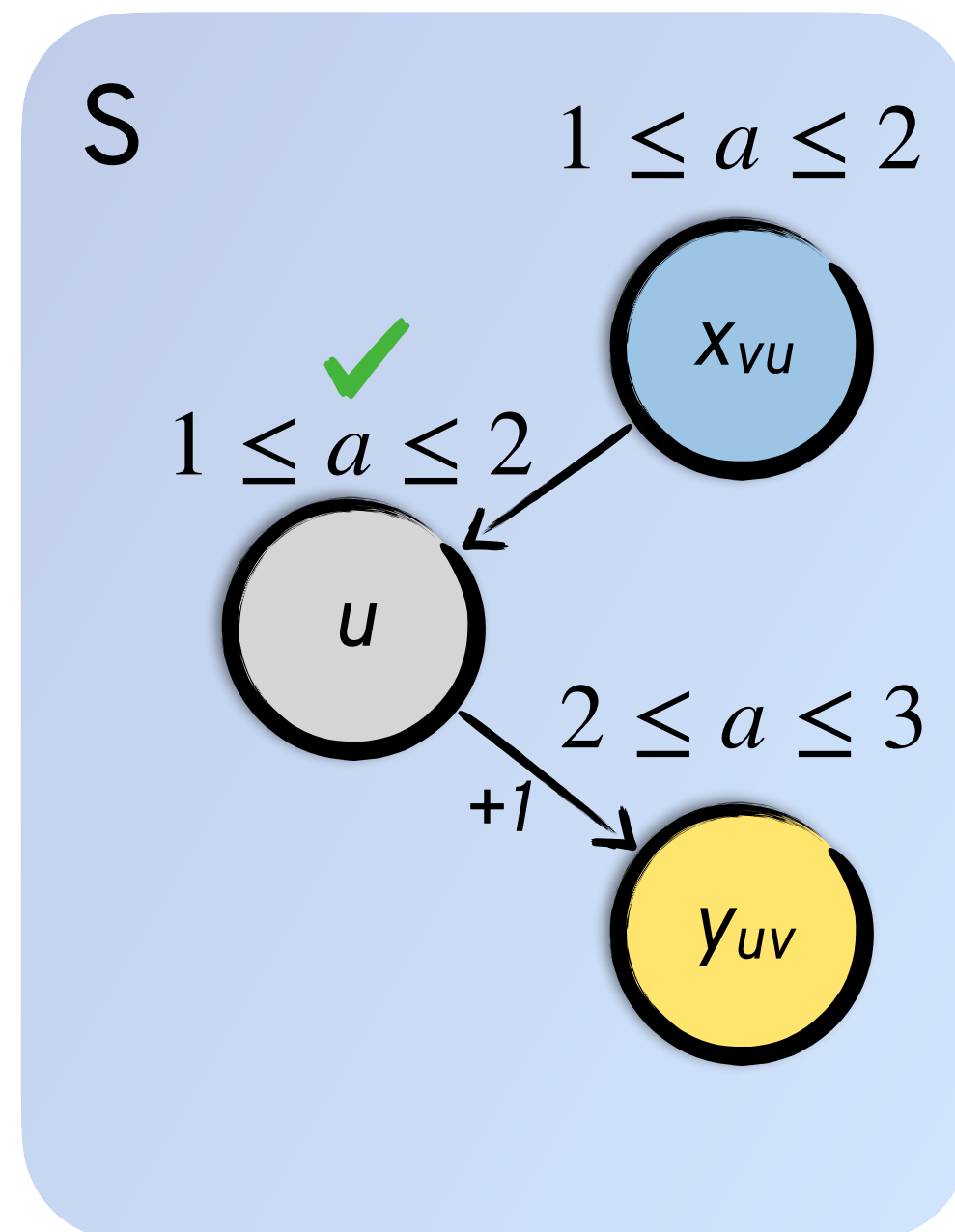


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

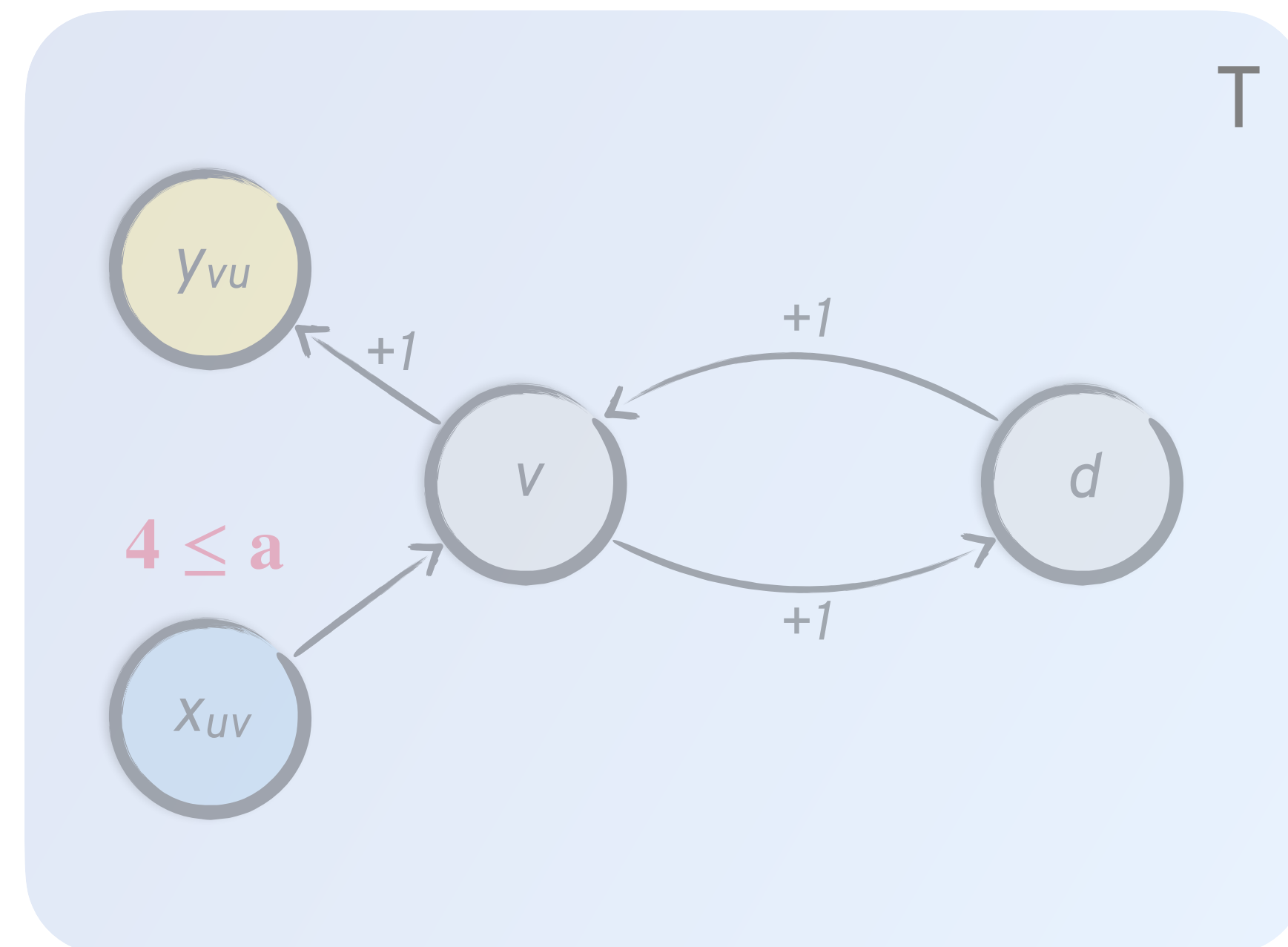
Checking an Interface

Inductiveness VC?

$(v, u, 1 \leq a \leq 2)$
 $(u, v, 4 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$

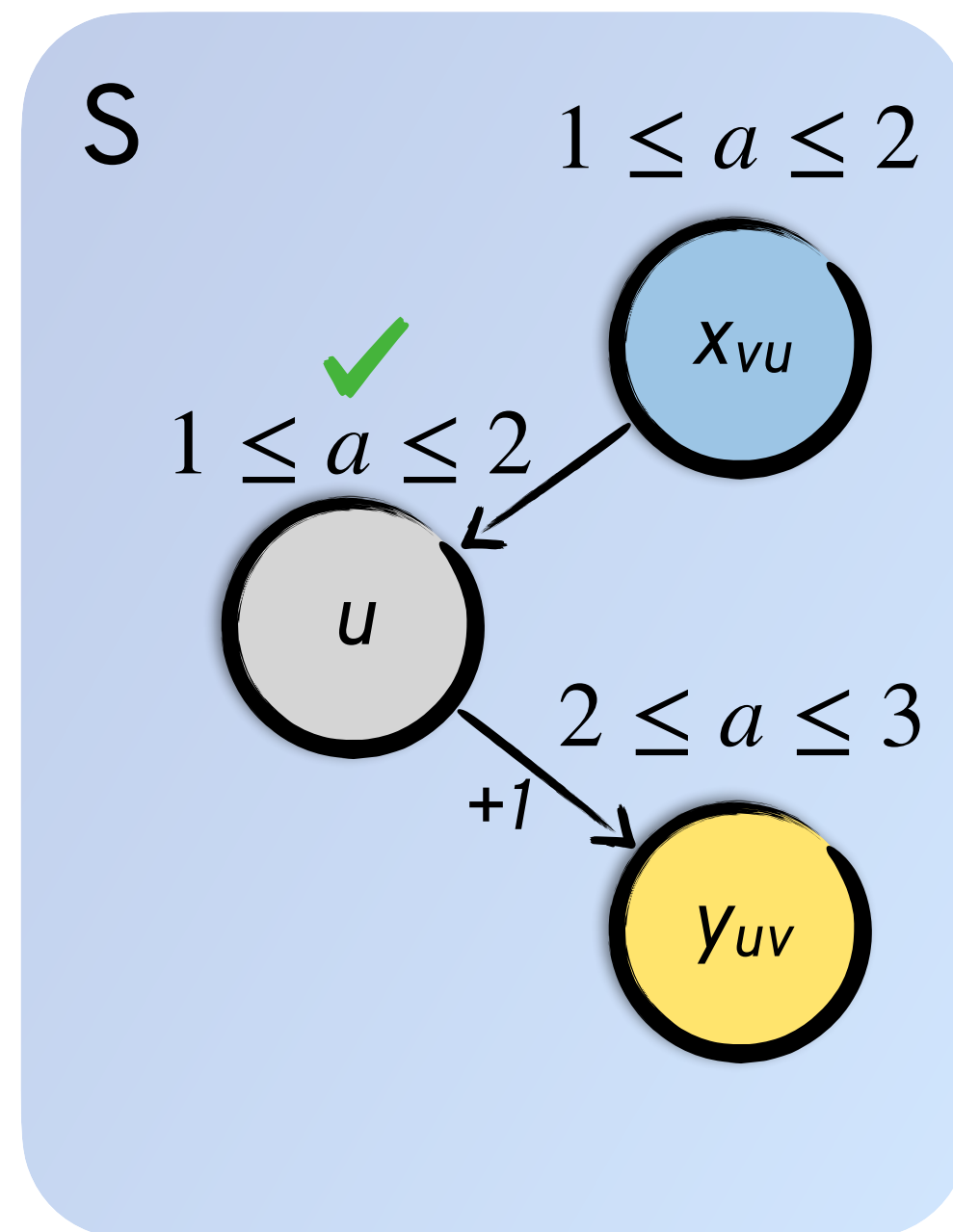


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

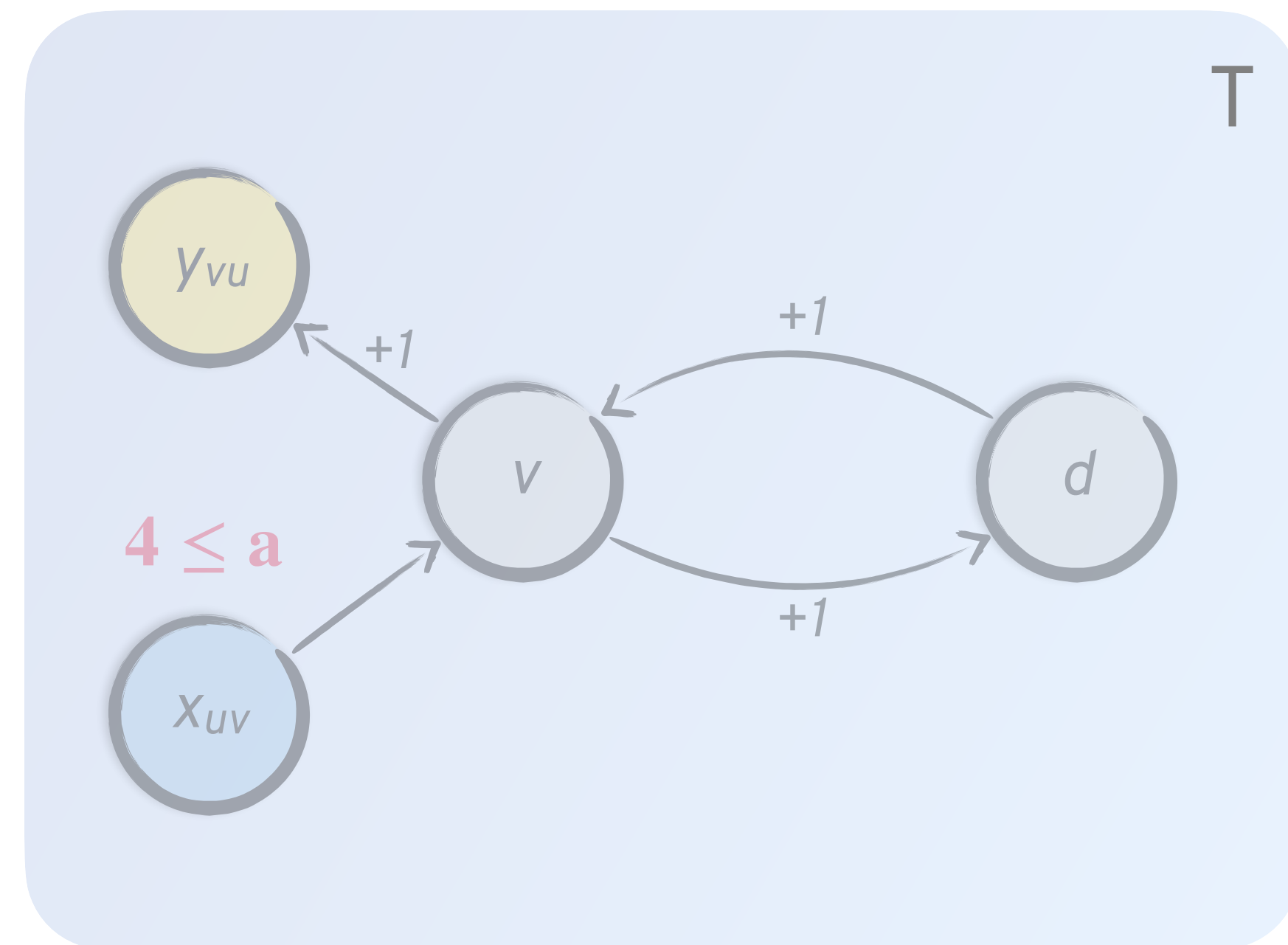
Checking an Interface

Inductiveness VC?

$(v, u, 1 \leq a \leq 2)$
 $(u, v, 4 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$

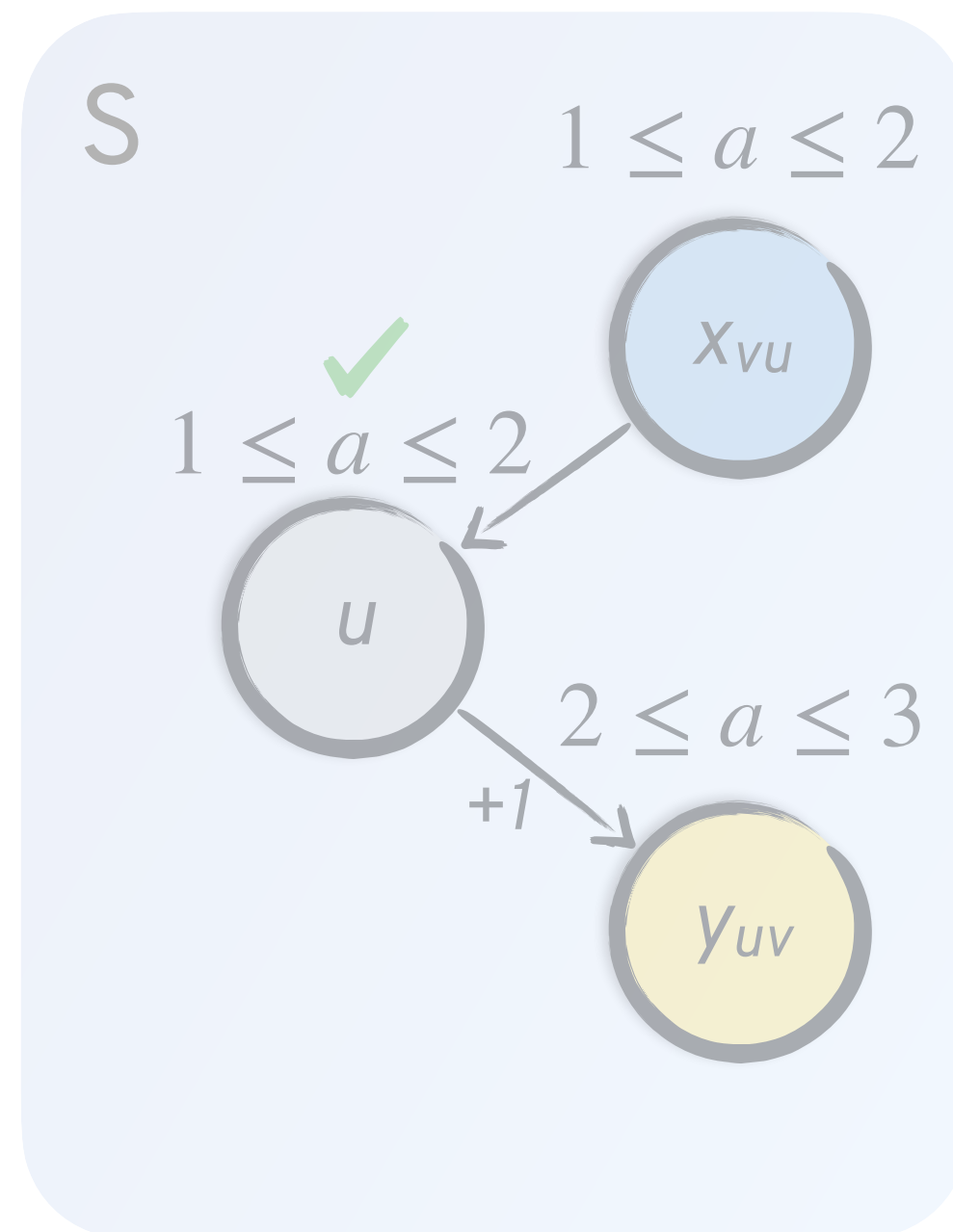


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

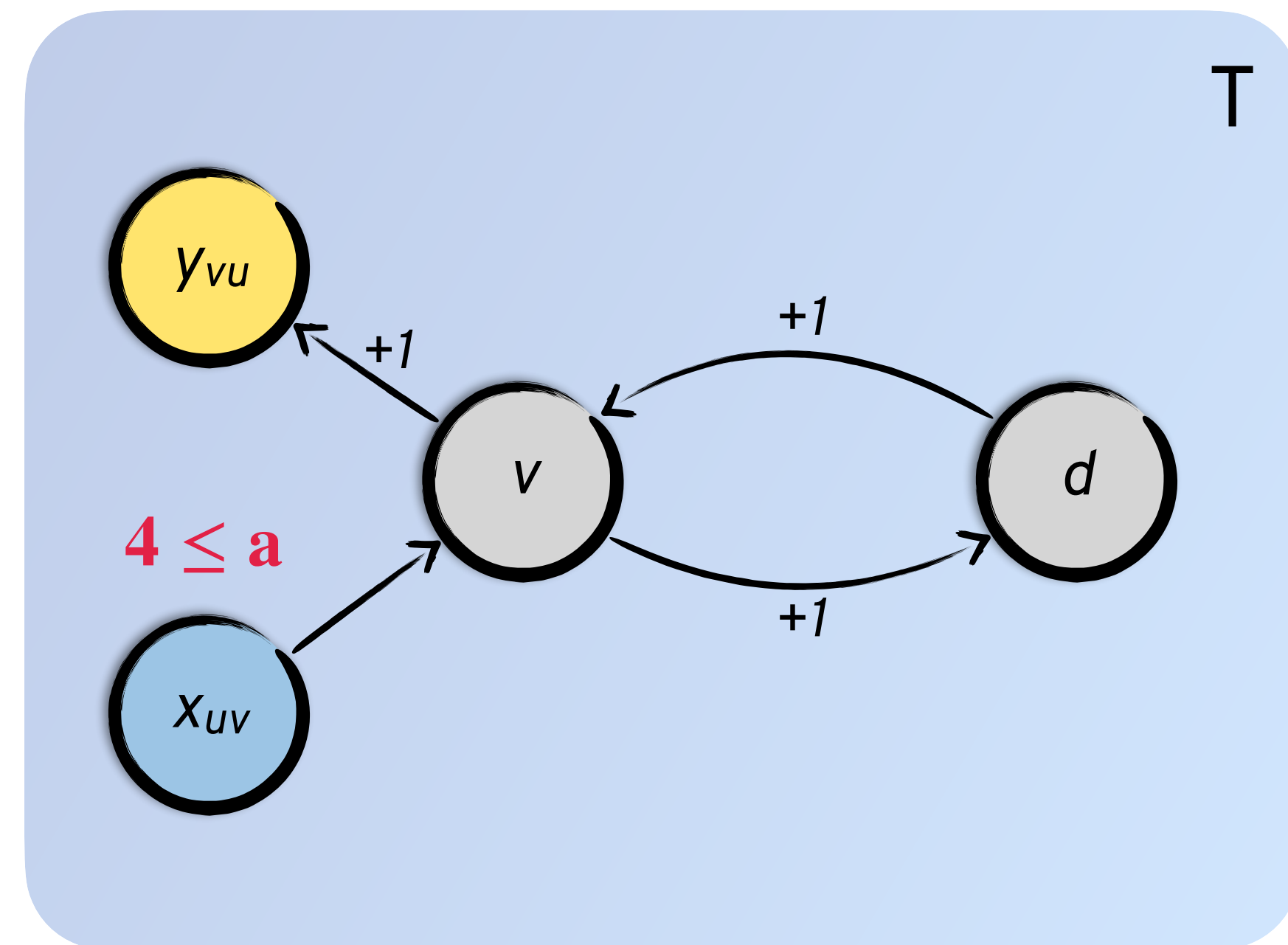
Checking an Interface

Inductiveness VC?

$(v, u, 1 \leq a \leq 2)$
 $(u, v, 4 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$

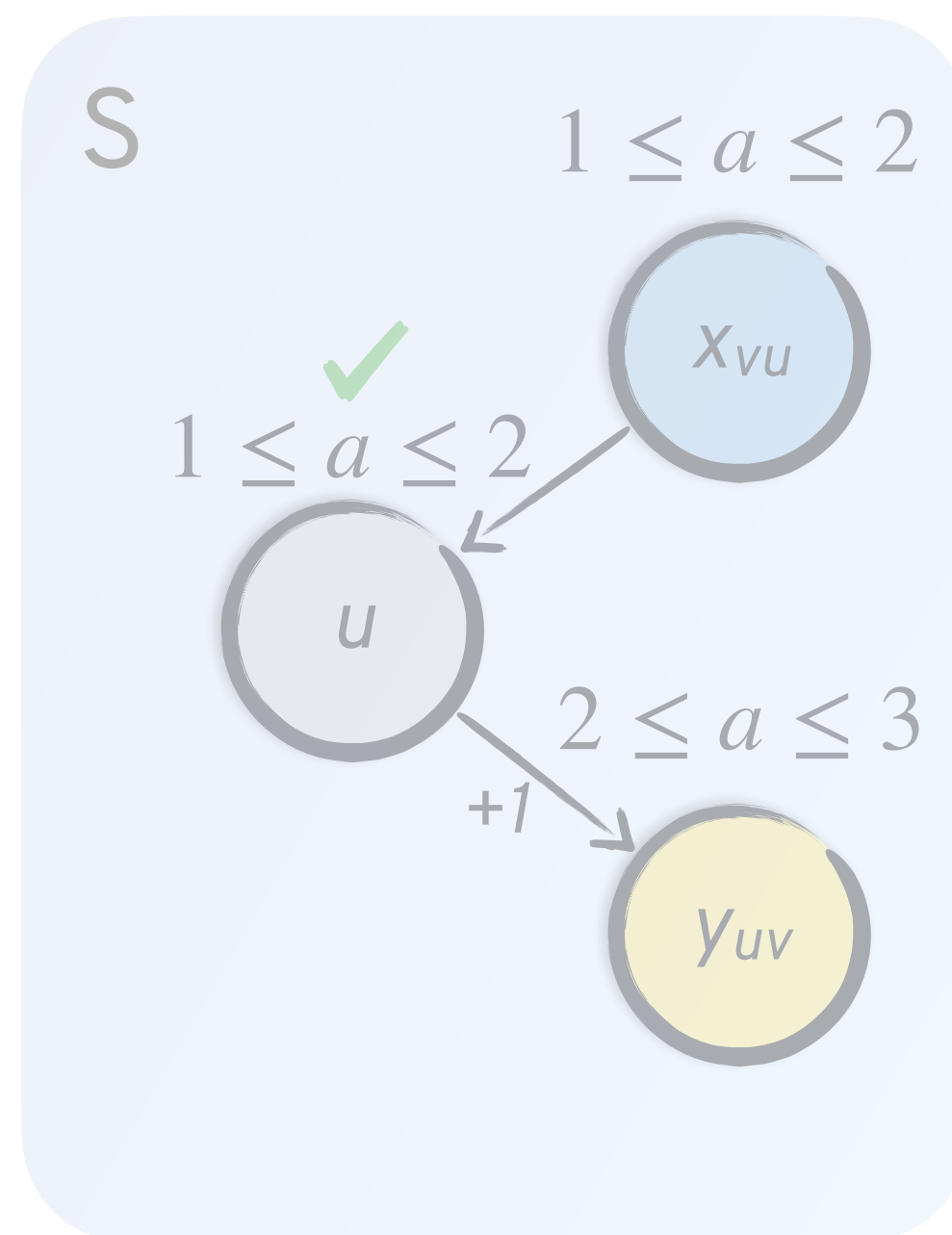


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

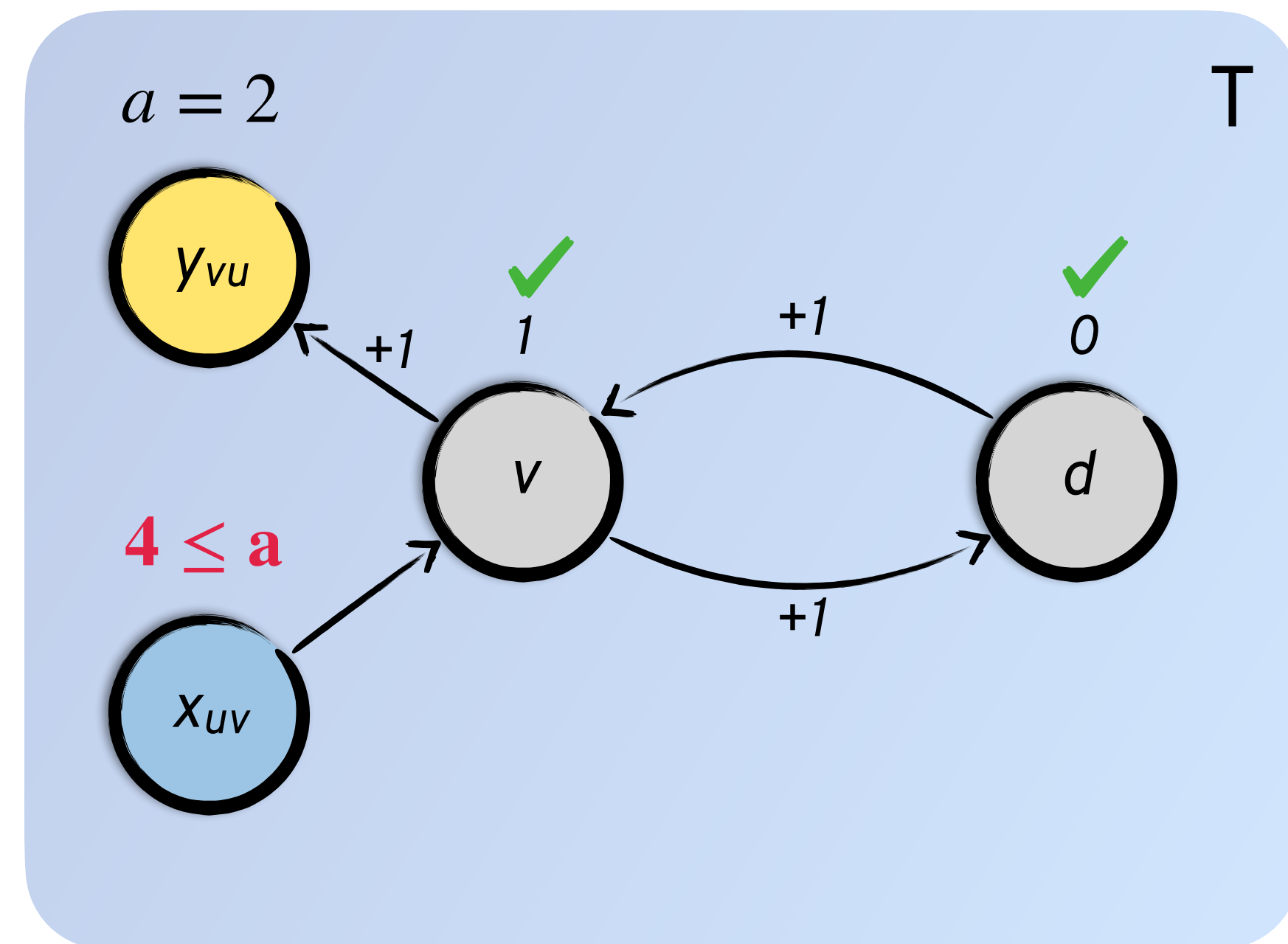
Checking an Interface

Inductiveness VC?

$(v, u, 1 \leq a \leq 2)$
 $(u, v, 4 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$

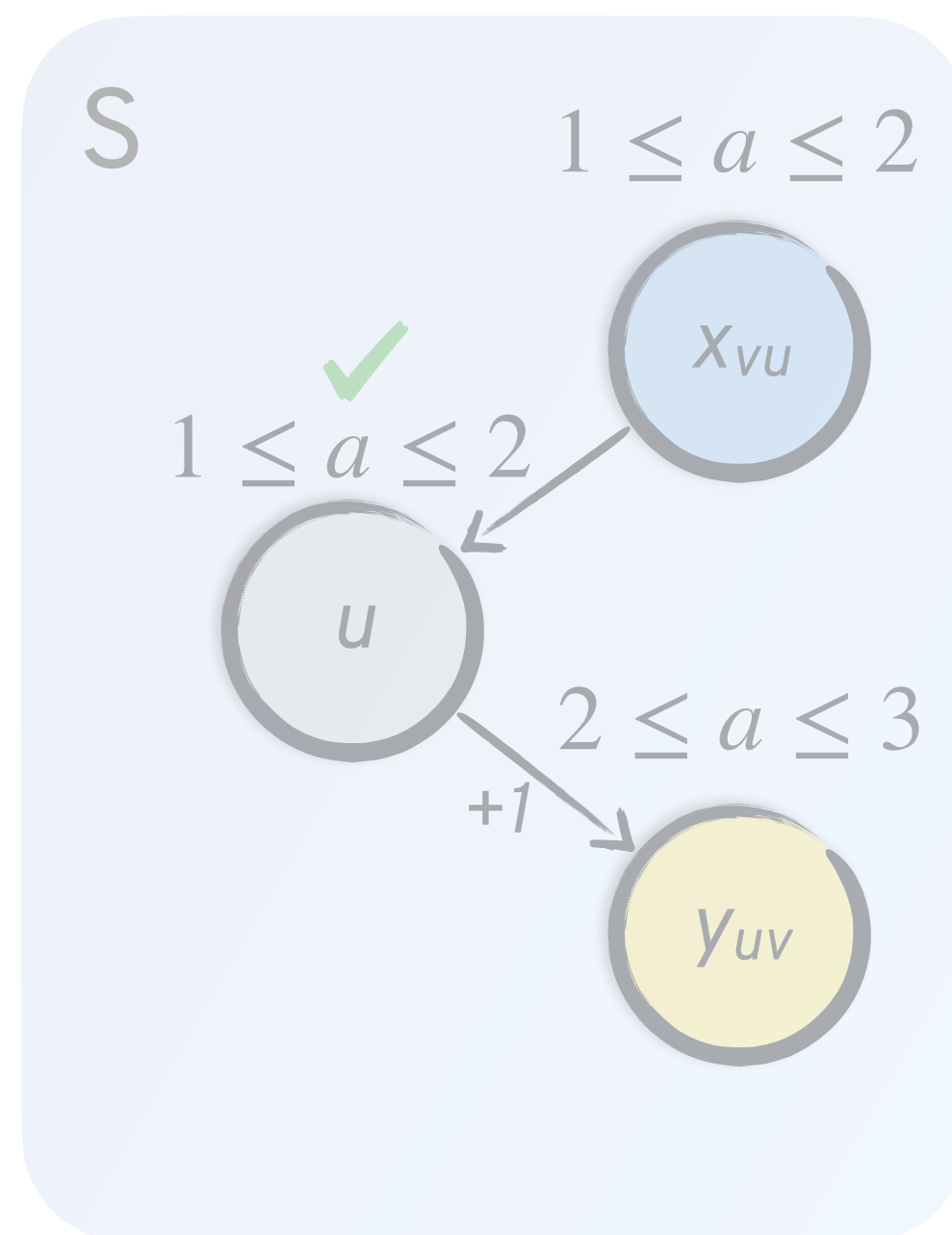


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

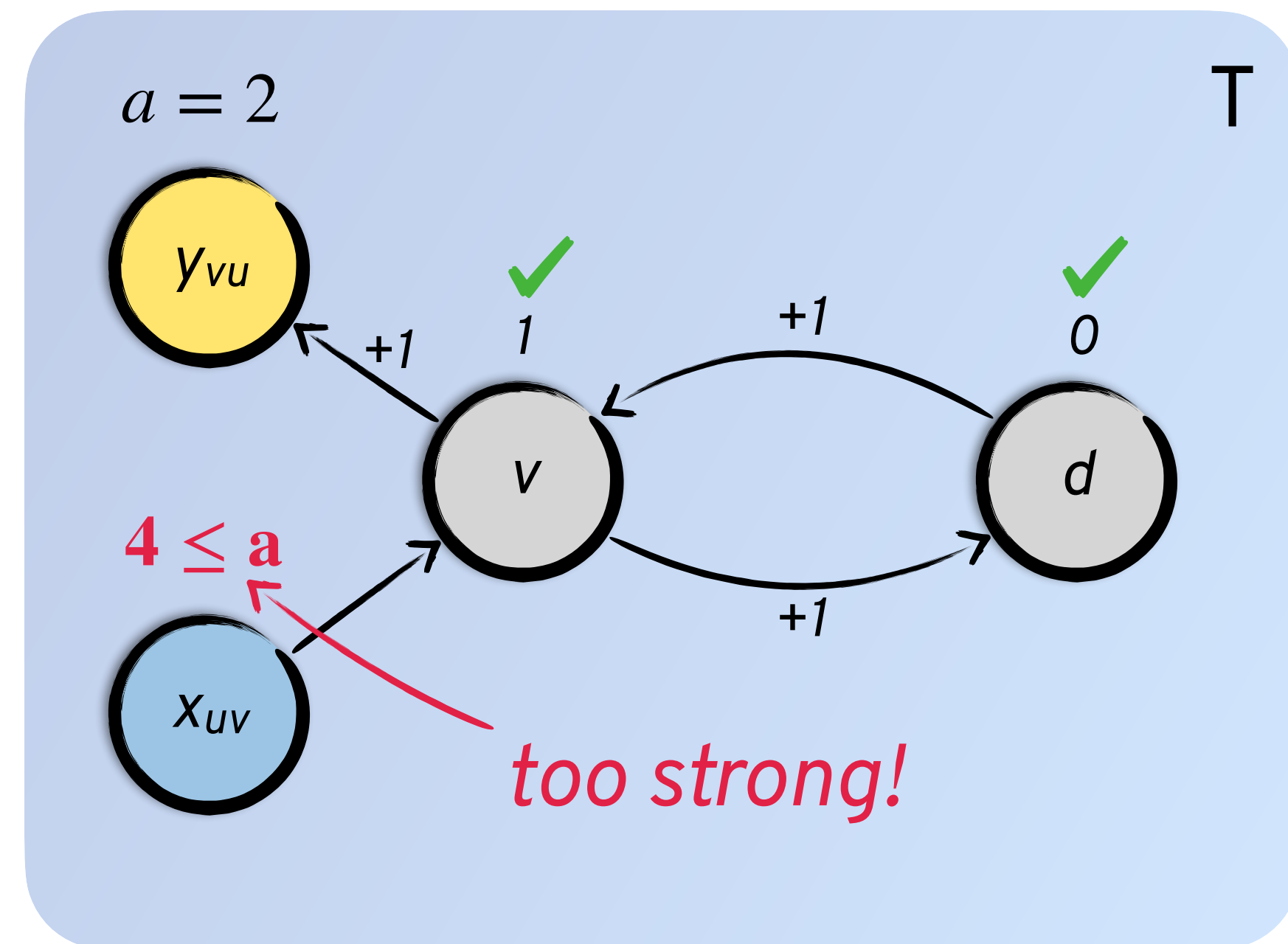
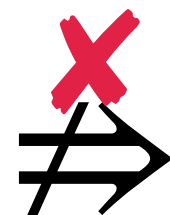
Checking an Interface

Inductiveness VC?

$(v, u, 1 \leq a \leq 2)$
 $(u, v, 4 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$

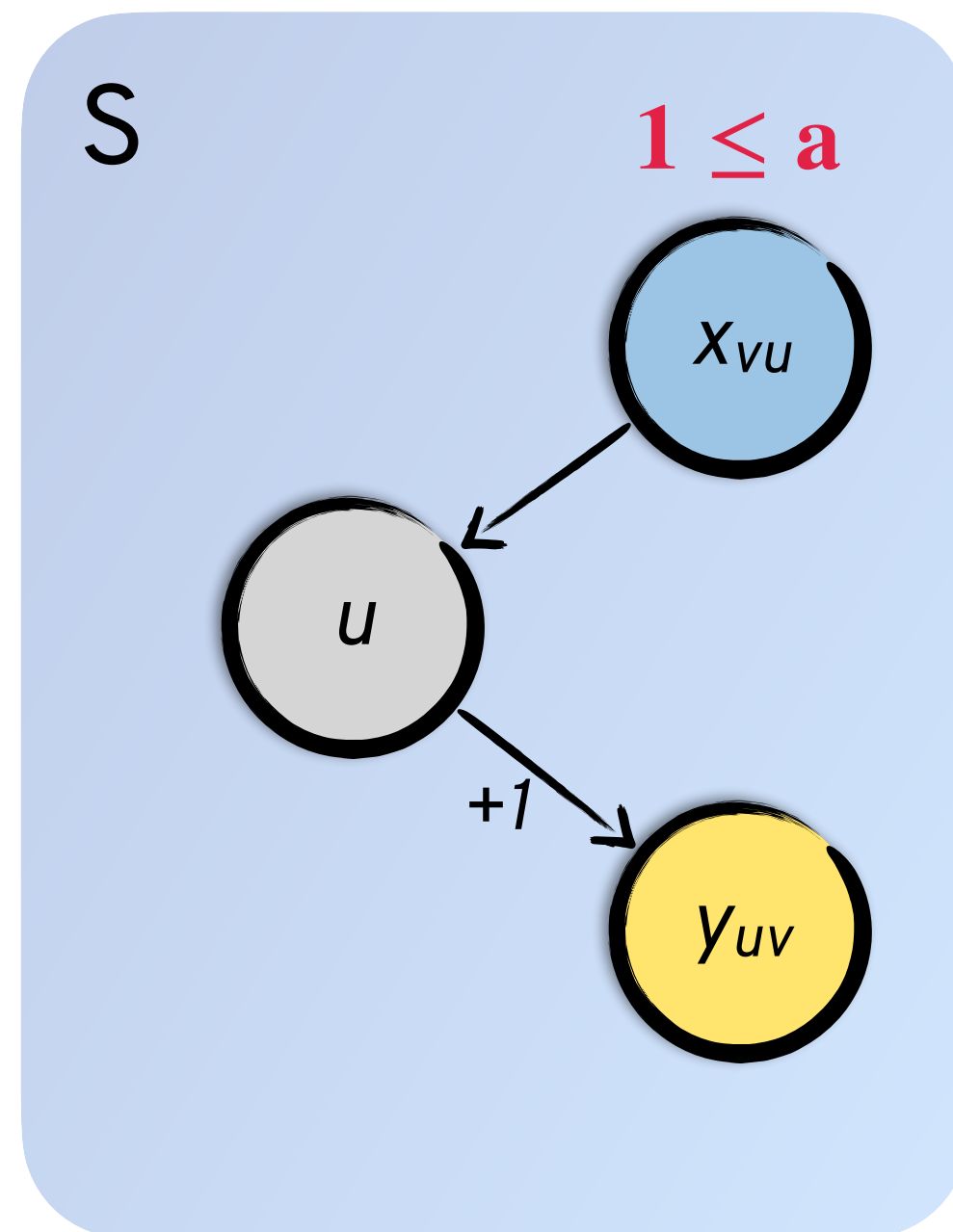


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

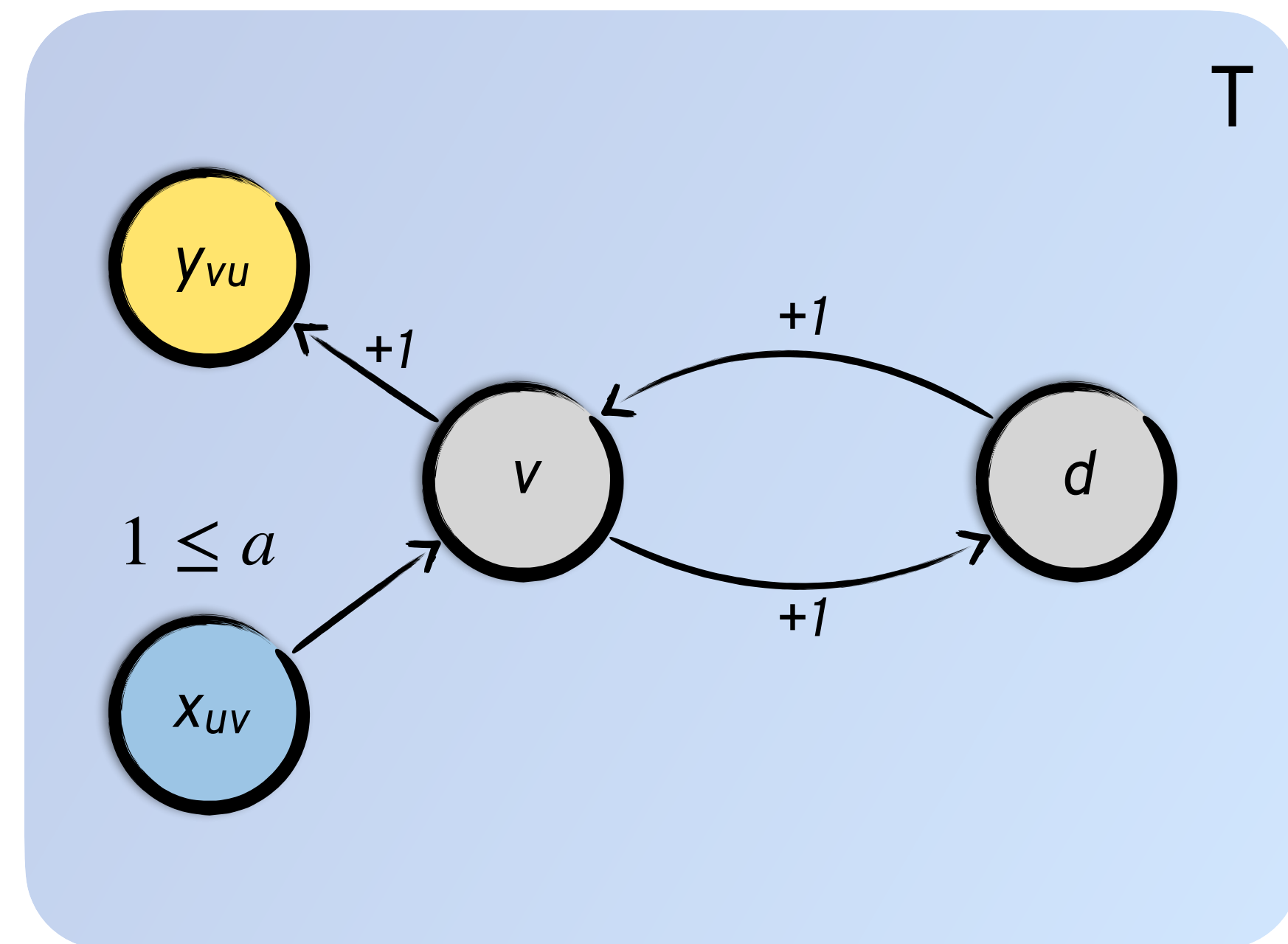
Checking an Interface

Safety VC?

$(v, u, \mathbf{1} \leq \mathbf{a})$
 $(u, v, 1 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$

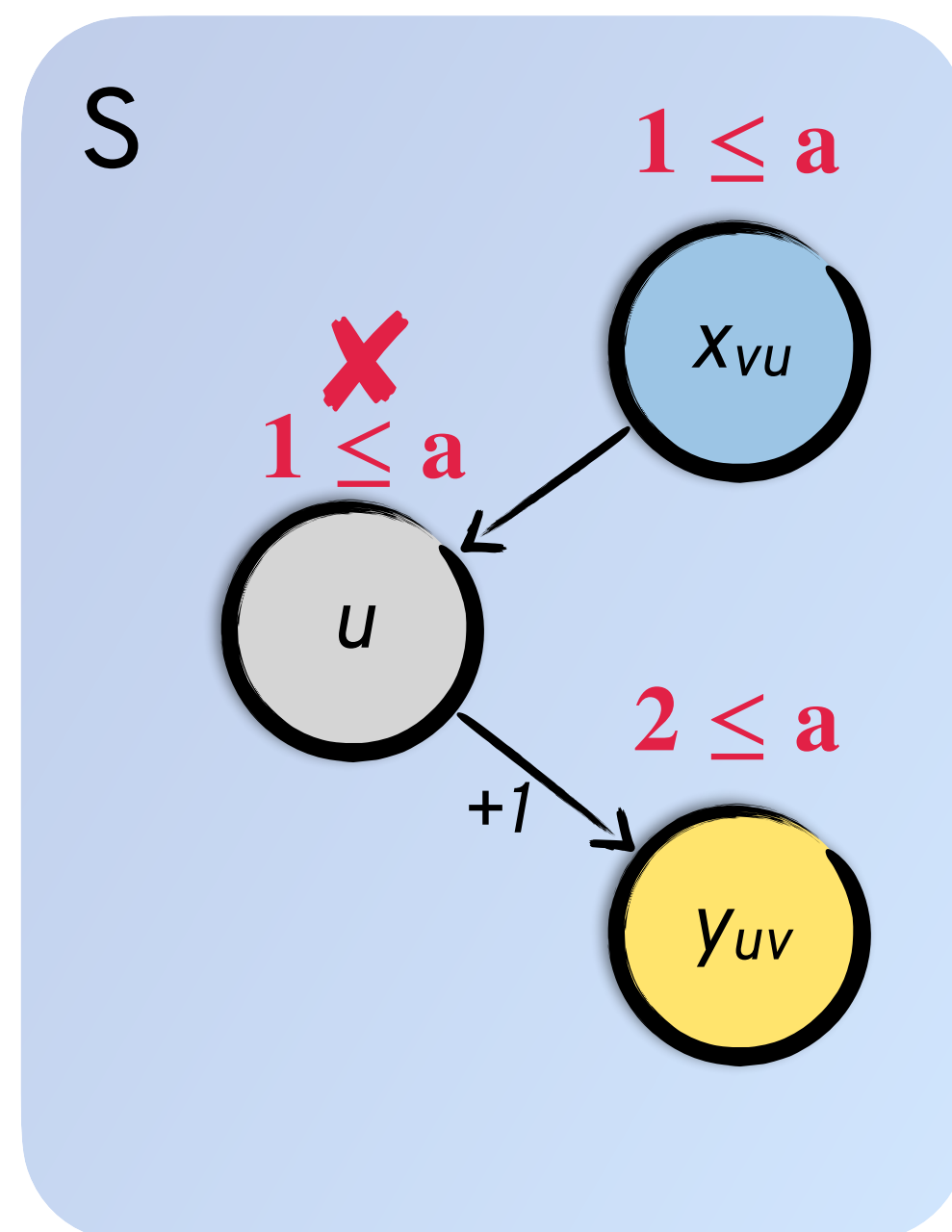


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

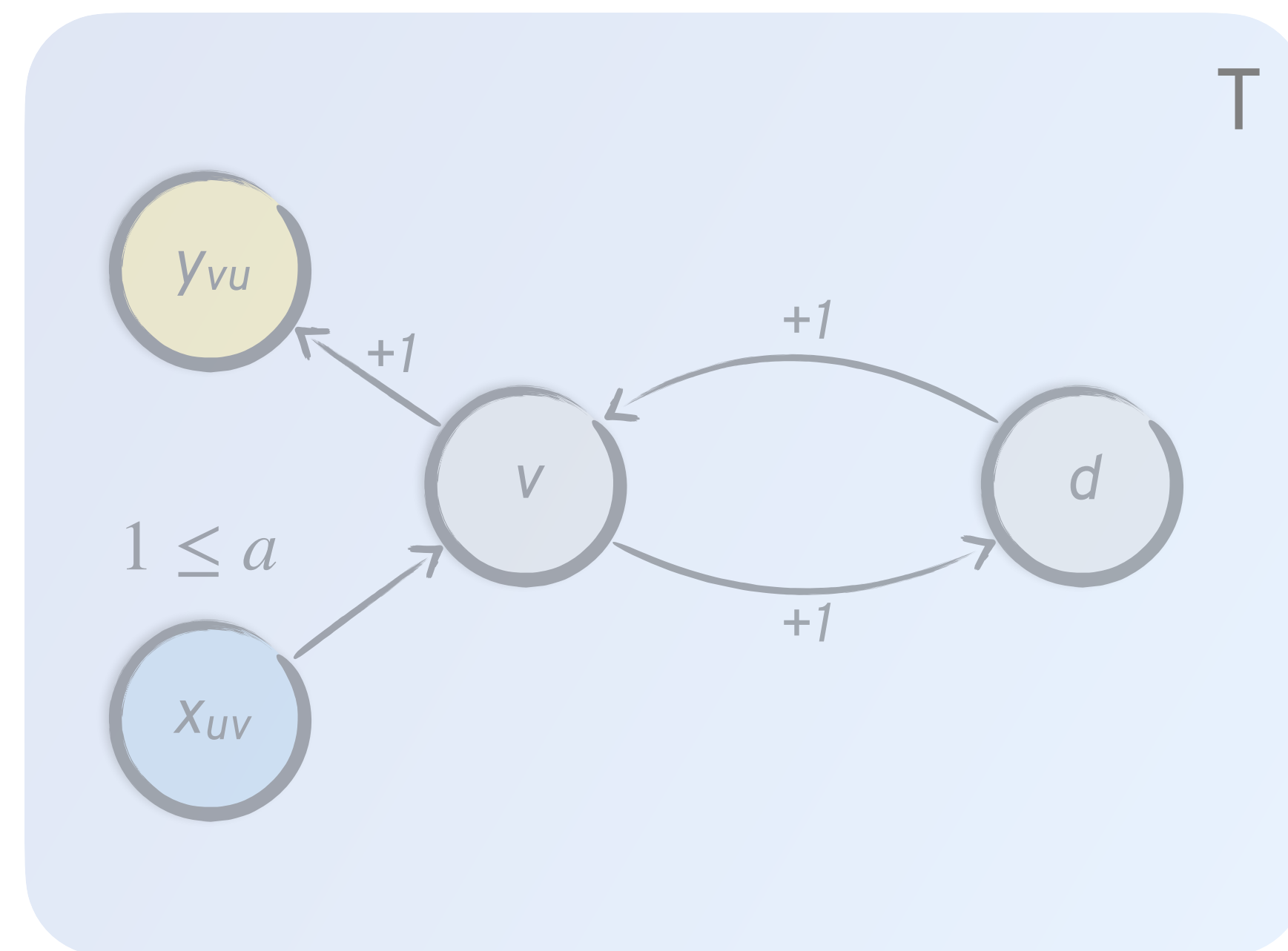
Checking an Interface

Safety VC?

$(v, u, 1 \leq a)$
 $(u, v, 1 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$

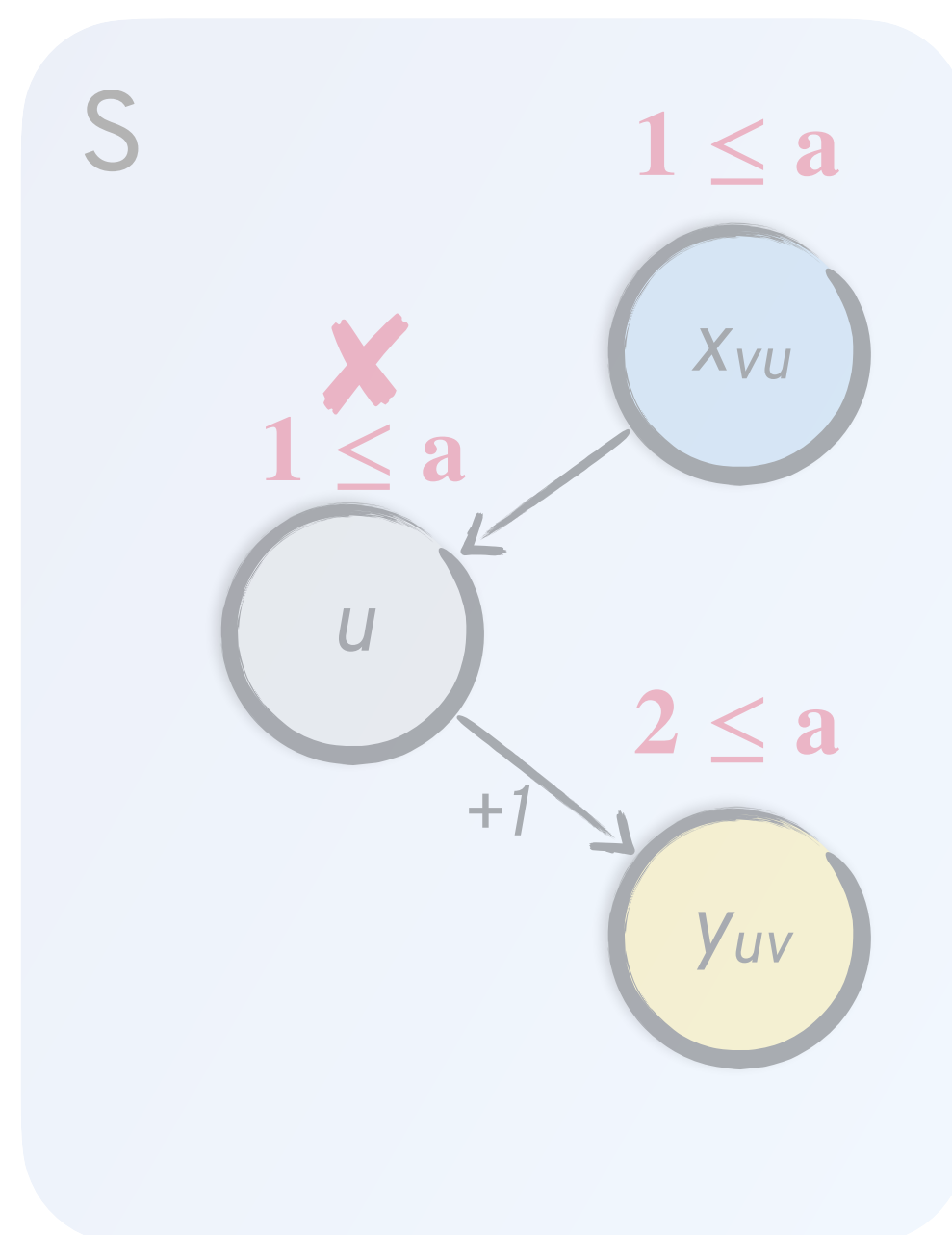


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

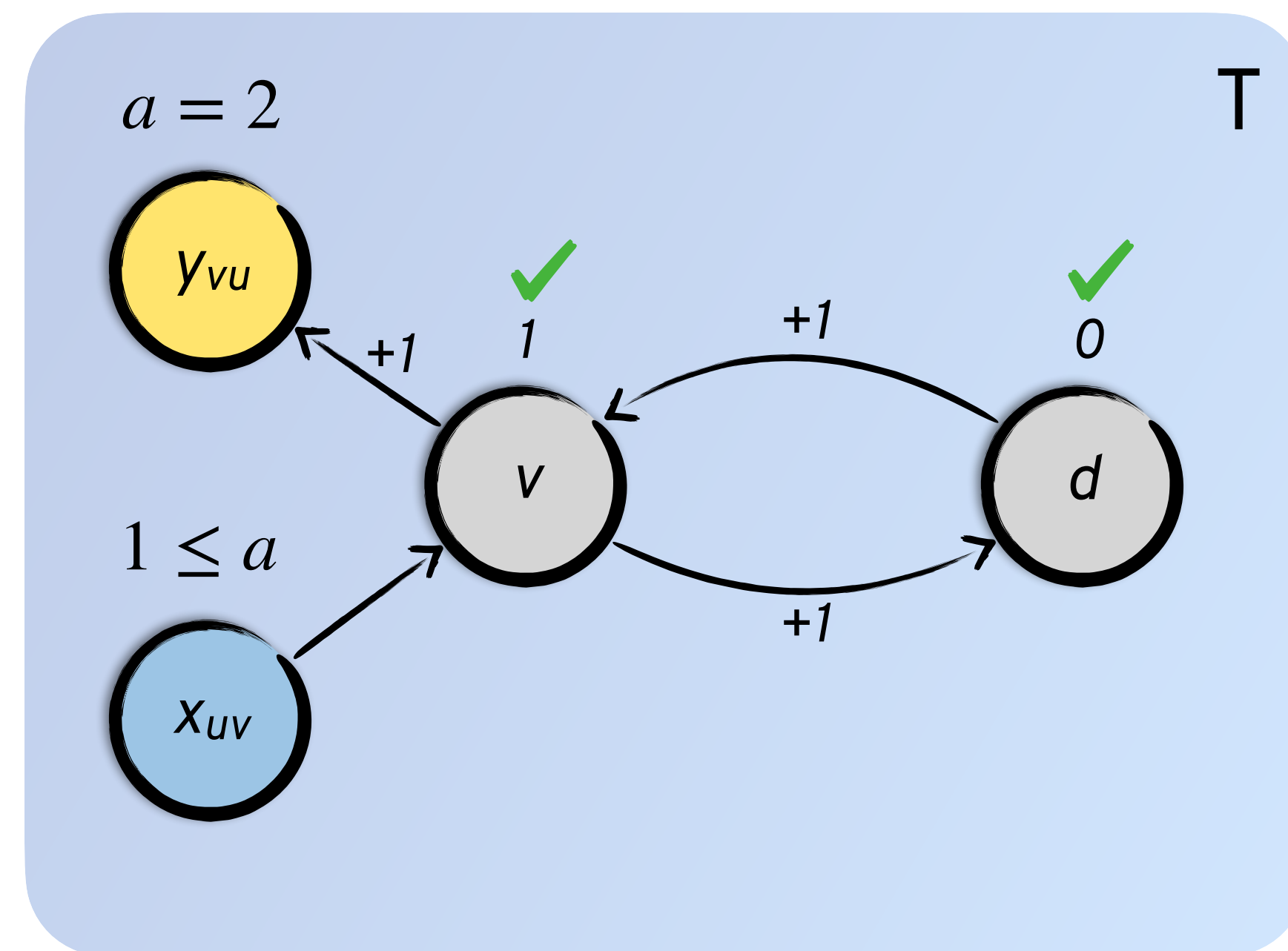
Checking an Interface

Safety VC?

$(v, u, \mathbf{1} \leq a)$
 $(u, v, 1 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$



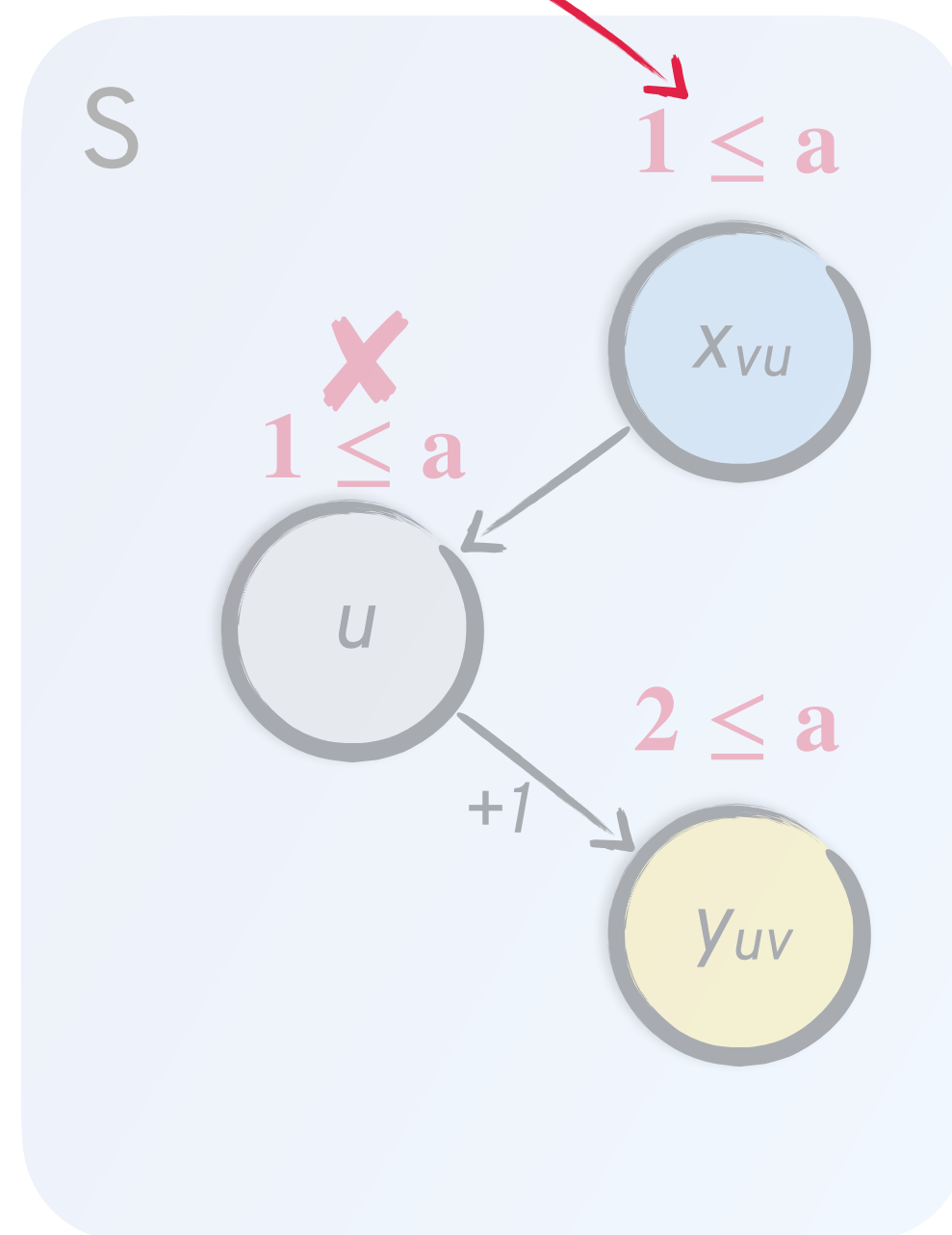
$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

Checking an Interface

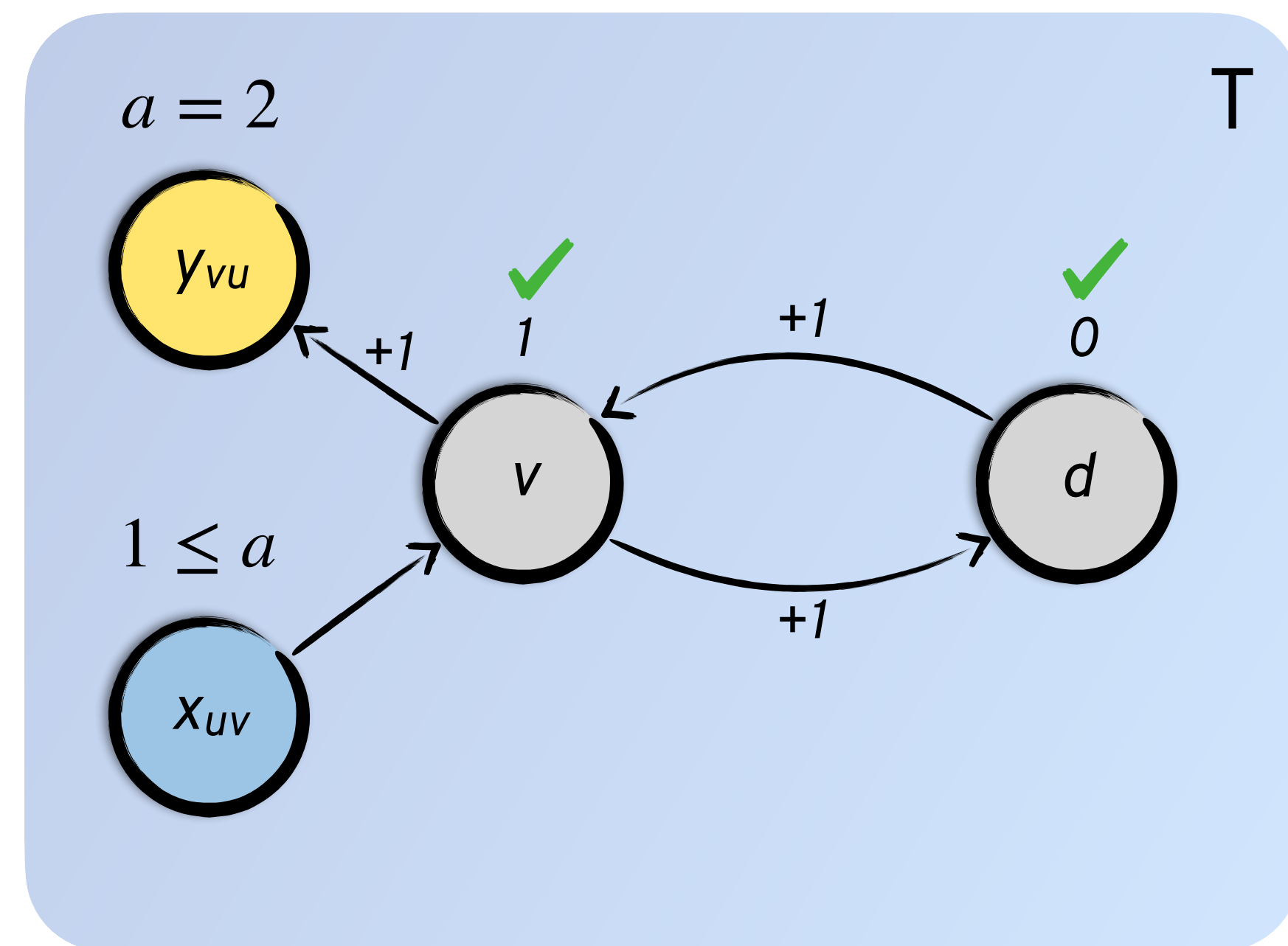
Safety VC?

too weak!

$(v, u, \mathbf{1} \leq a)$
 $(u, v, 1 \leq a)$



$$P_S = \mathcal{L}(u) < 10$$

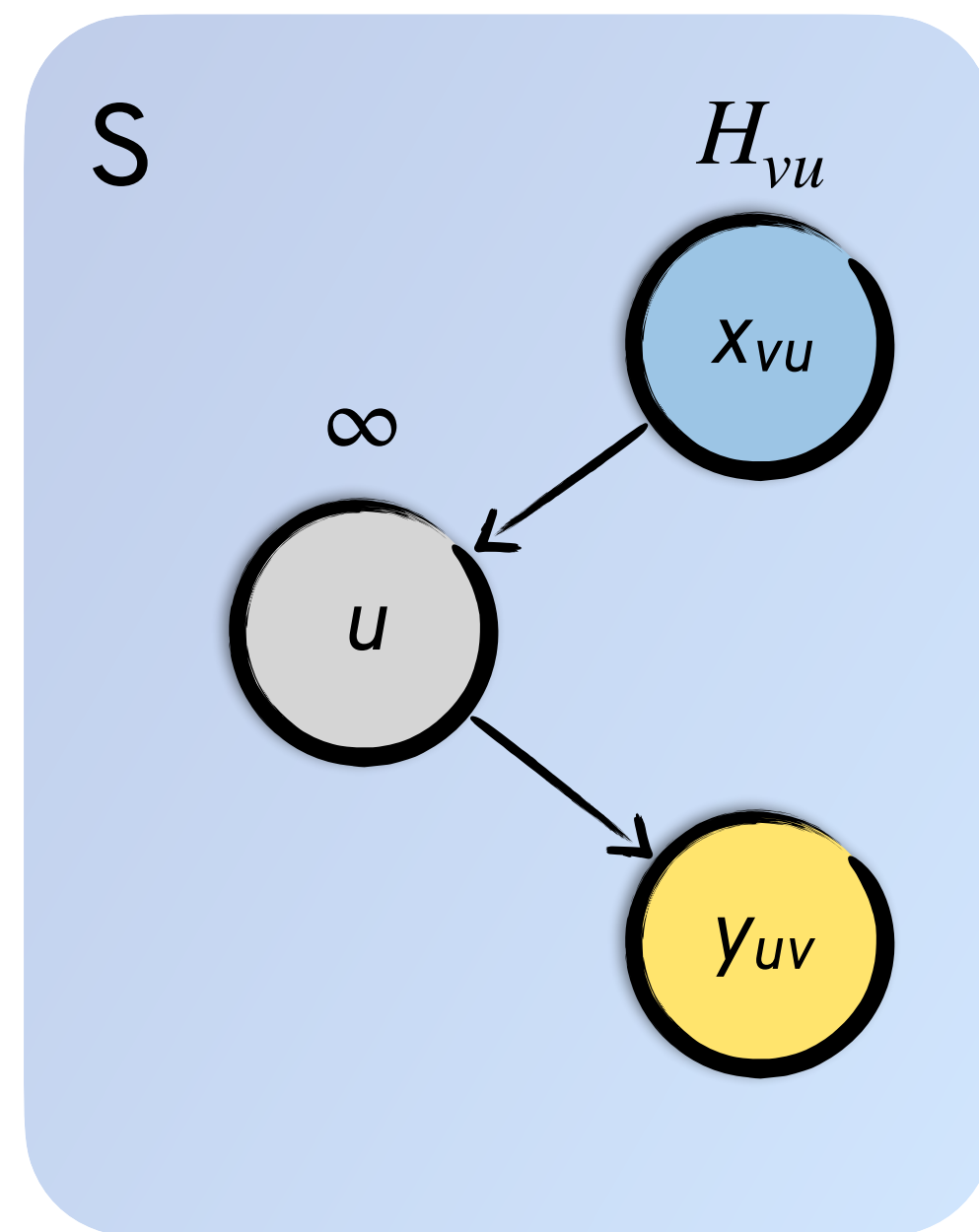


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

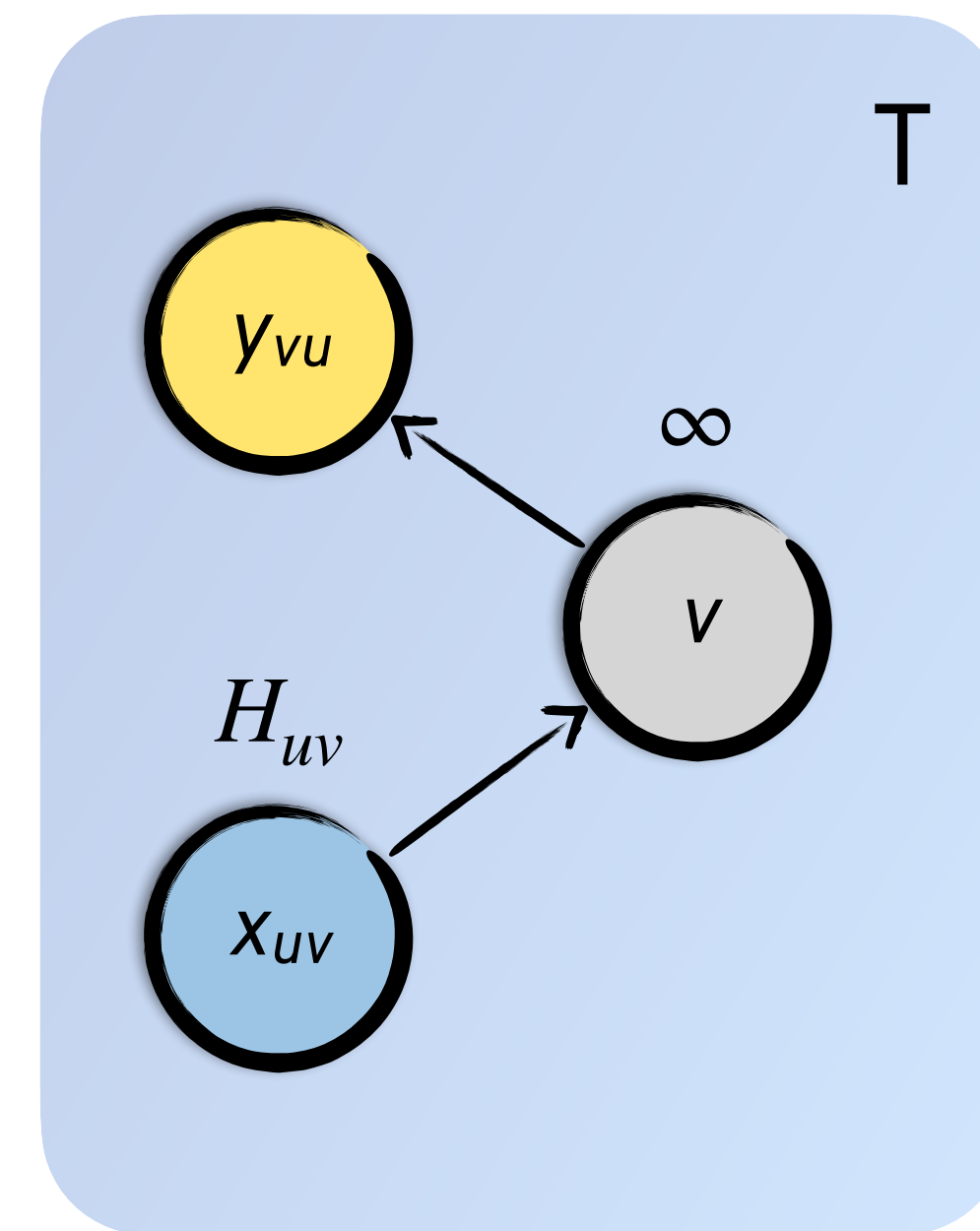
Checking an Interface

Networks with Multiple Solutions

$(v, u, a = \infty)$
 $(u, v, a = \infty)$



$P_S = \text{true}$

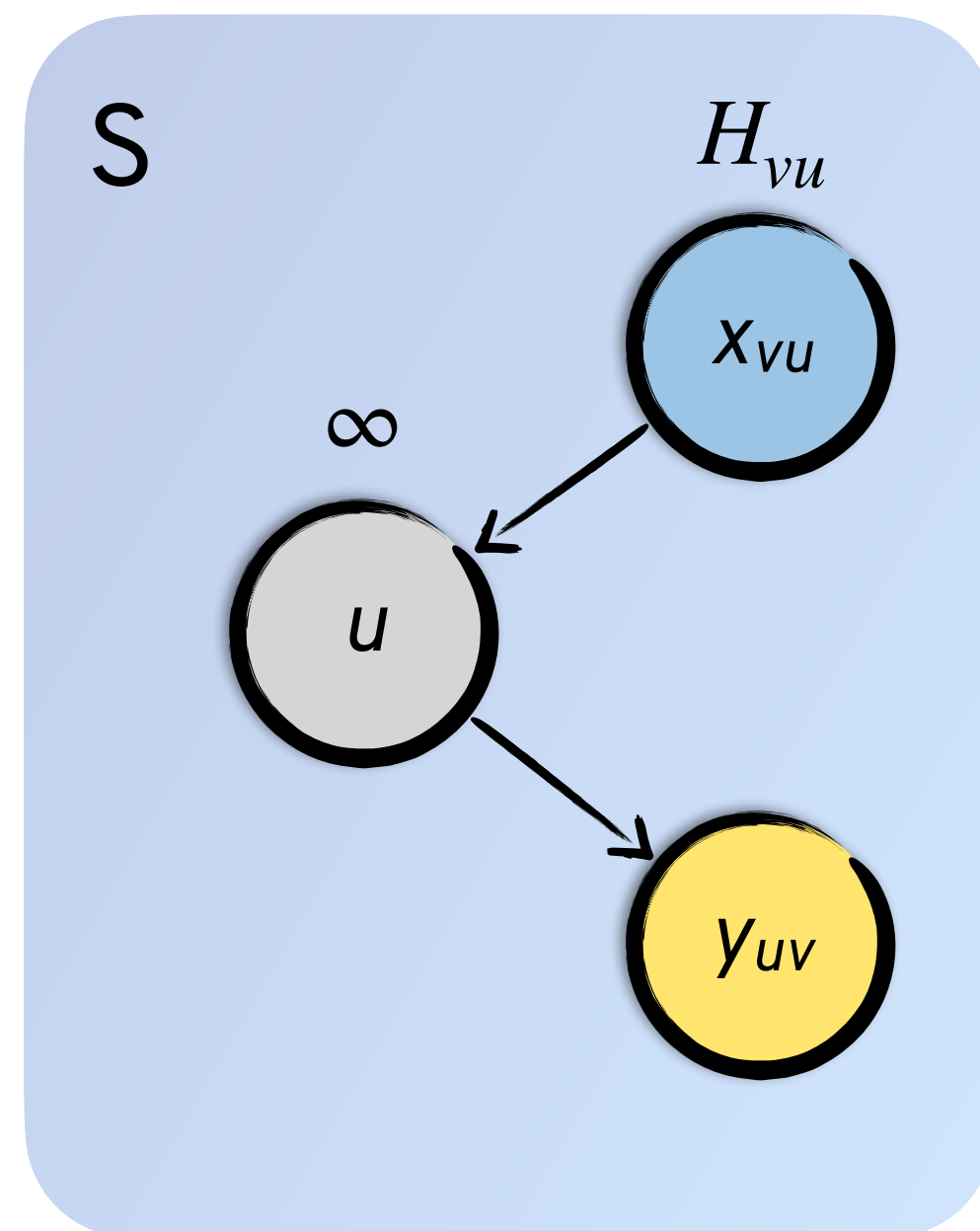


$P_T = \text{true}$

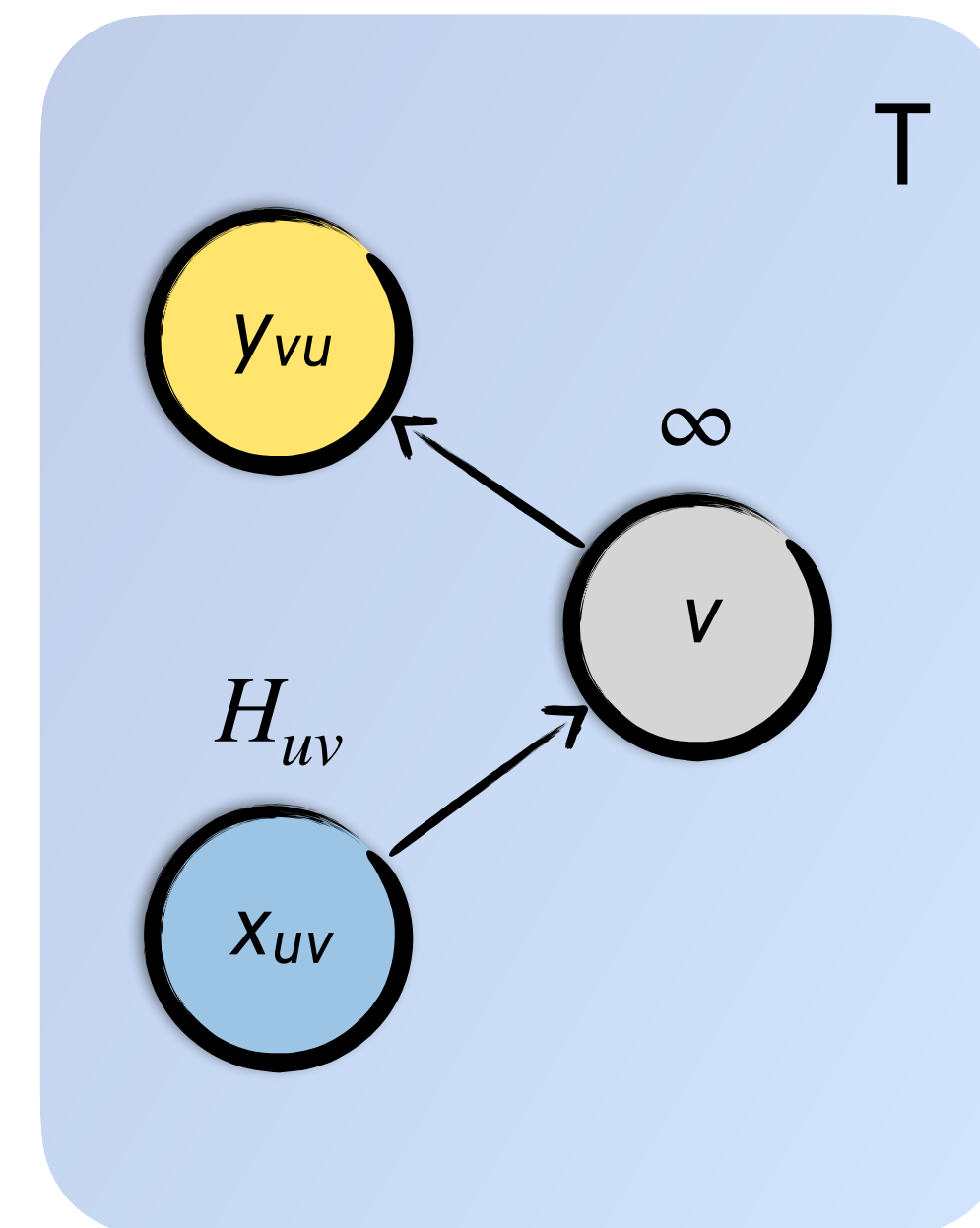
Checking an Interface

Networks with Multiple Solutions

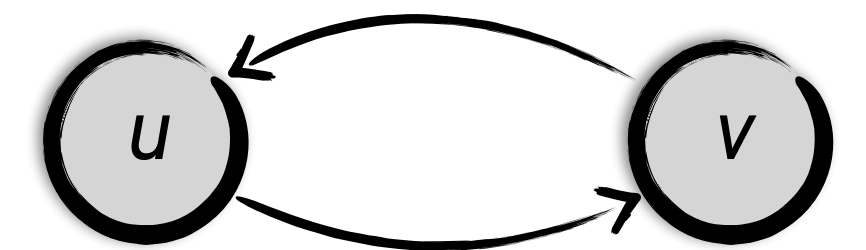
$(v, u, a = \infty)$
 $(u, v, a = \infty)$



$P_S = \text{true}$



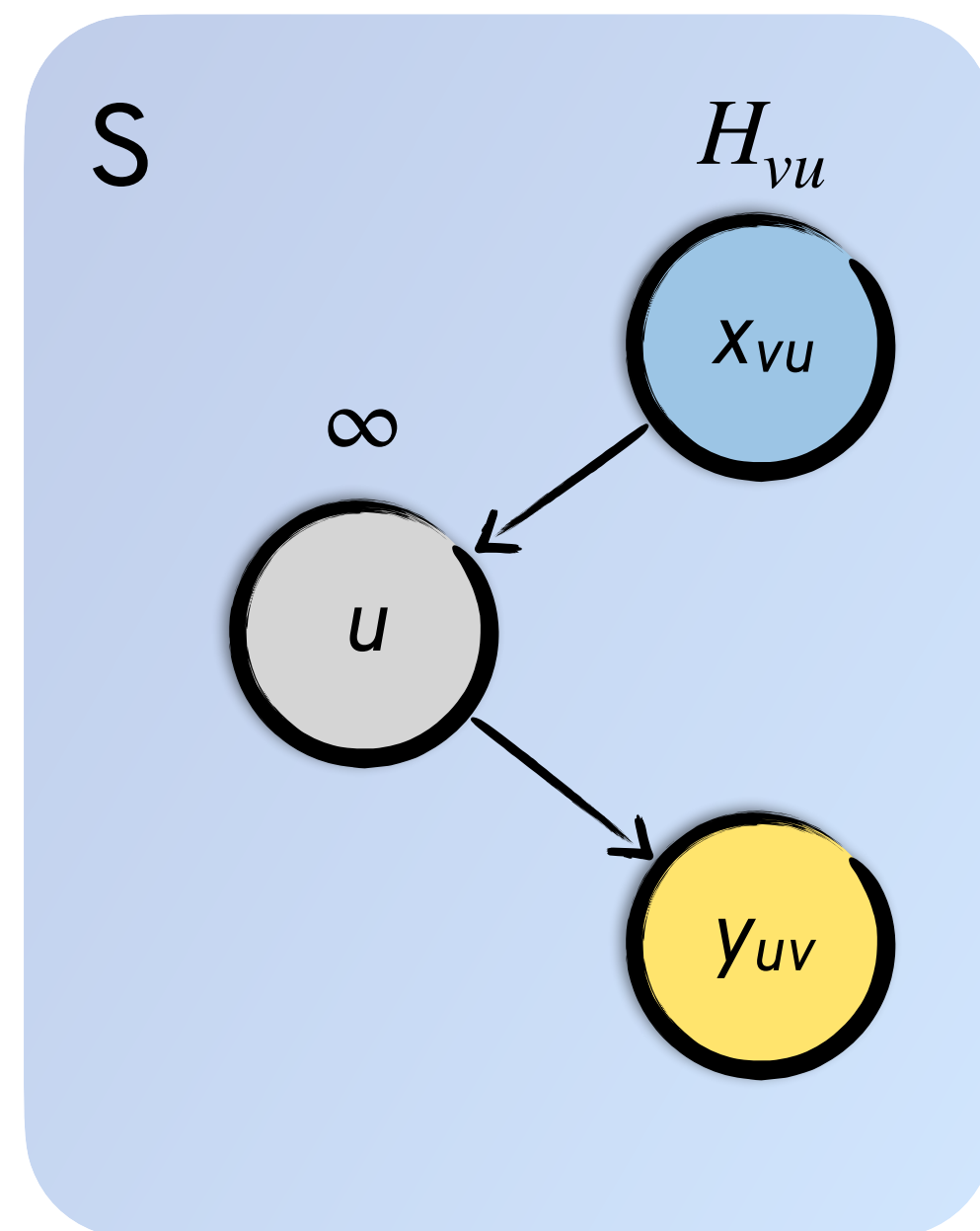
$P_T = \text{true}$



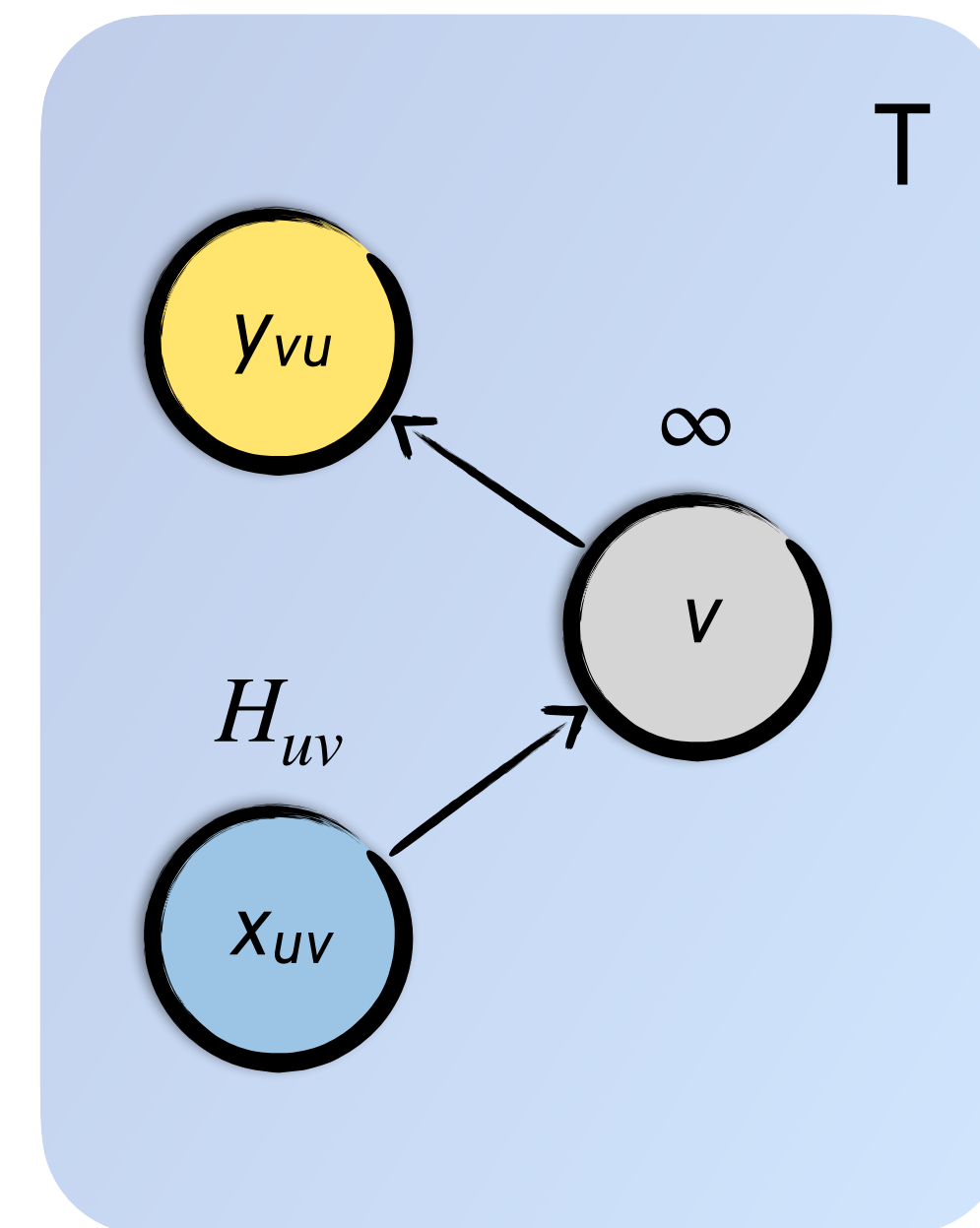
Checking an Interface

Networks with Multiple Solutions

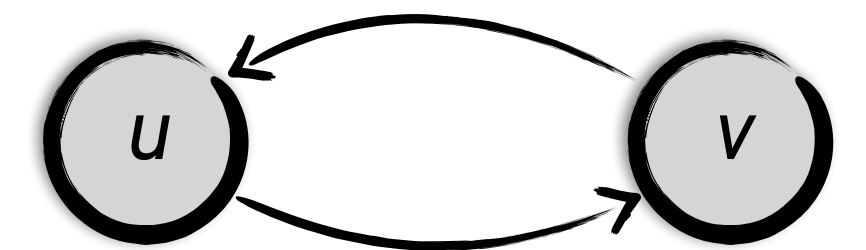
$$\begin{aligned} (v, u, a = \infty) \\ (u, v, a = \infty) \end{aligned}$$



$$P_S = \text{true}$$



$$P_T = \text{true}$$

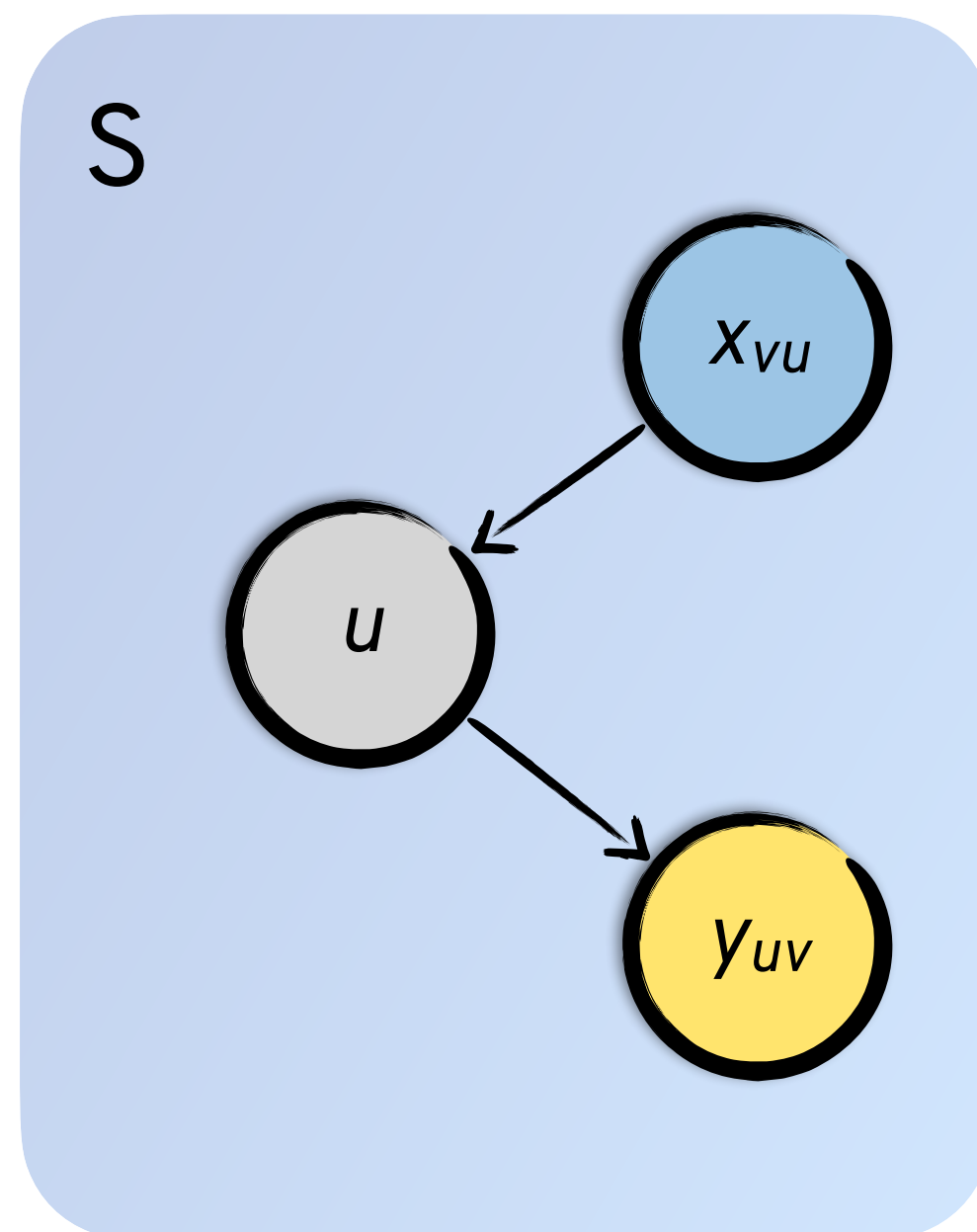


$$\begin{aligned} \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\ &= 1 \\ &= 2 \\ &\dots \\ &= \infty \end{aligned}$$

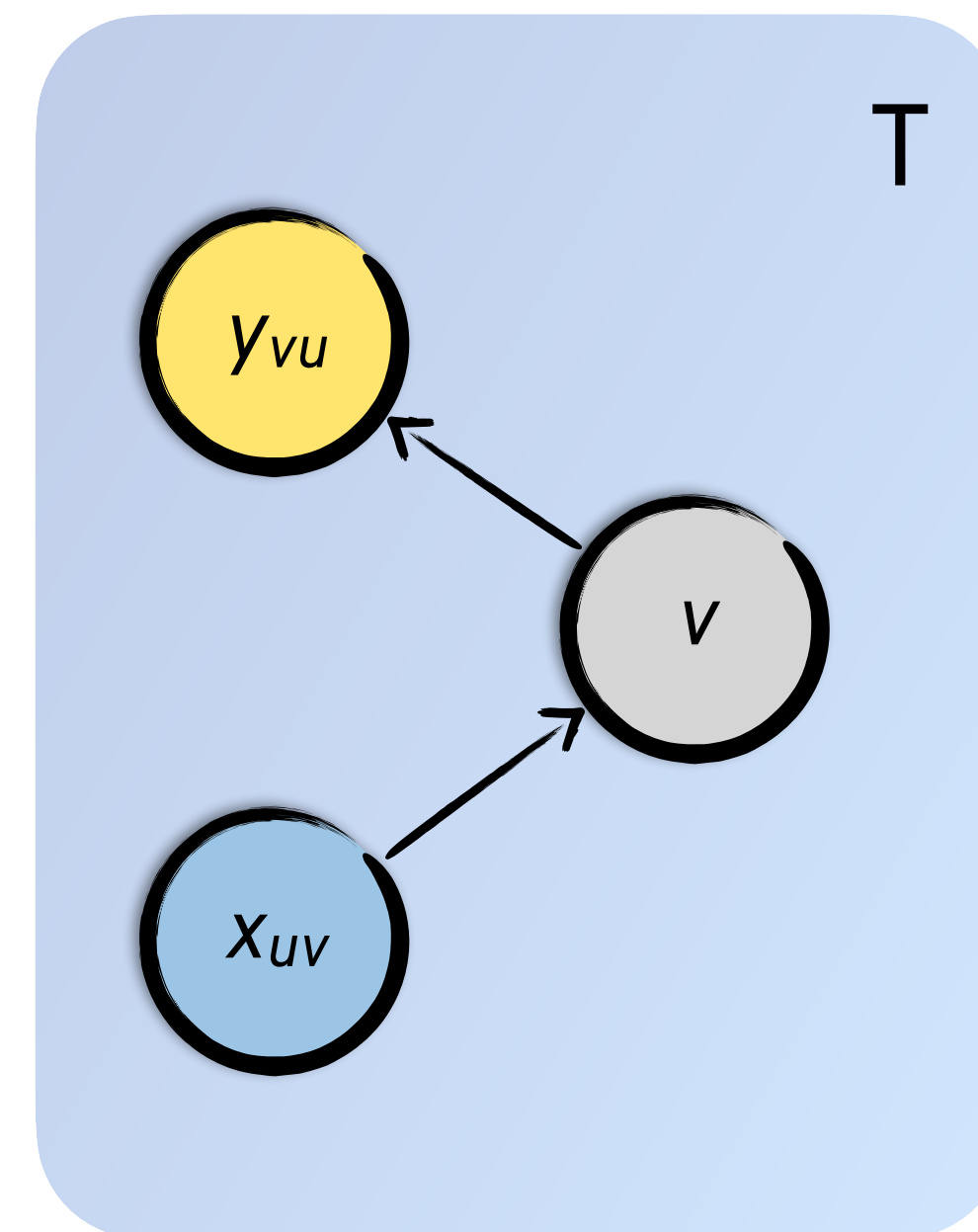
Checking an Interface

Networks with Multiple Solutions

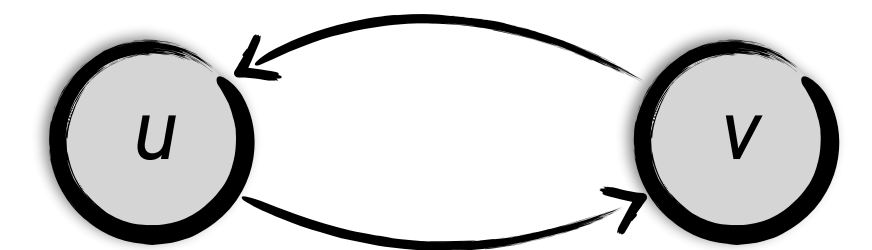
$$\begin{aligned} (v, u, a = \infty) \\ (u, v, a = \infty) \end{aligned}$$



$$P_S = \text{true}$$



$$P_T = \text{true}$$

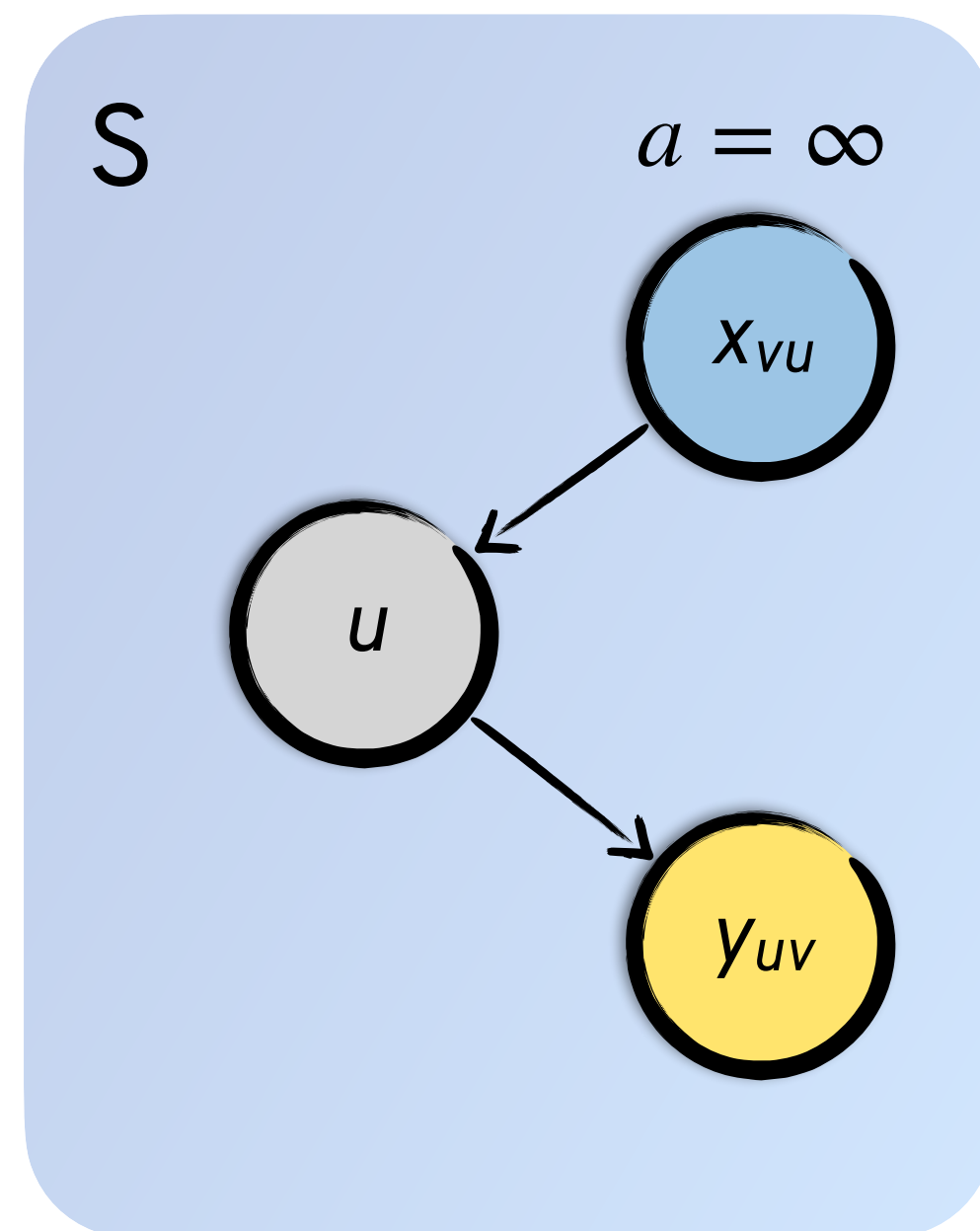


$$\begin{aligned} \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\ &= 1 \\ &= 2 \\ &\dots \\ &= \infty \end{aligned}$$

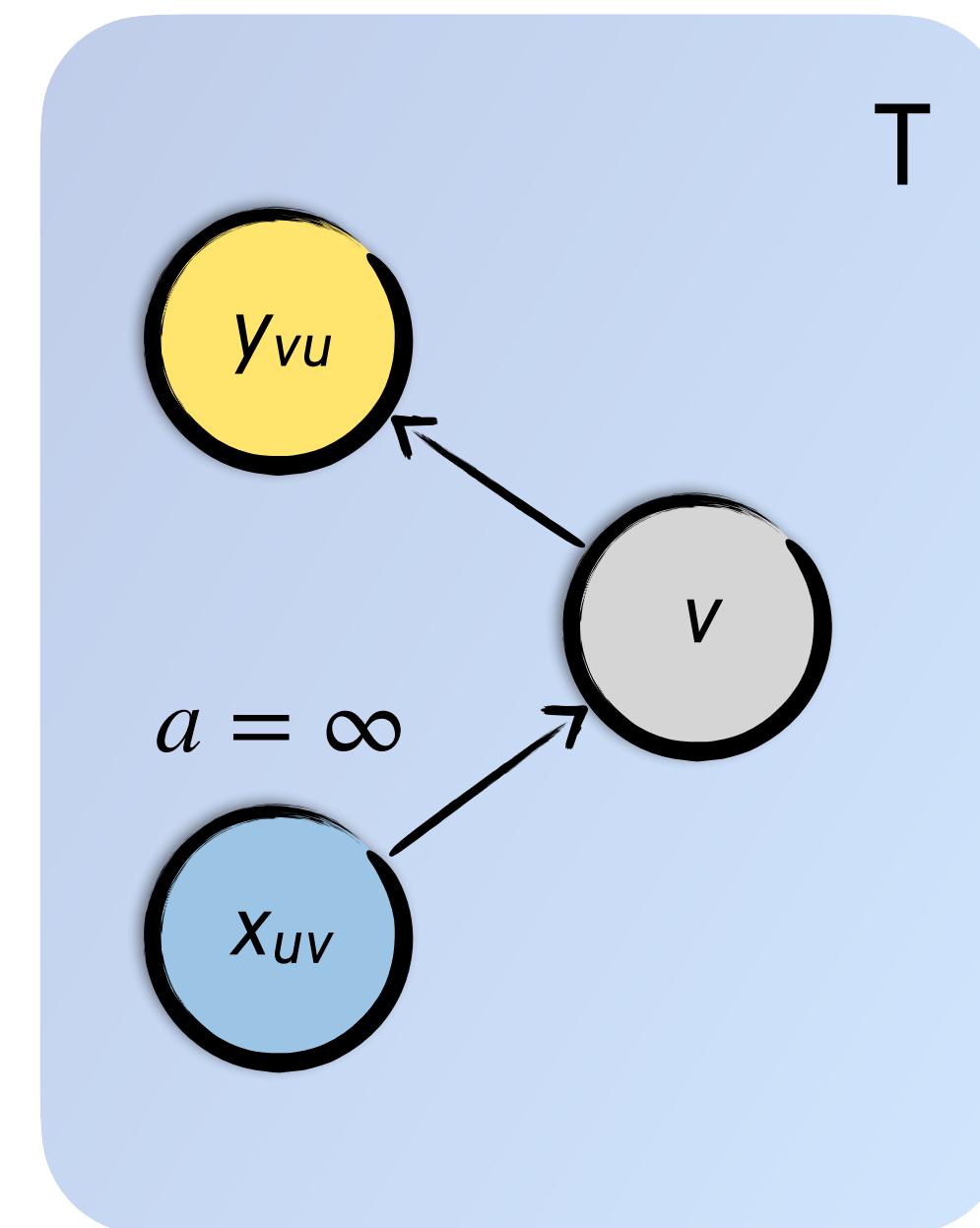
Checking an Interface

Networks with Multiple Solutions

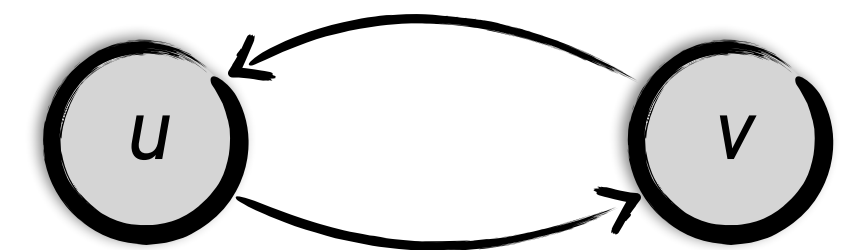
$(v, u, a = \infty)$
 $(u, v, a = \infty)$



$P_S = \text{true}$



$P_T = \text{true}$

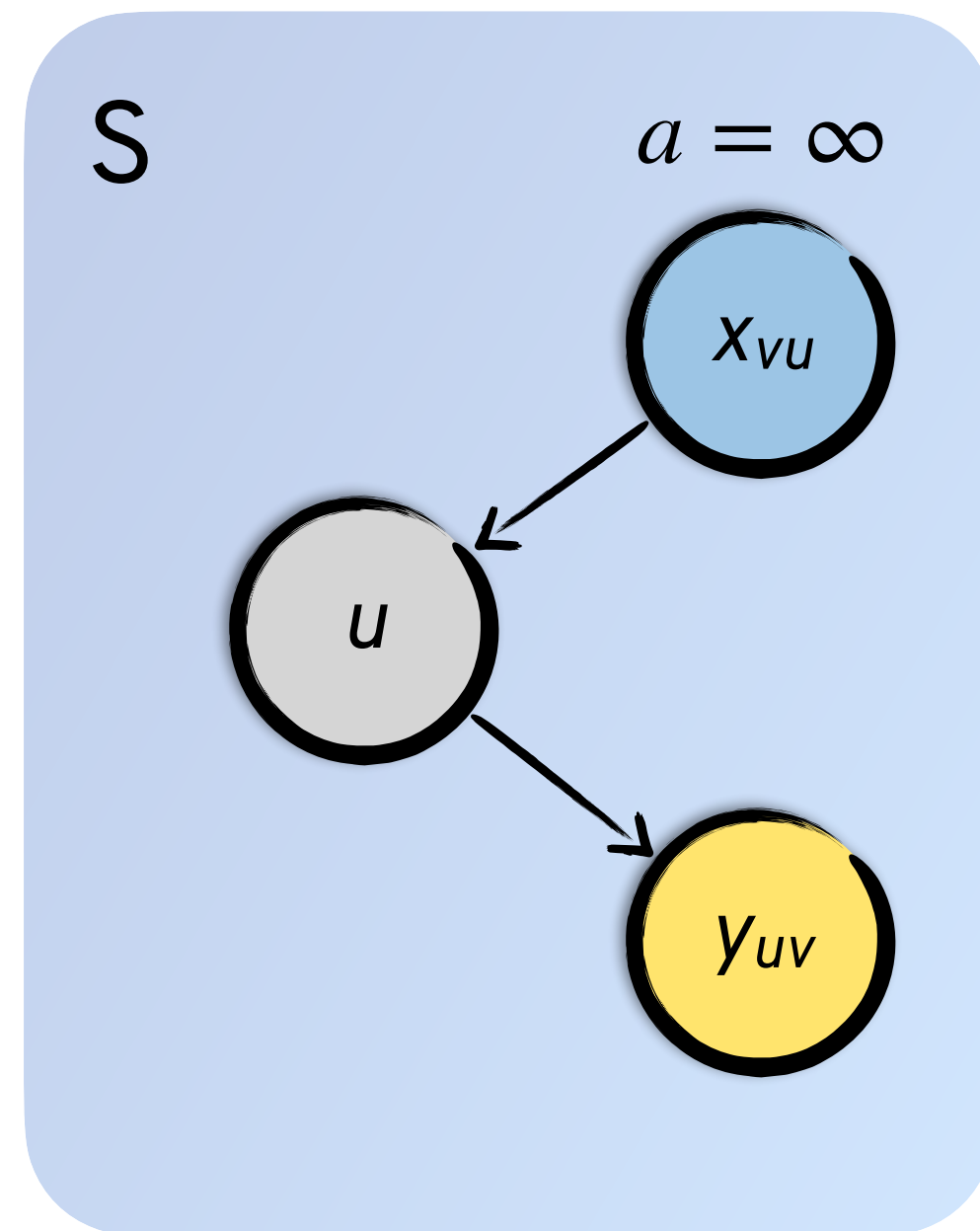


$$\begin{aligned}
 \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\
 &= 1 \\
 &= 2 \\
 &\dots \\
 &= \infty
 \end{aligned}$$

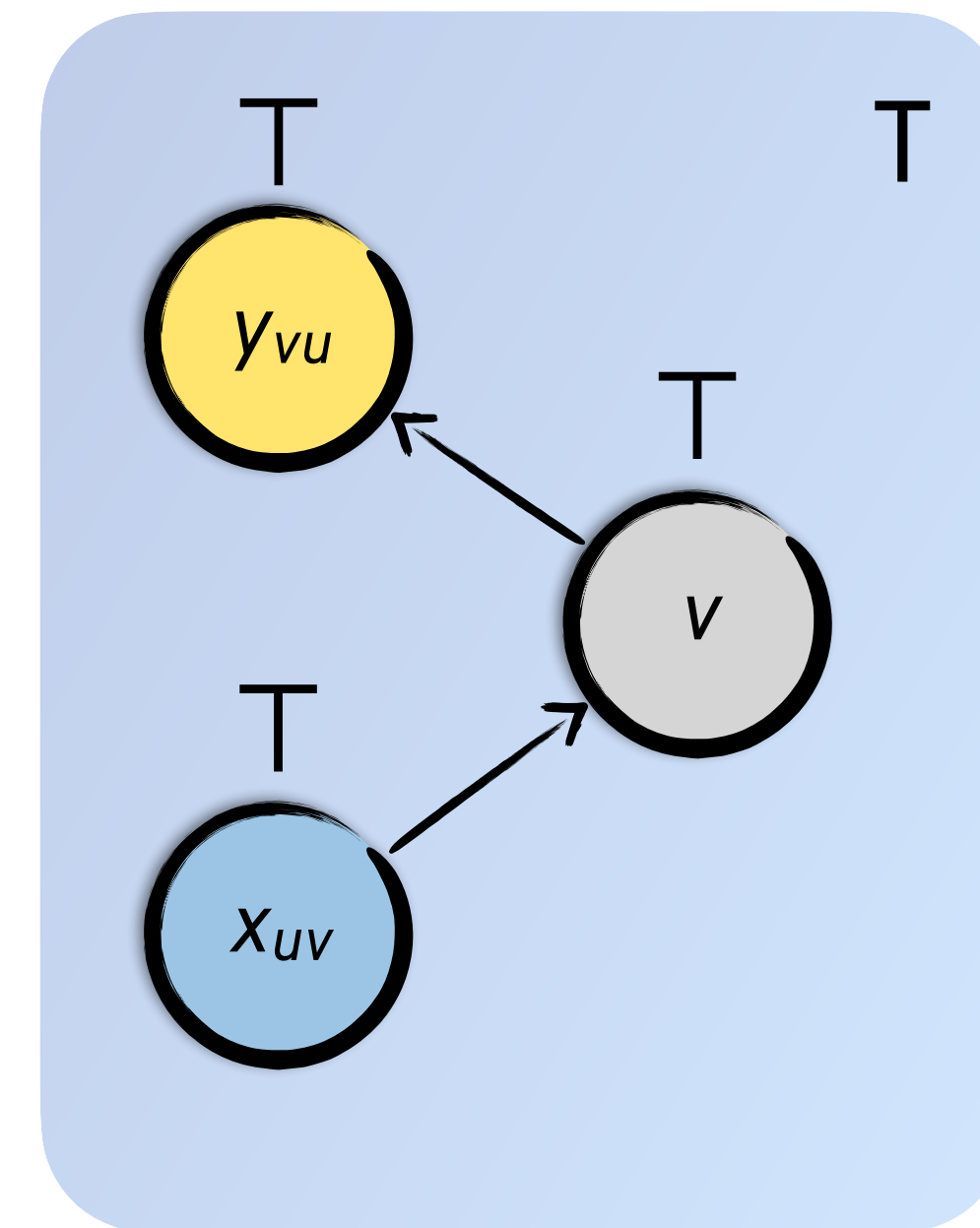
Checking an Interface

Networks with Multiple Solutions

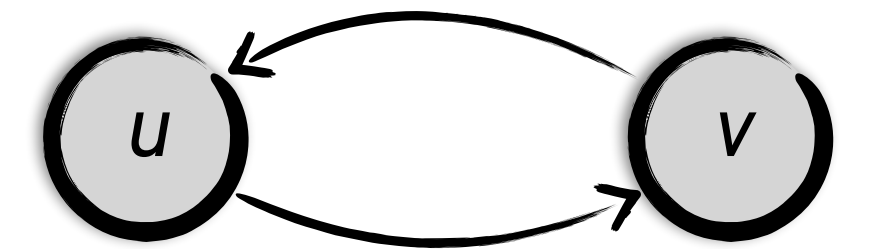
$$\begin{aligned} (v, u, a = \infty) \\ (u, v, a = \infty) \end{aligned}$$



$$P_S = \text{true}$$



$$P_T = \text{true}$$

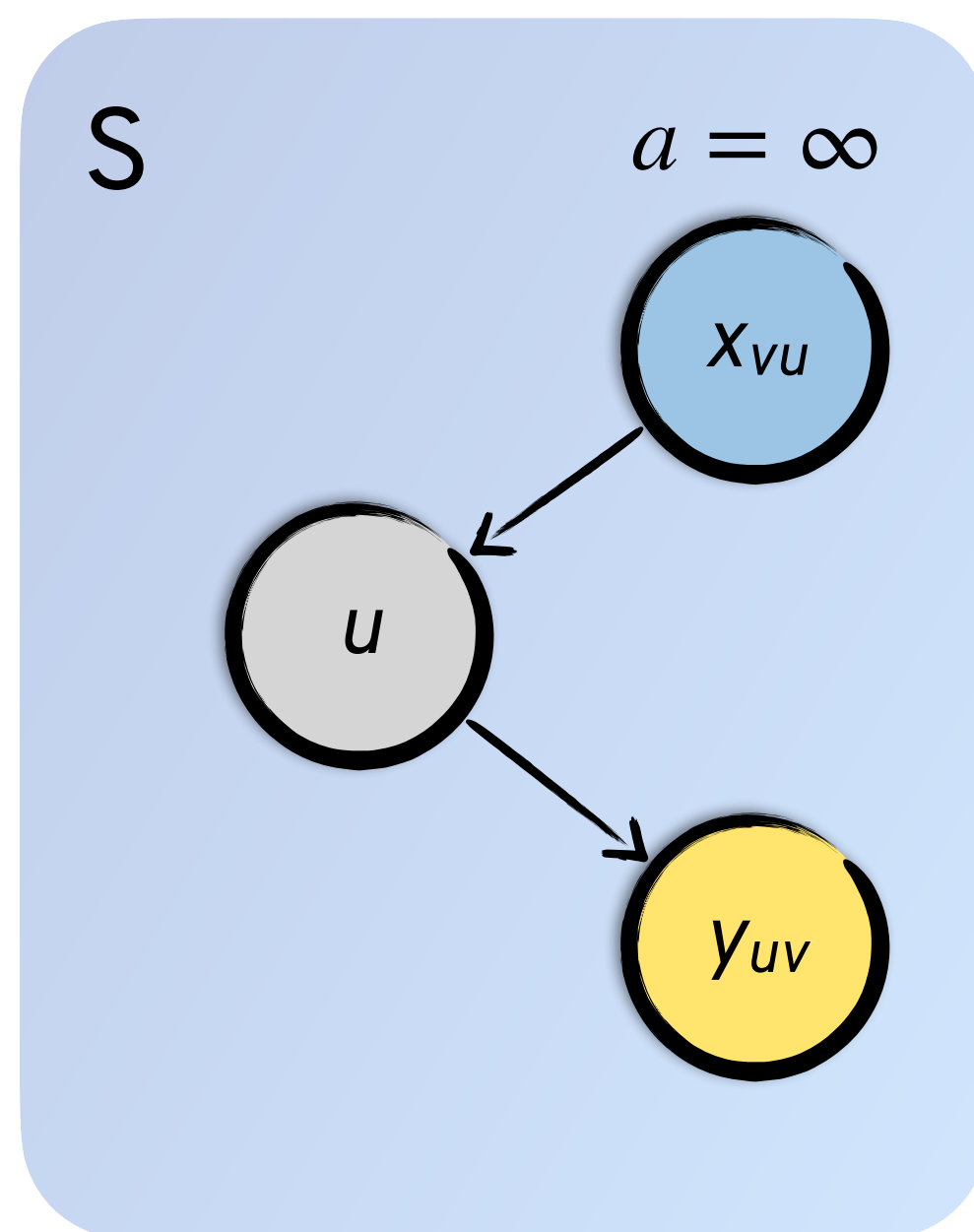


$$\begin{aligned} \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\ &= 1 \\ &= 2 \\ &\dots \\ &= \infty \end{aligned}$$

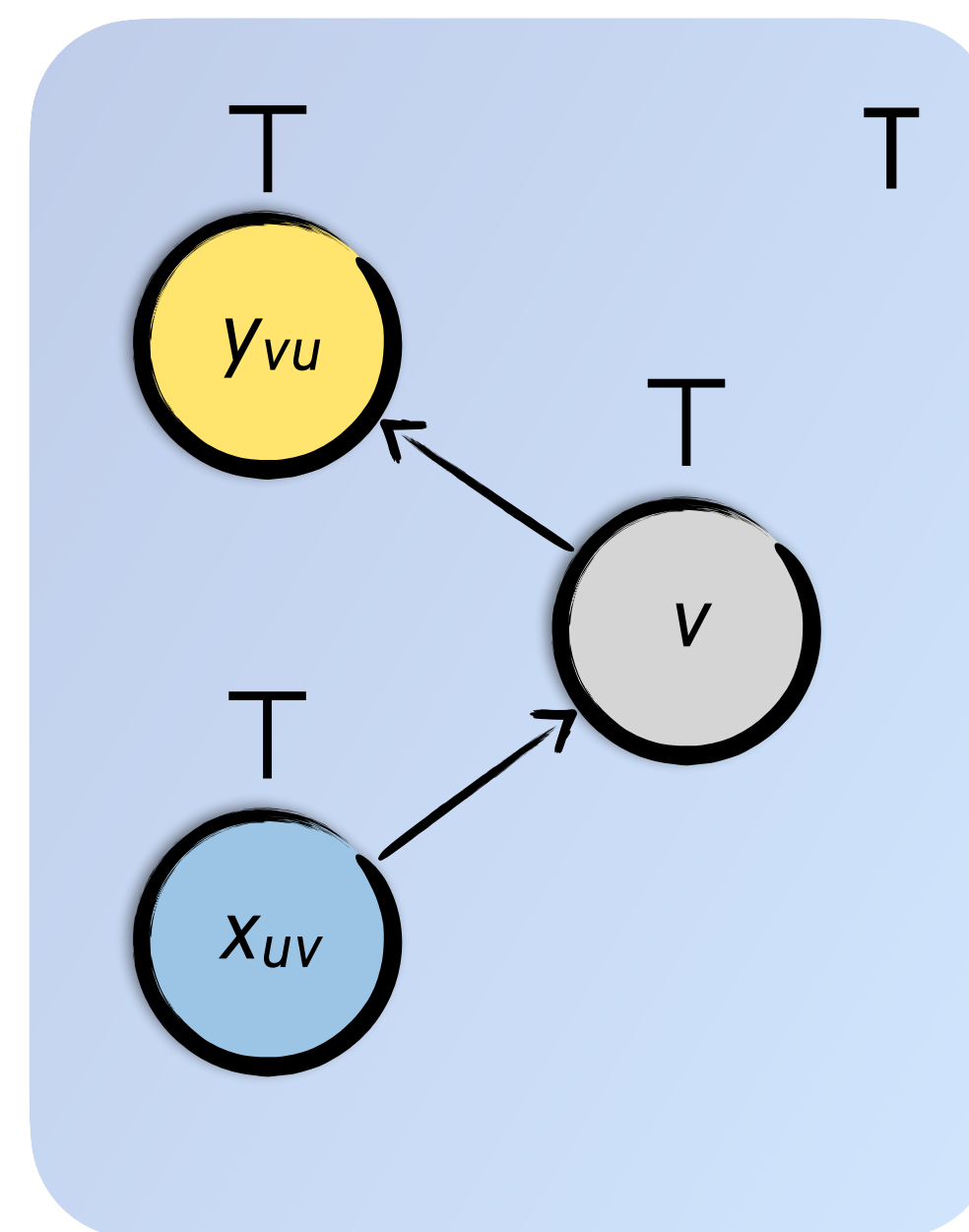
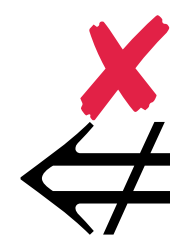
Checking an Interface

Networks with Multiple Solutions

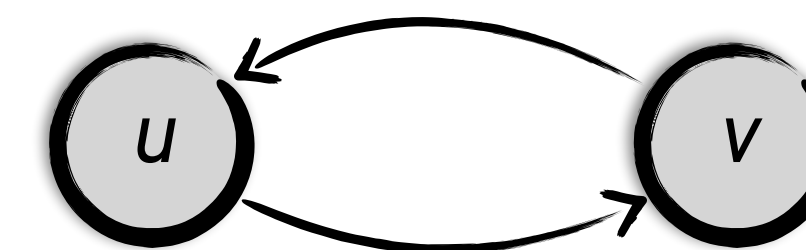
$(v, u, a = \infty)$
 $(u, v, a = \infty)$



$P_S = \text{true}$



$P_T = \text{true}$

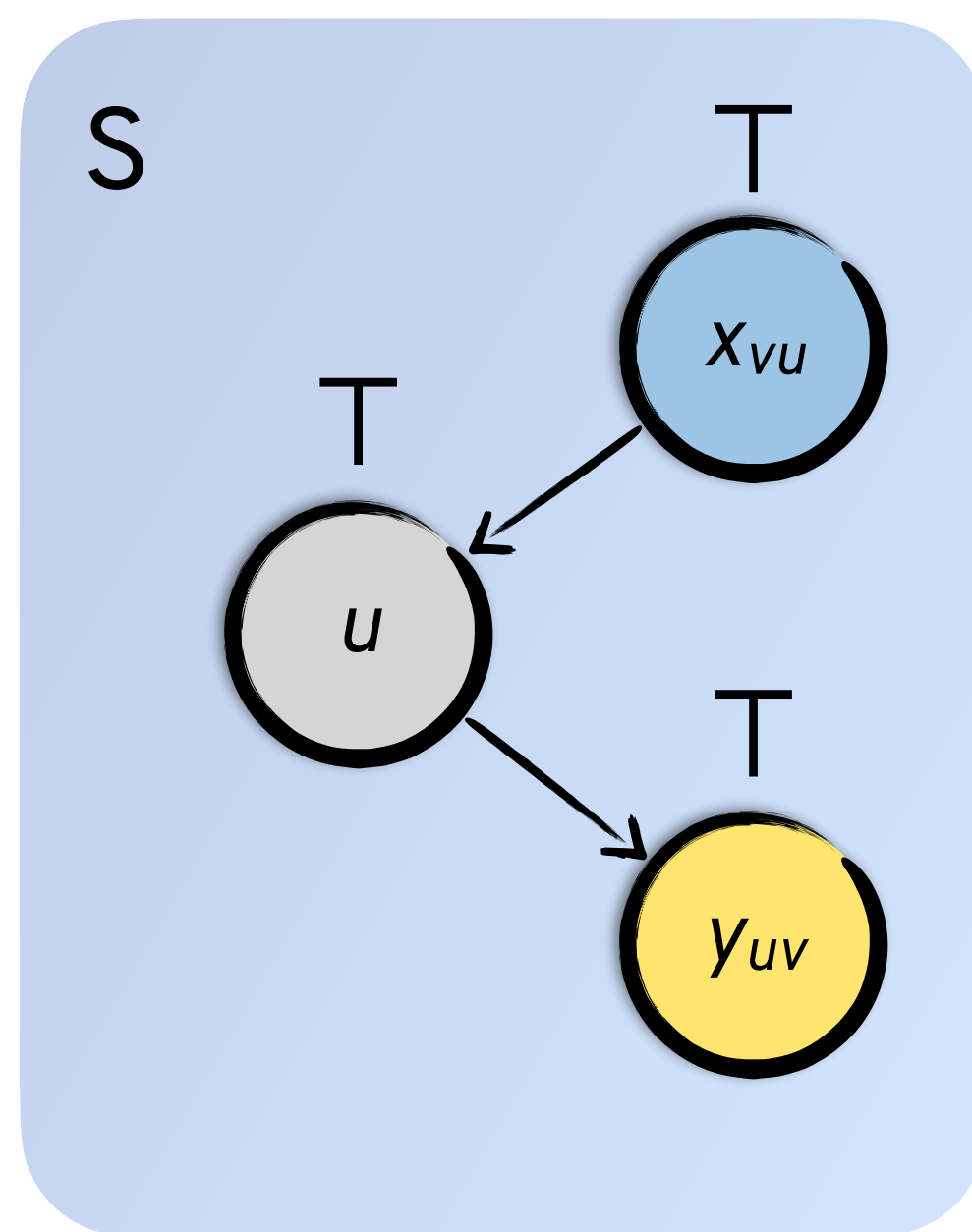


$$\begin{aligned}
 \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\
 &= 1 \\
 &= 2 \\
 &\dots \\
 &= \infty
 \end{aligned}$$

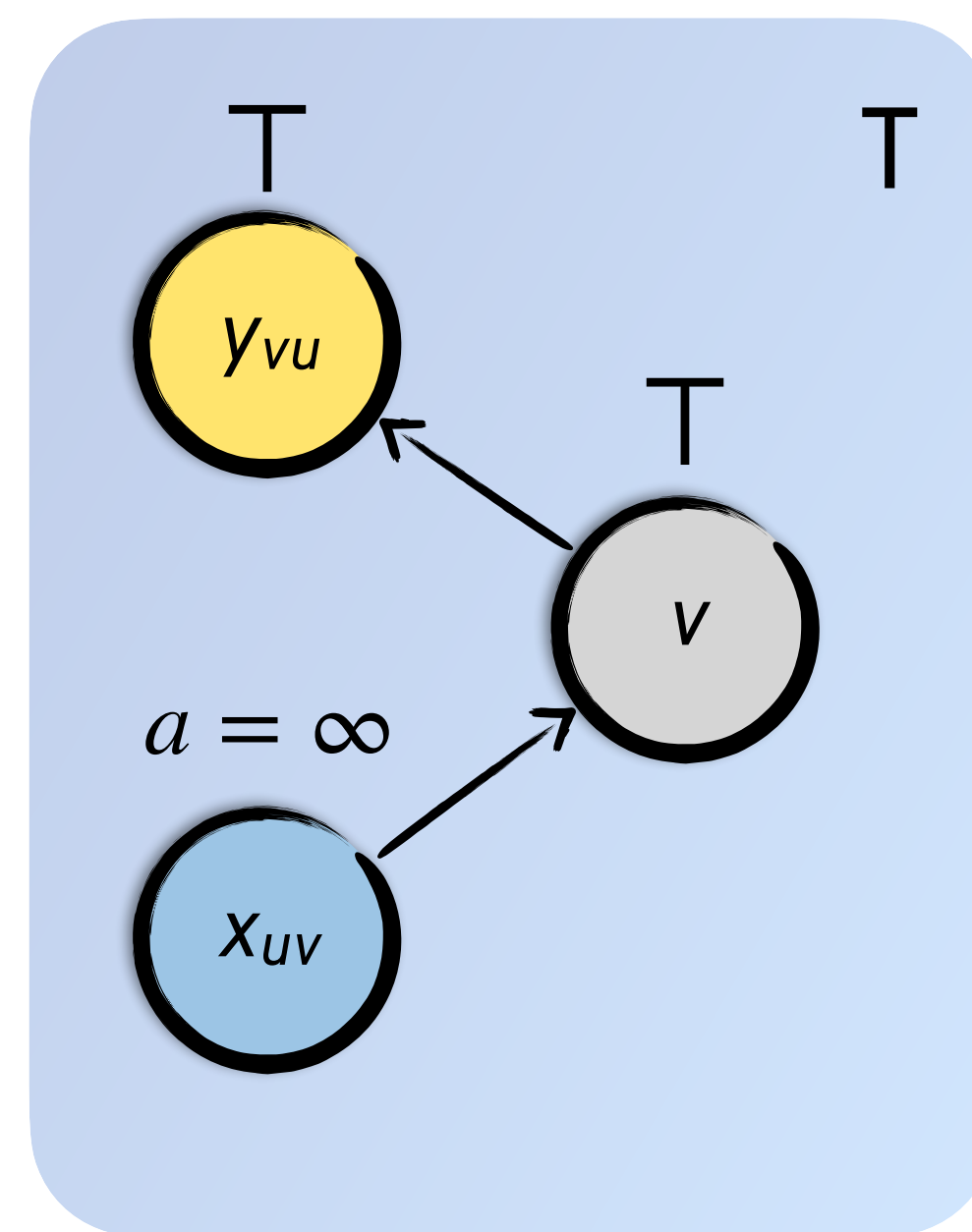
Checking an Interface

Networks with Multiple Solutions

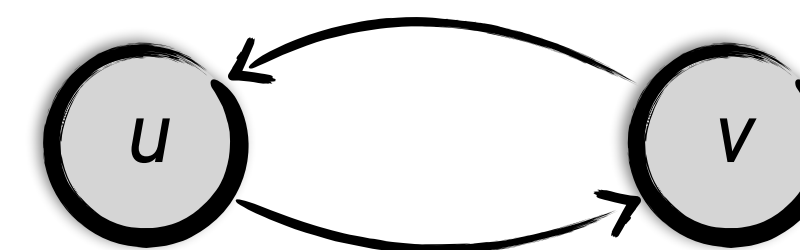
$$\begin{aligned} (v, u, a = \infty) \\ (u, v, a = \infty) \end{aligned}$$



$$P_S = \text{true}$$



$$P_T = \text{true}$$

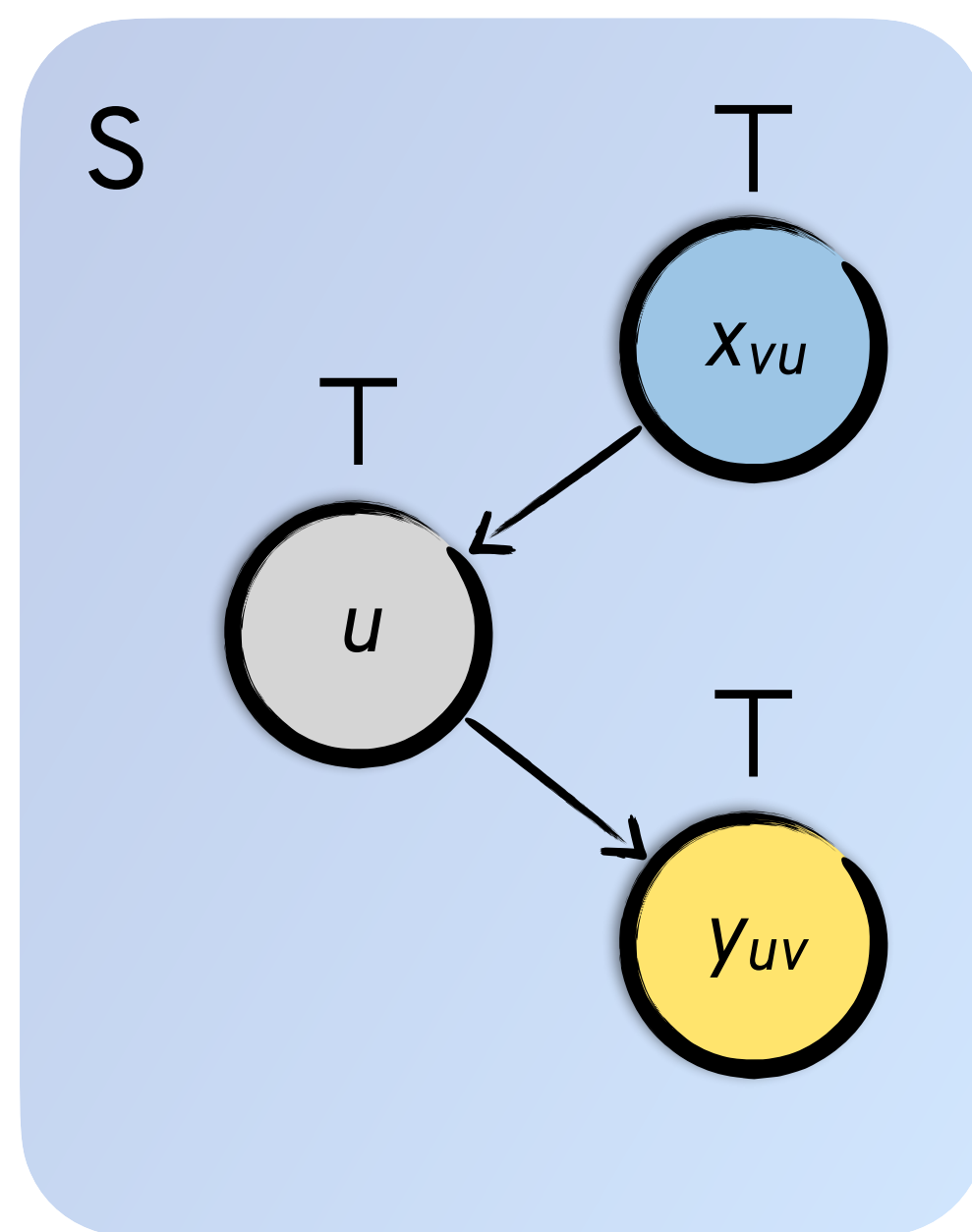


$$\begin{aligned} \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\ &= 1 \\ &= 2 \\ &\dots \\ &= \infty \end{aligned}$$

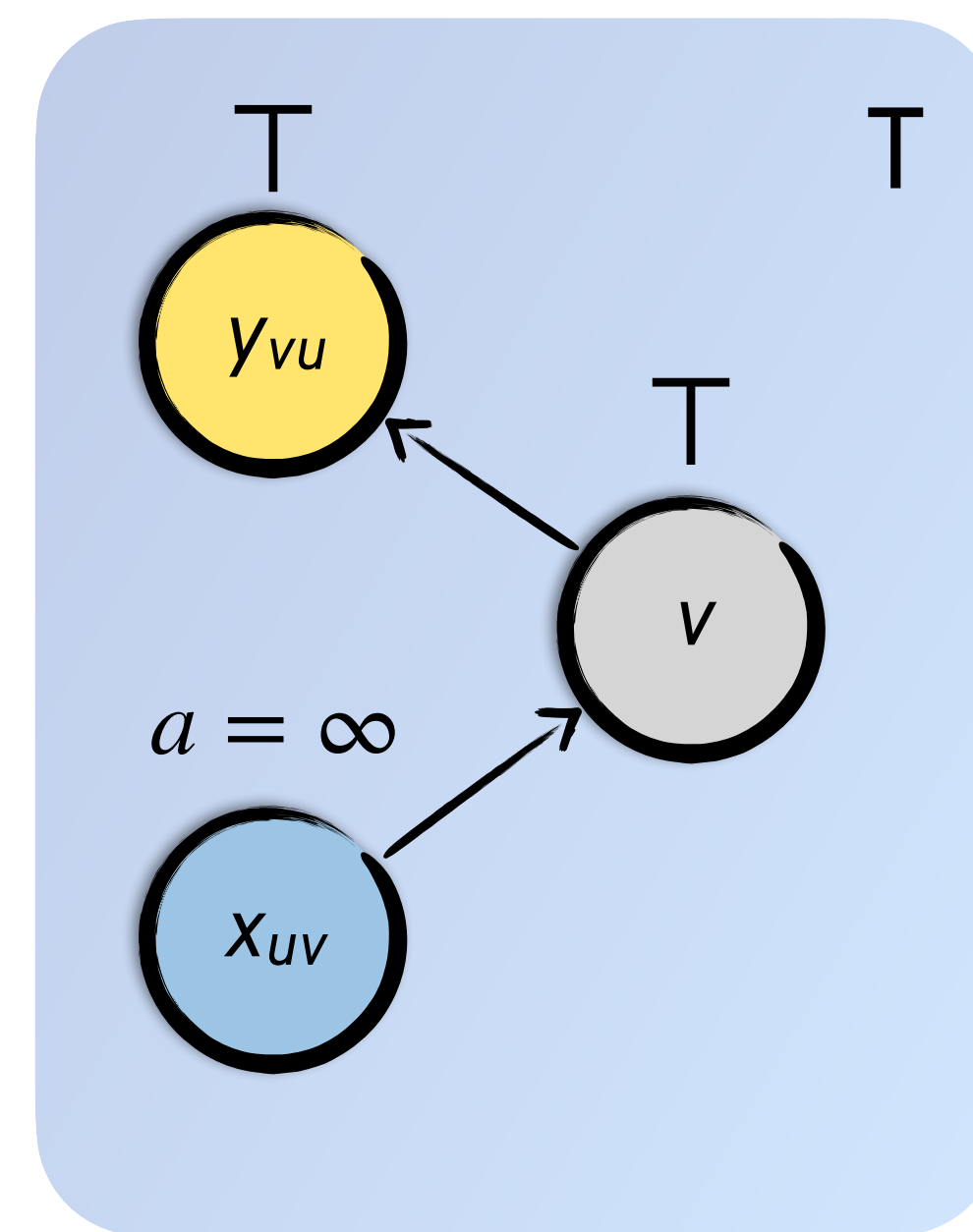
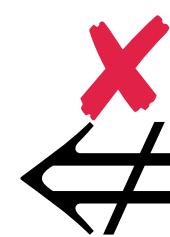
Checking an Interface

Networks with Multiple Solutions

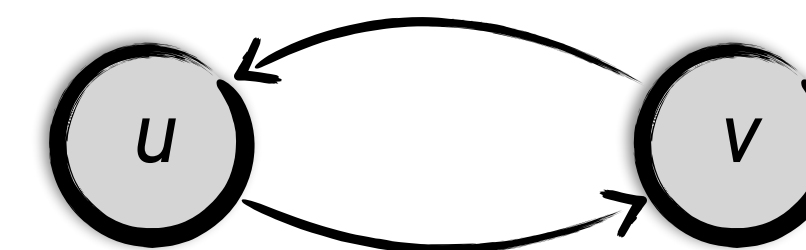
$$\begin{aligned} (v, u, a = \infty) \\ (u, v, a = \infty) \end{aligned}$$



$$P_S = \text{true}$$



$$P_T = \text{true}$$

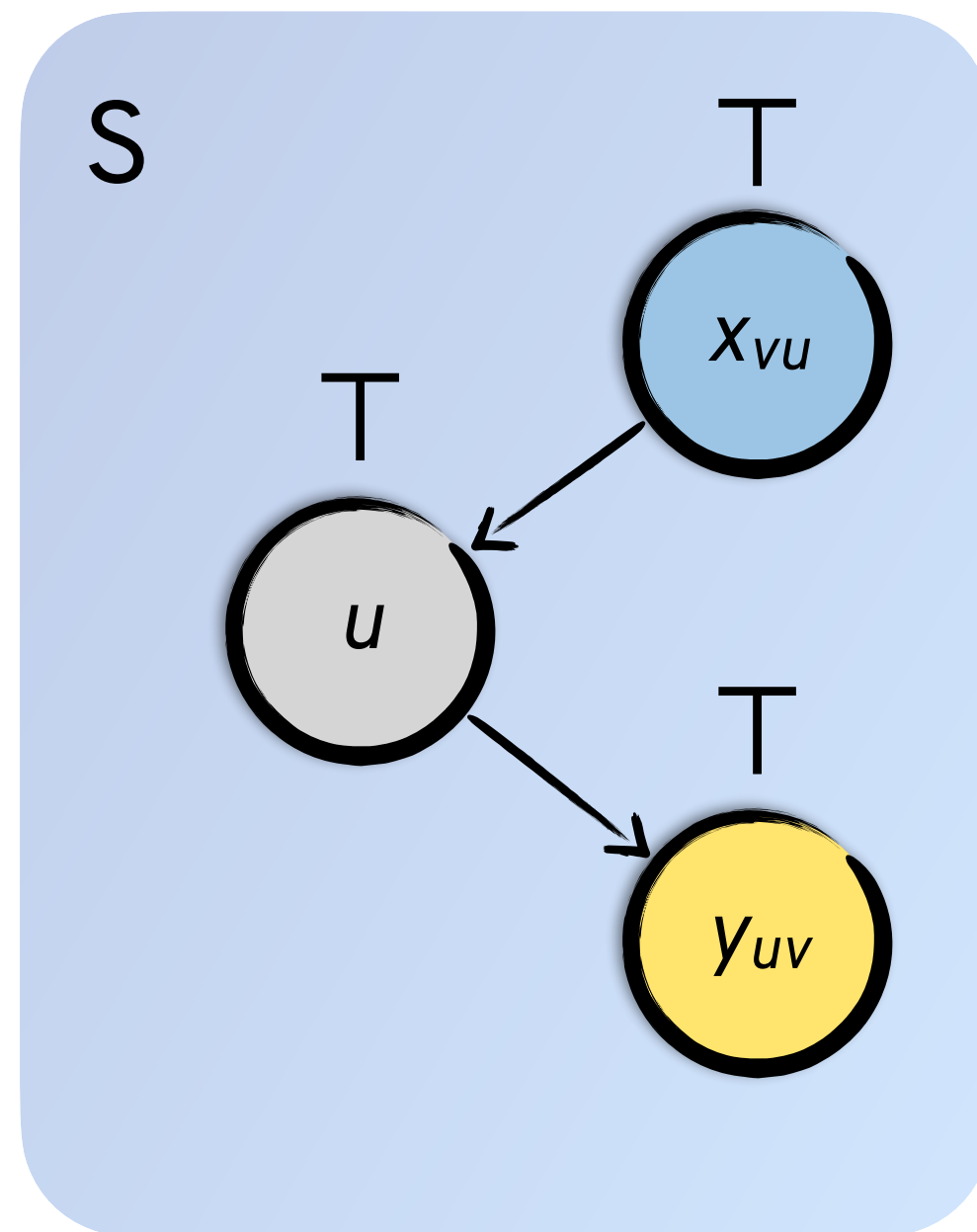


$$\begin{aligned} \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\ &= 1 \\ &= 2 \\ &\dots \\ &= \infty \end{aligned}$$

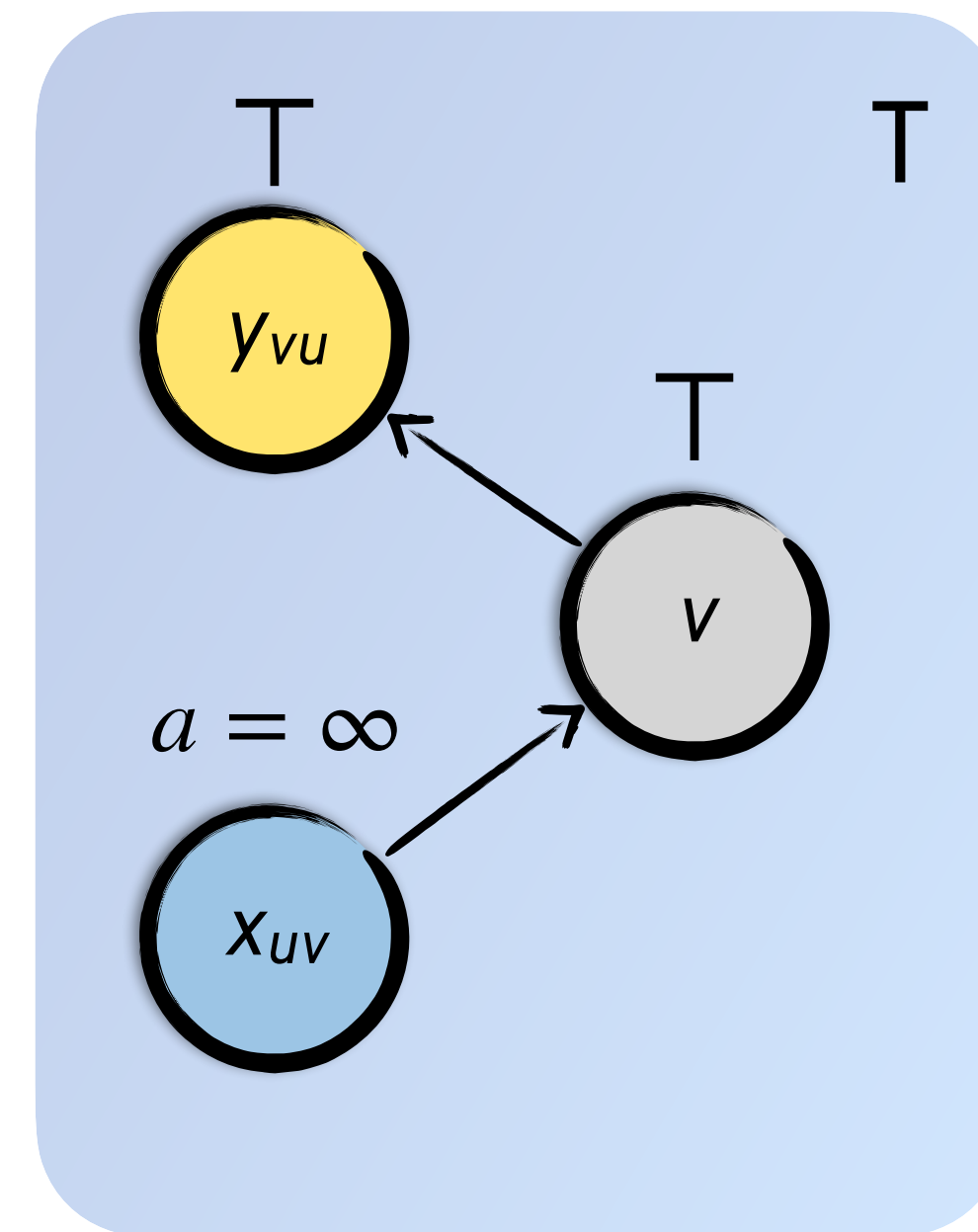
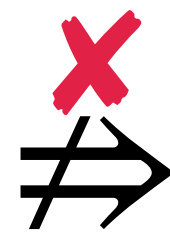
Checking an Interface

Networks with Multiple Solutions

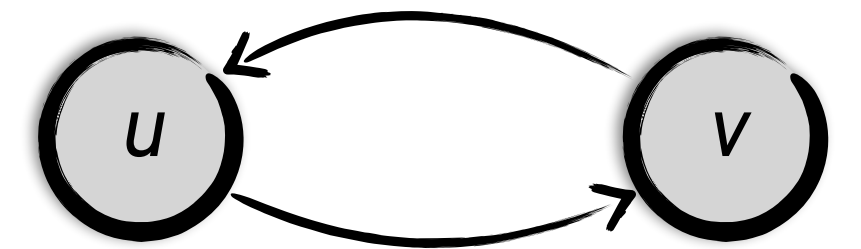
(v, u, true)
 (u, v, true)



$P_S = \text{true}$



$P_T = \text{true}$

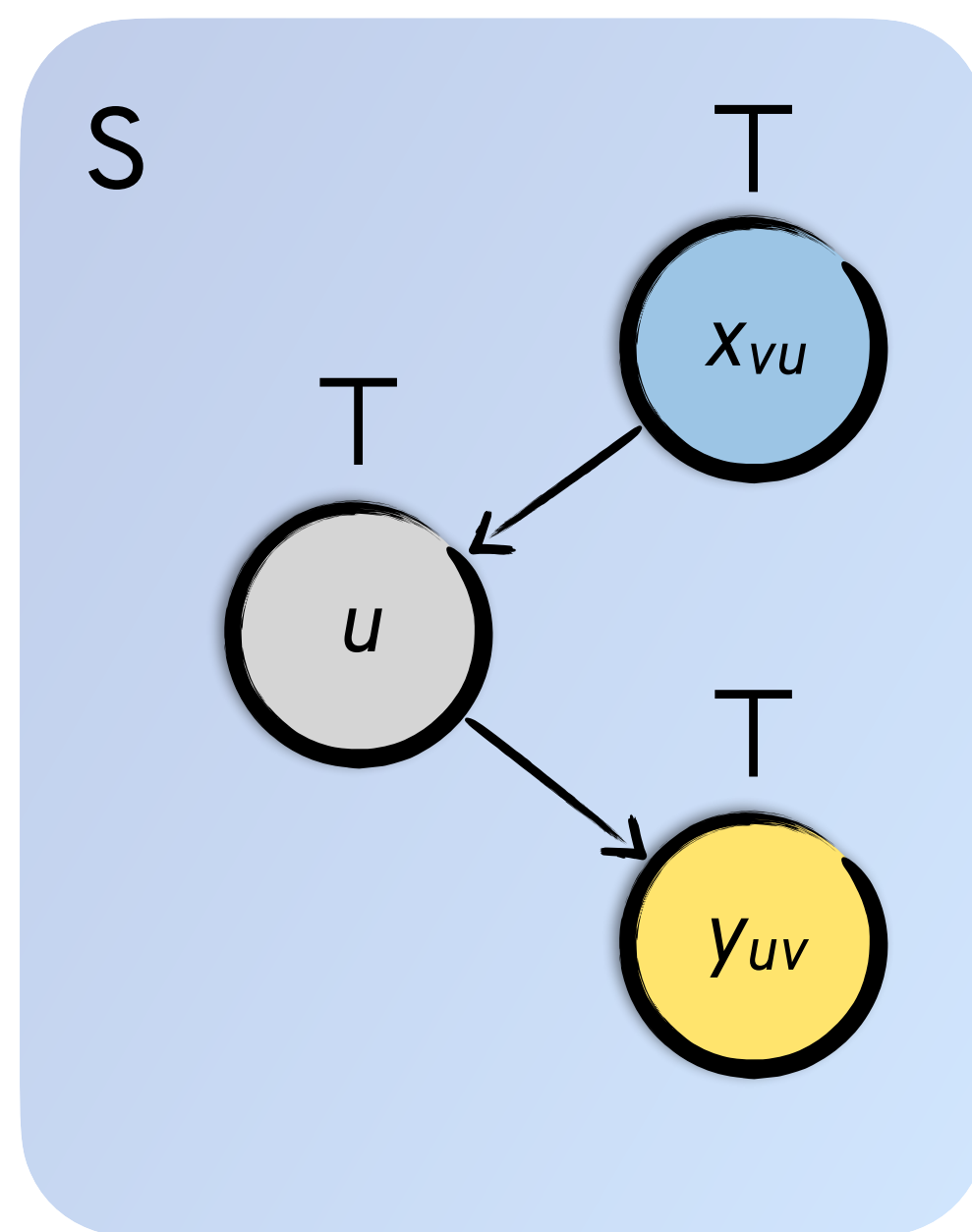


$$\begin{aligned} \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\ &= 1 \\ &= 2 \\ &\dots \\ &= \infty \end{aligned}$$

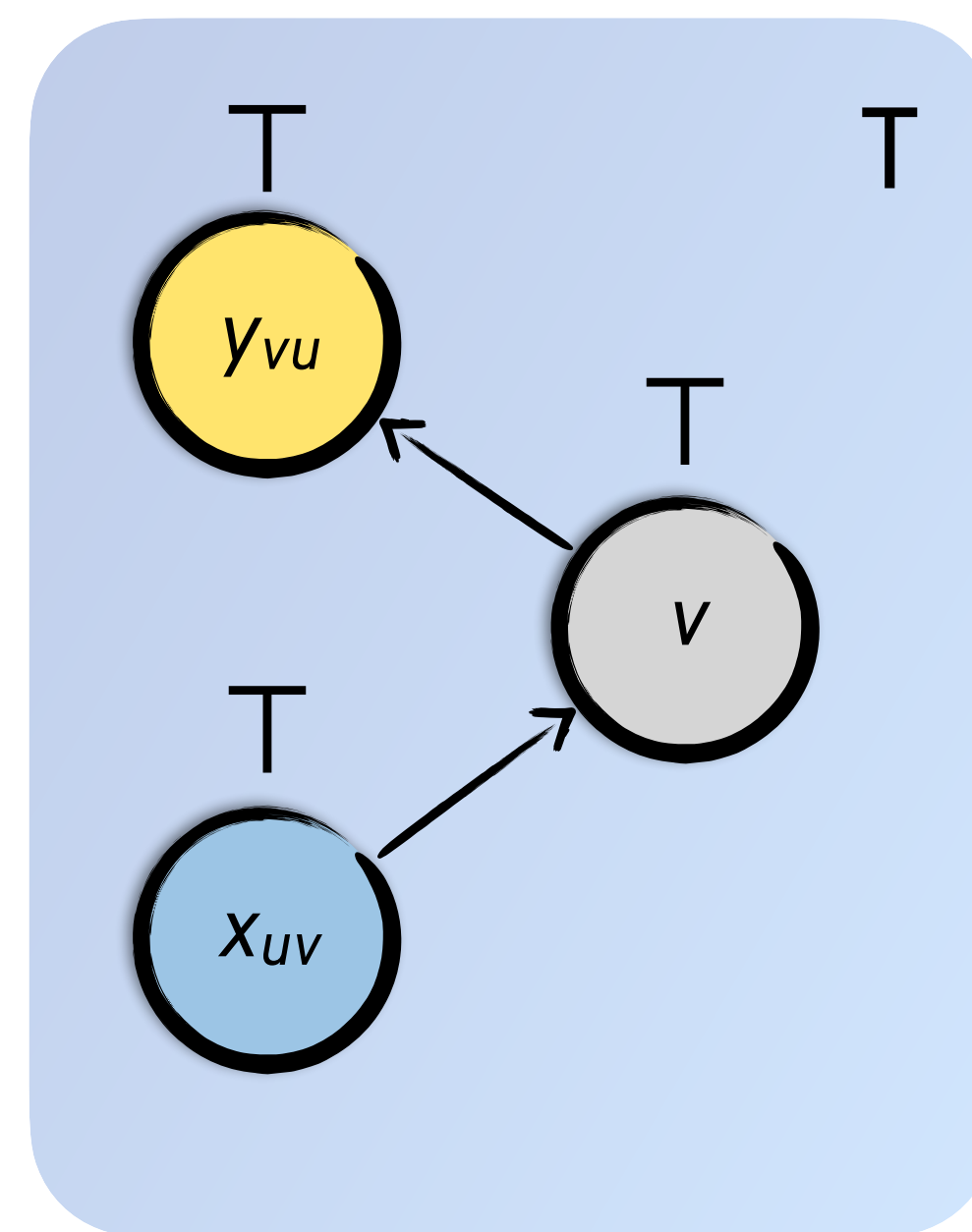
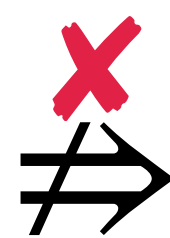
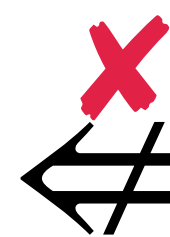
Checking an Interface

Networks with Multiple Solutions

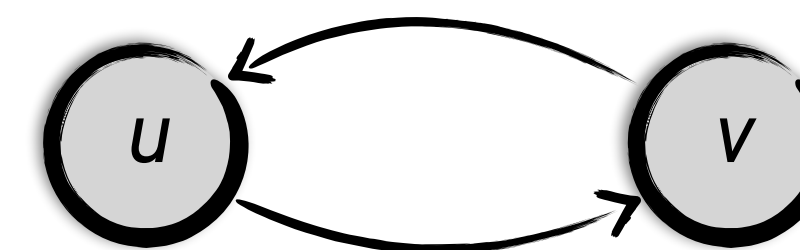
(v, u, true)
 (u, v, true)



$P_S = \text{true}$



$P_T = \text{true}$

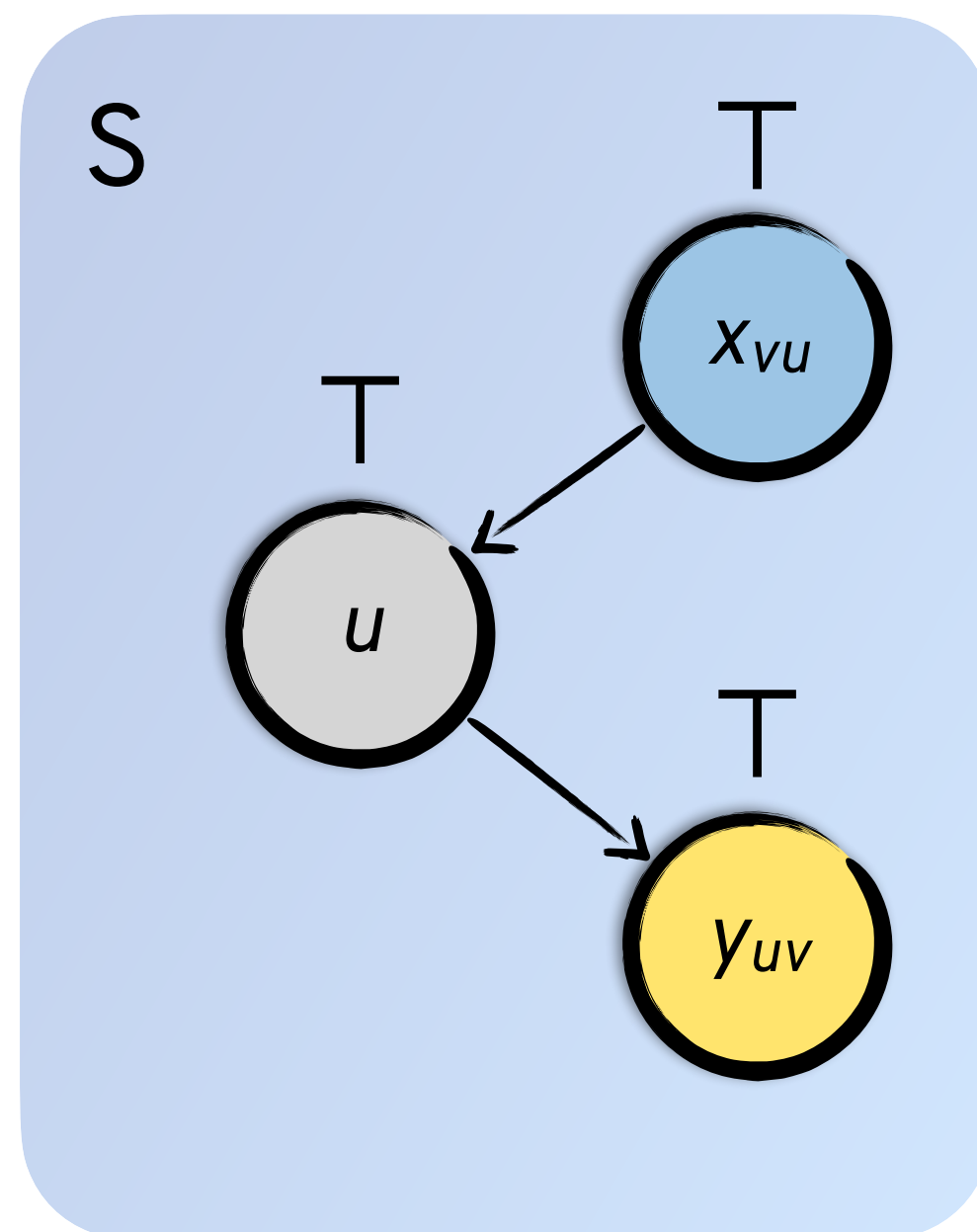


$$\begin{aligned} \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\ &= 1 \\ &= 2 \\ &\dots \\ &= \infty \end{aligned}$$

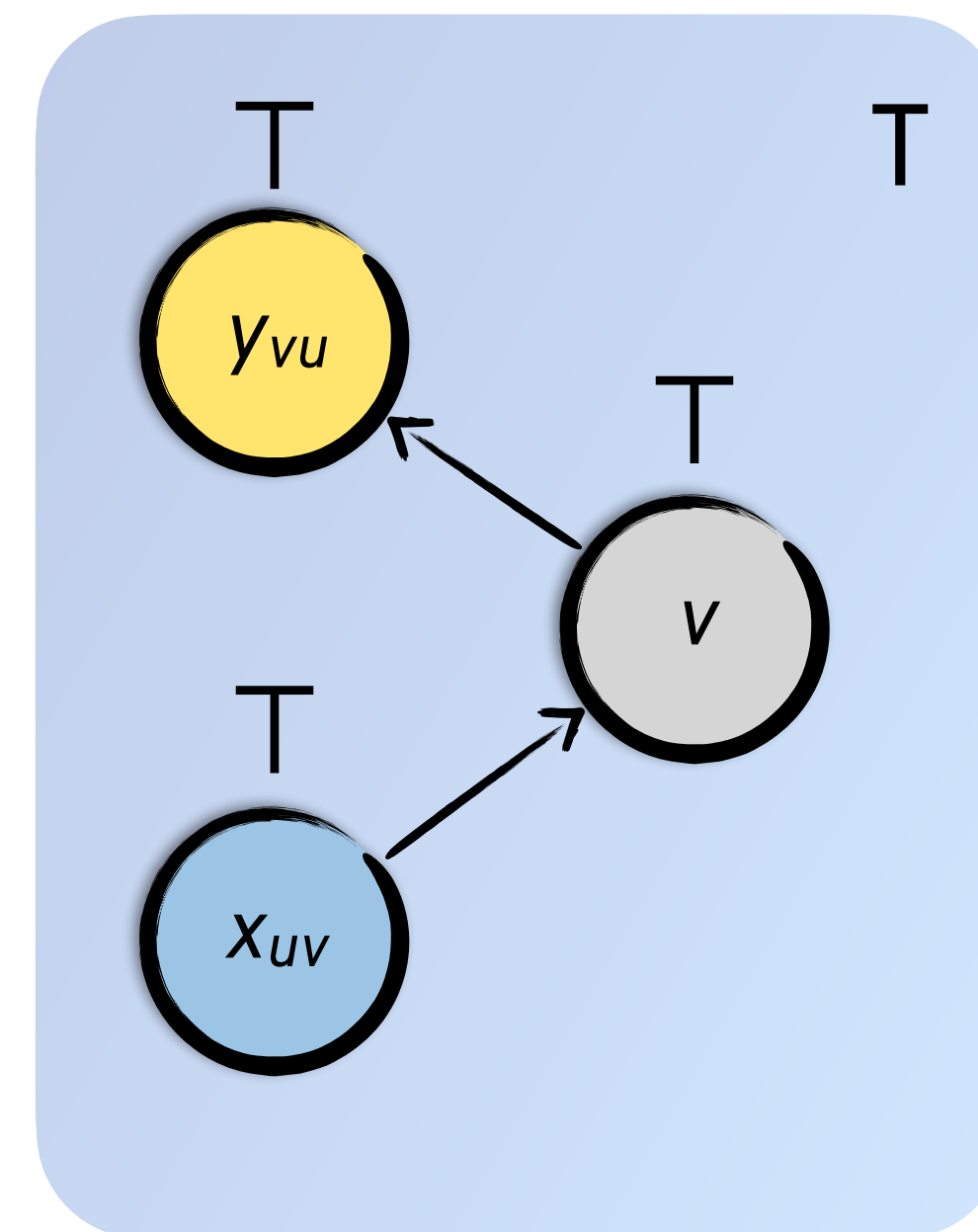
Checking an Interface

Networks with Multiple Solutions

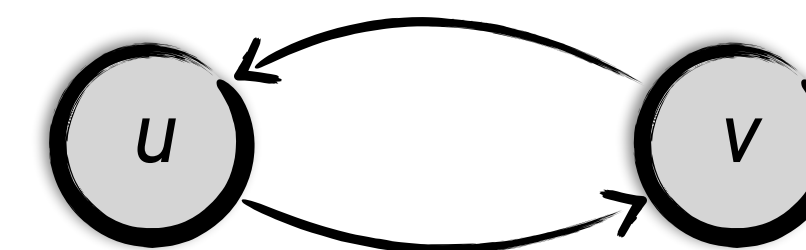
(v, u, true)
 (u, v, true)



$P_S = \text{true}$



$P_T = \text{true}$

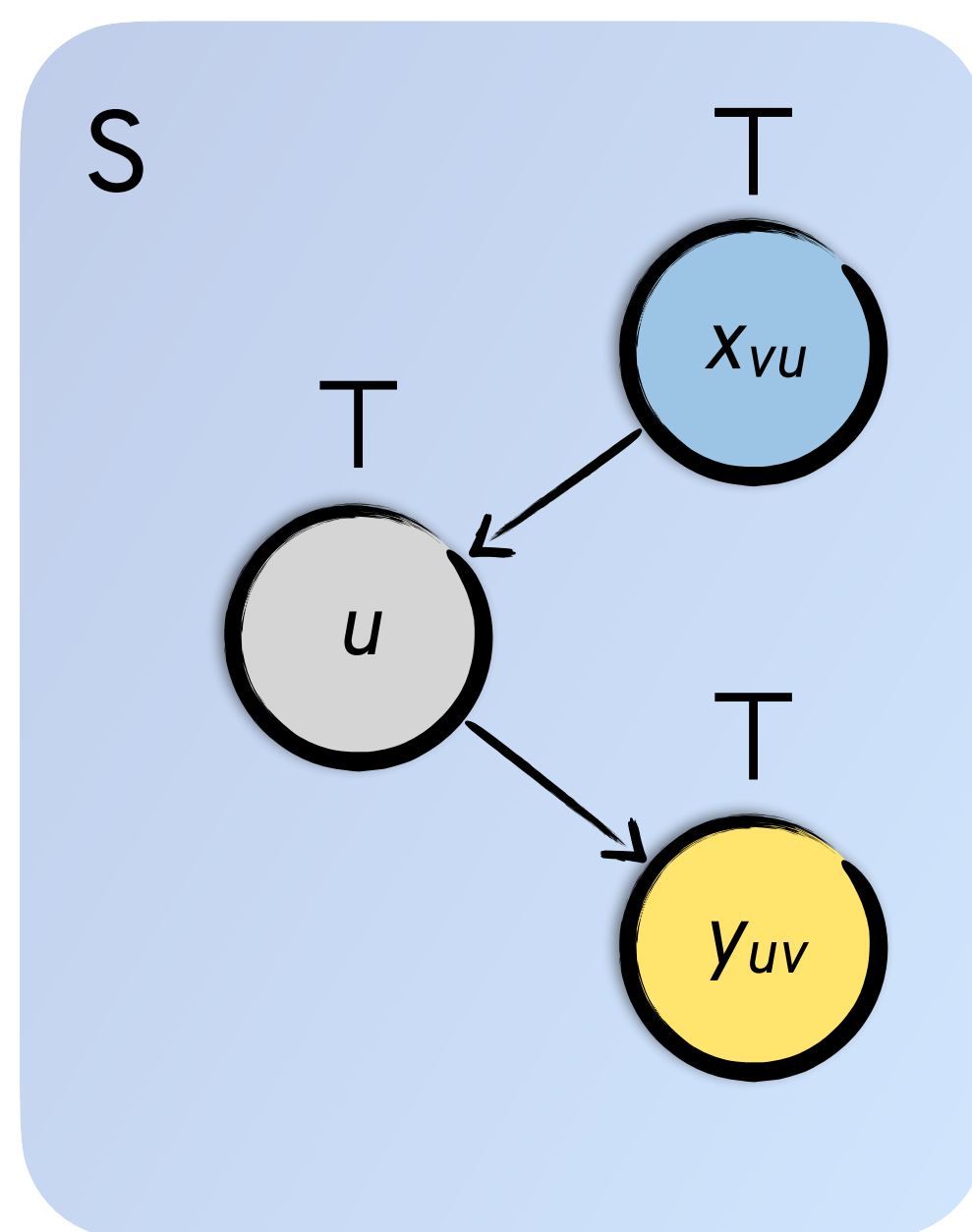


$$\begin{aligned} \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\ &= 1 \\ &= 2 \\ &\dots \\ &= \infty \end{aligned}$$

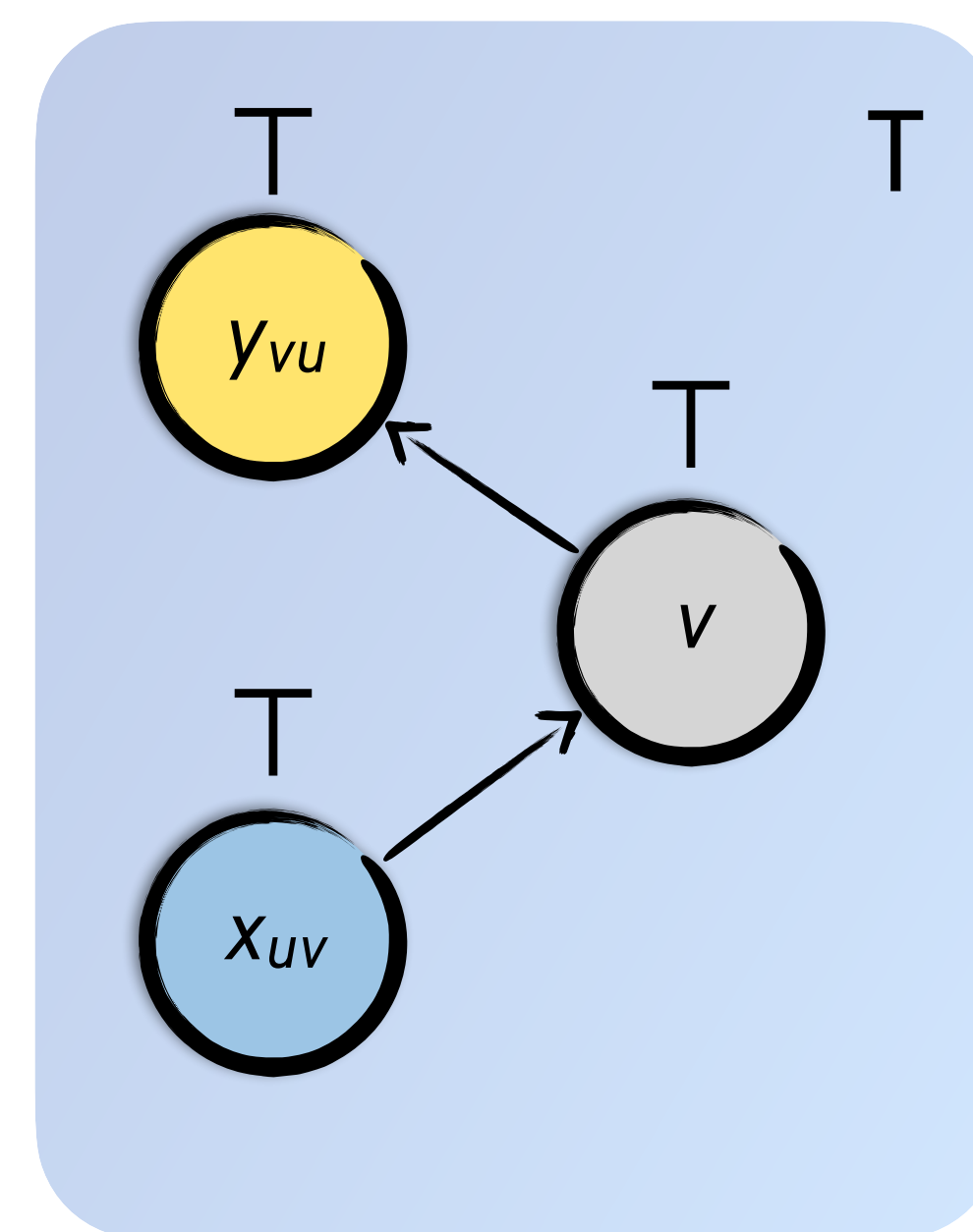
Checking an Interface

Networks with Multiple Solutions

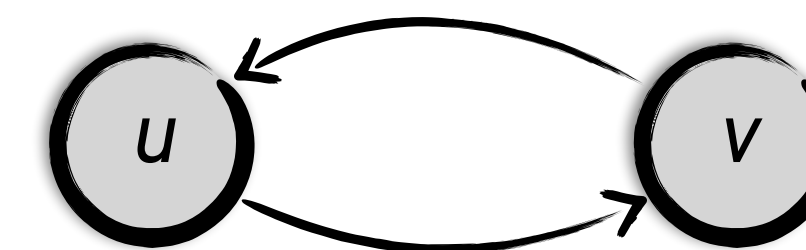
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$$\begin{aligned} \mathcal{L}(u) = \mathcal{L}(v) &= 0 \\ &= 1 \\ &= 2 \\ &\dots \\ &= \infty \end{aligned}$$

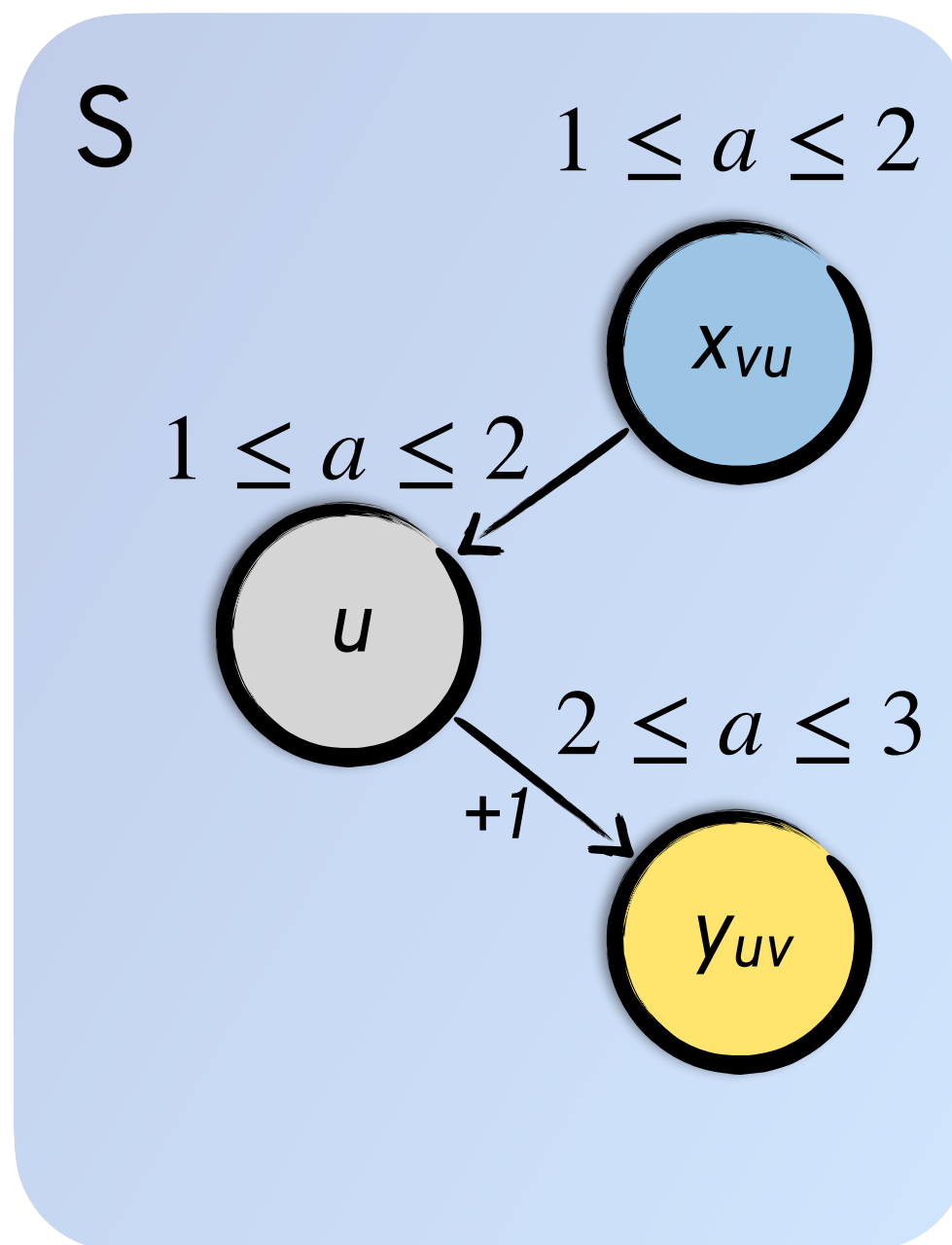
Kirigami Is Sound!

Kirigami Is Sound!

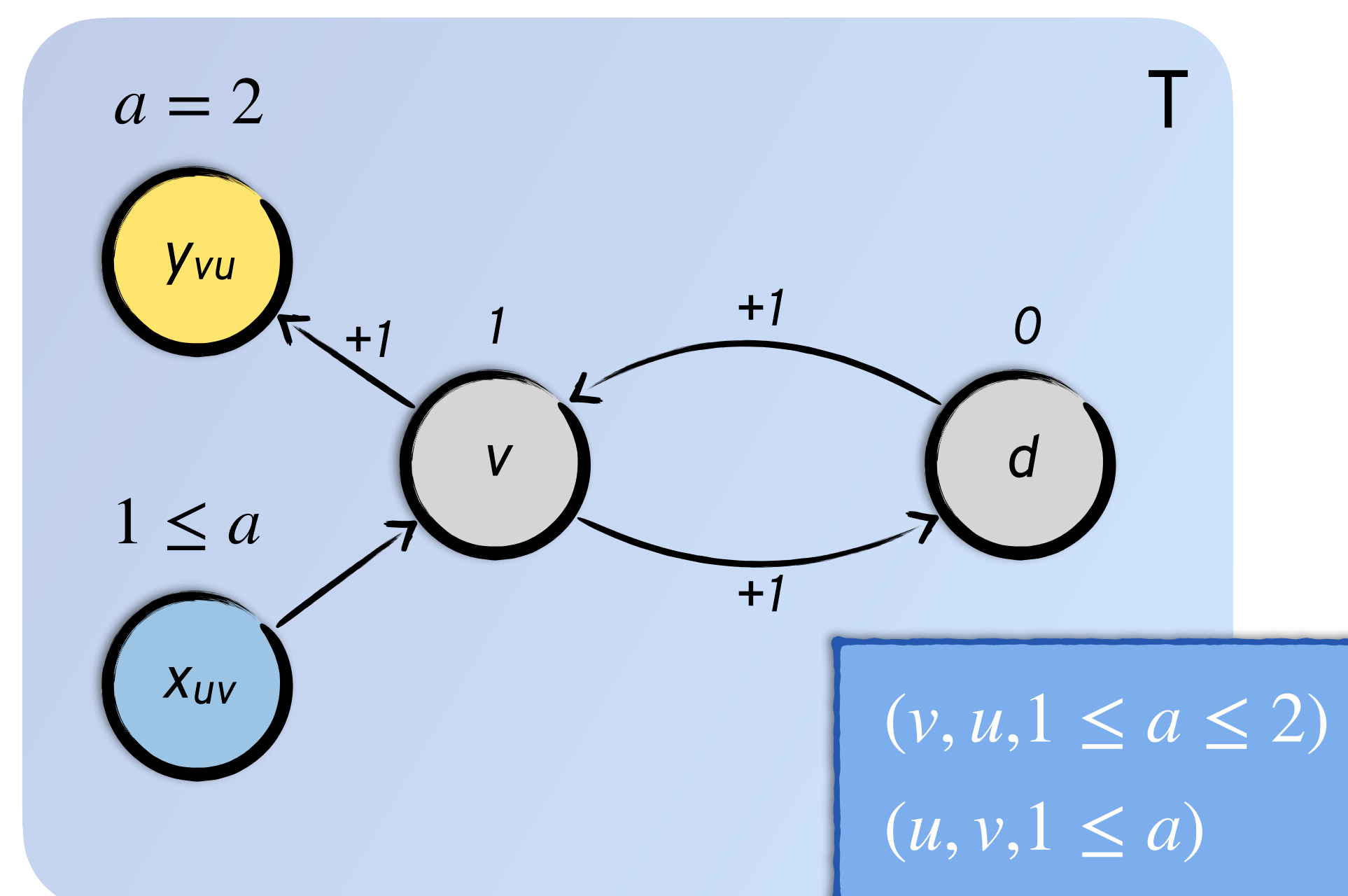
Theorem: if Kirigami returns true, then property P holds for monolithic network R

Kirigami Is Sound!

Theorem: if Kirigami returns true, then property P holds for monolithic network R



$$P_S = \mathcal{L}(u) < 10$$



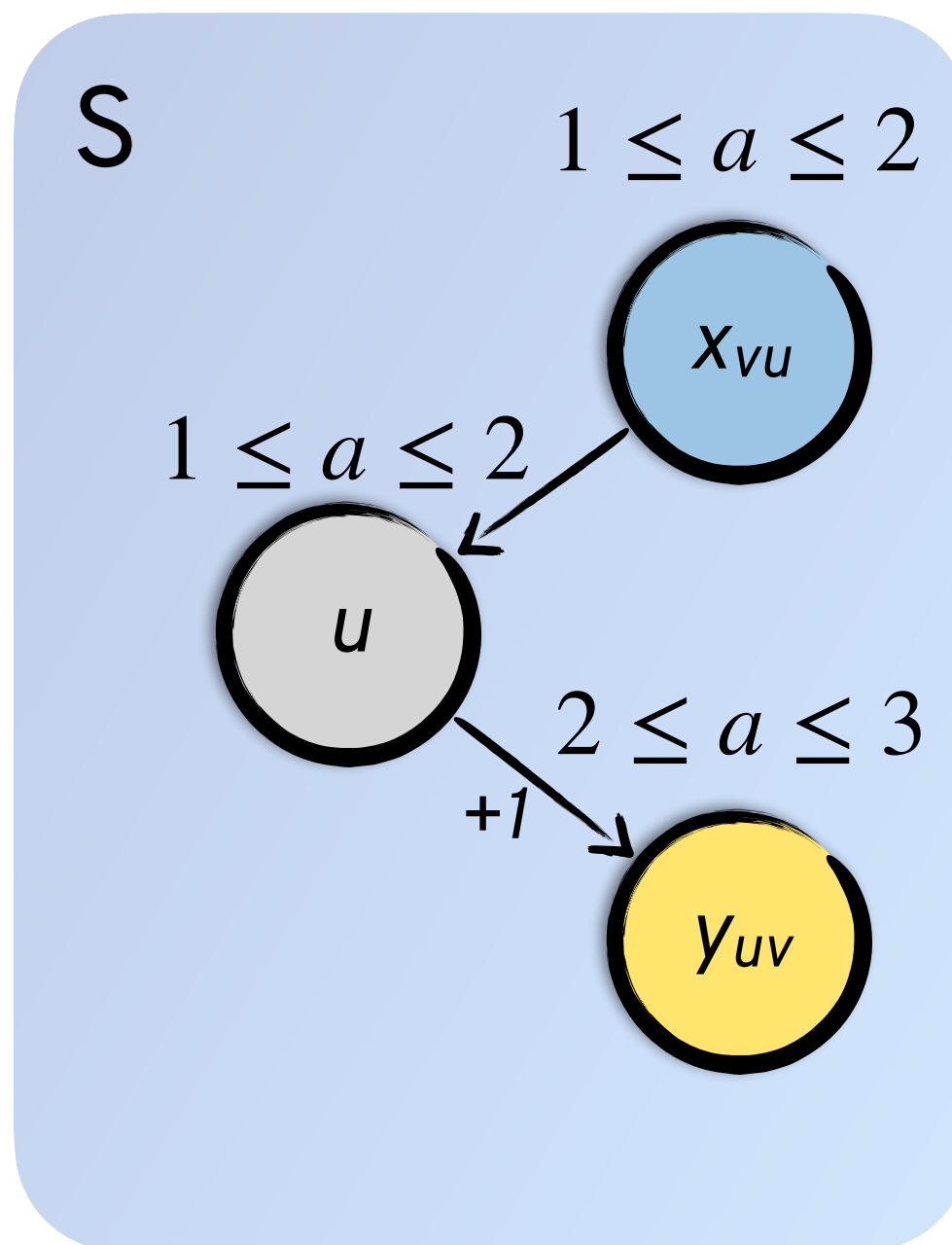
$$(v, u, 1 \leq a \leq 2)$$

$$(u, v, 1 \leq a)$$

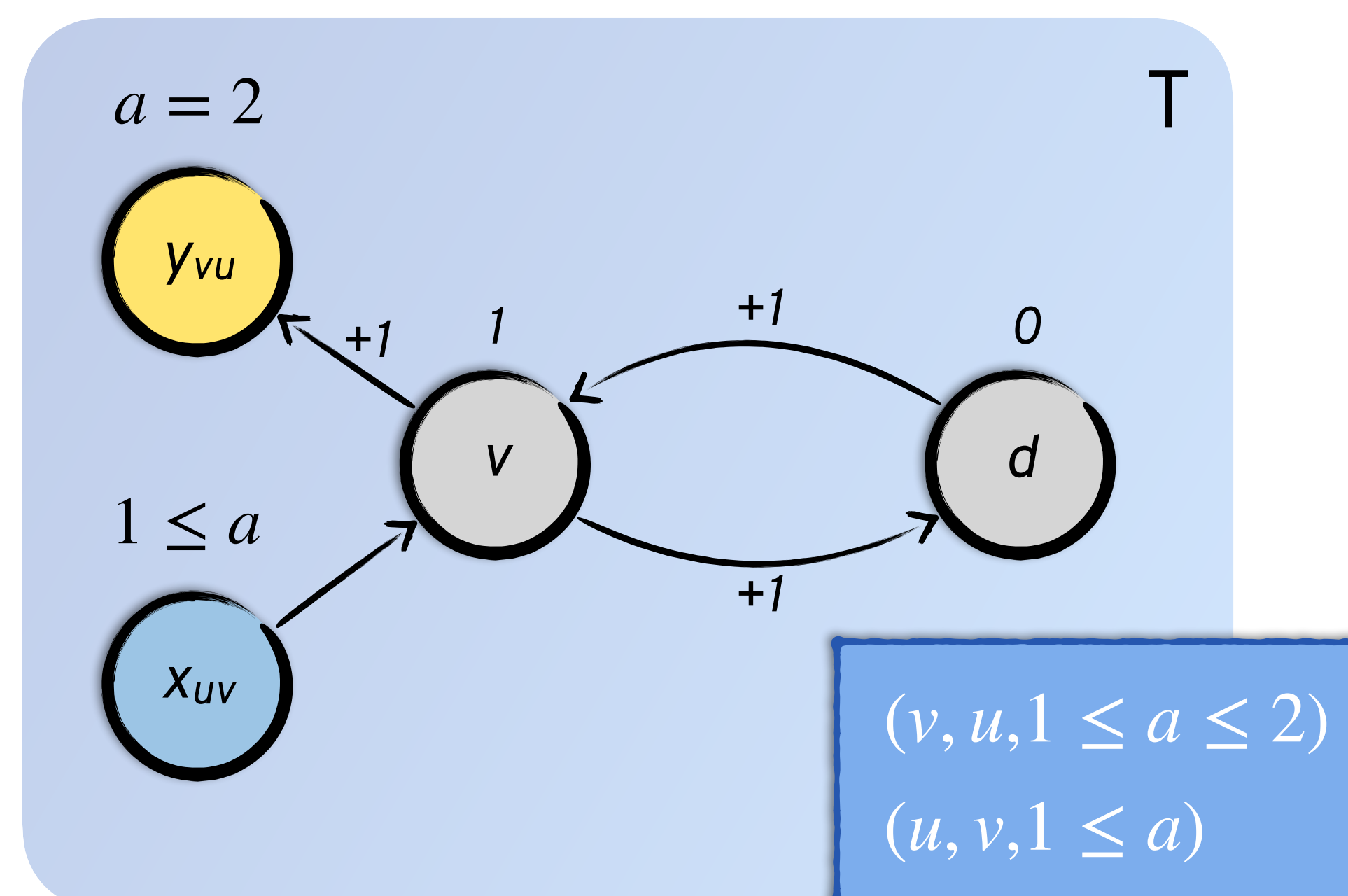
$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

Kirigami Is Sound!

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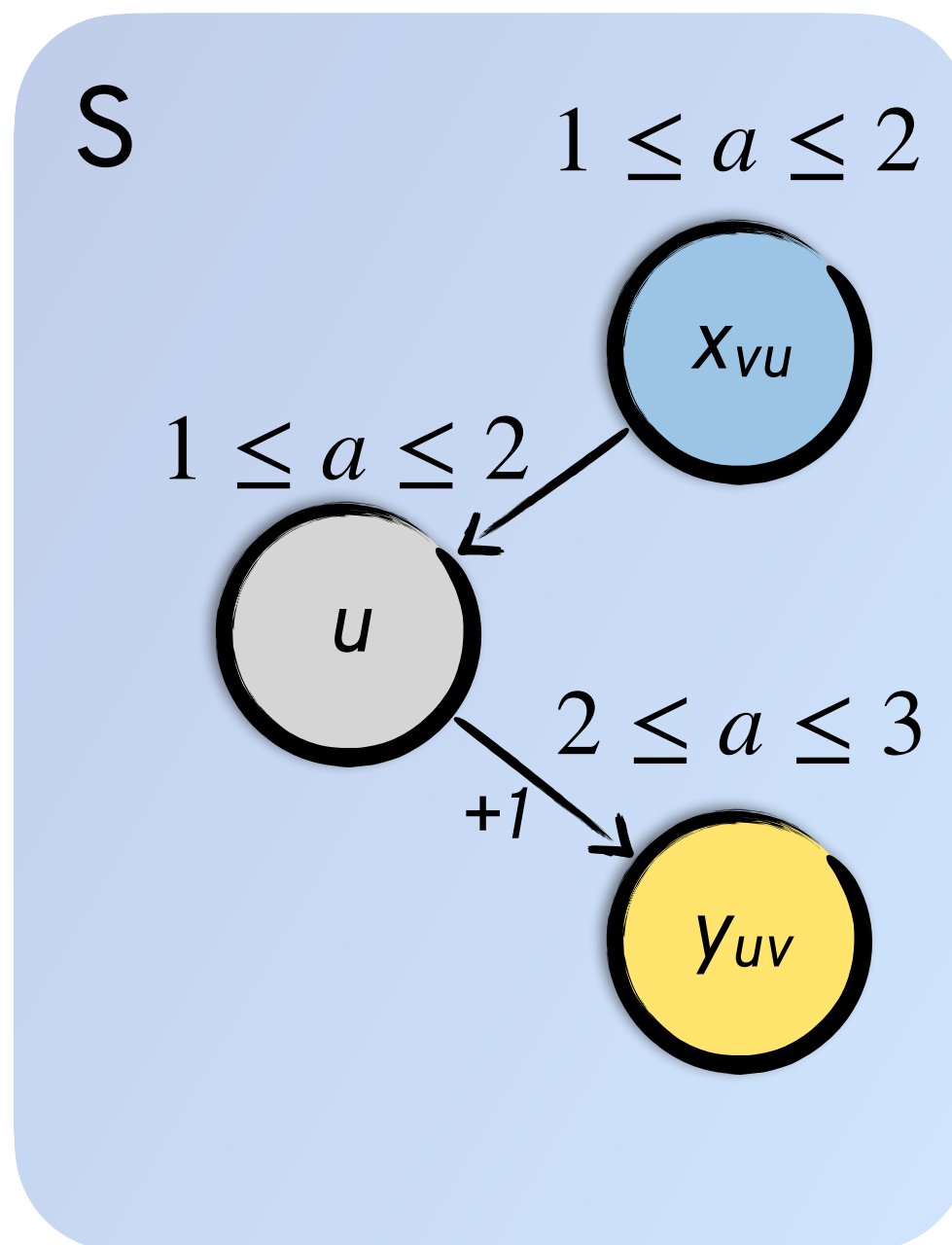


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

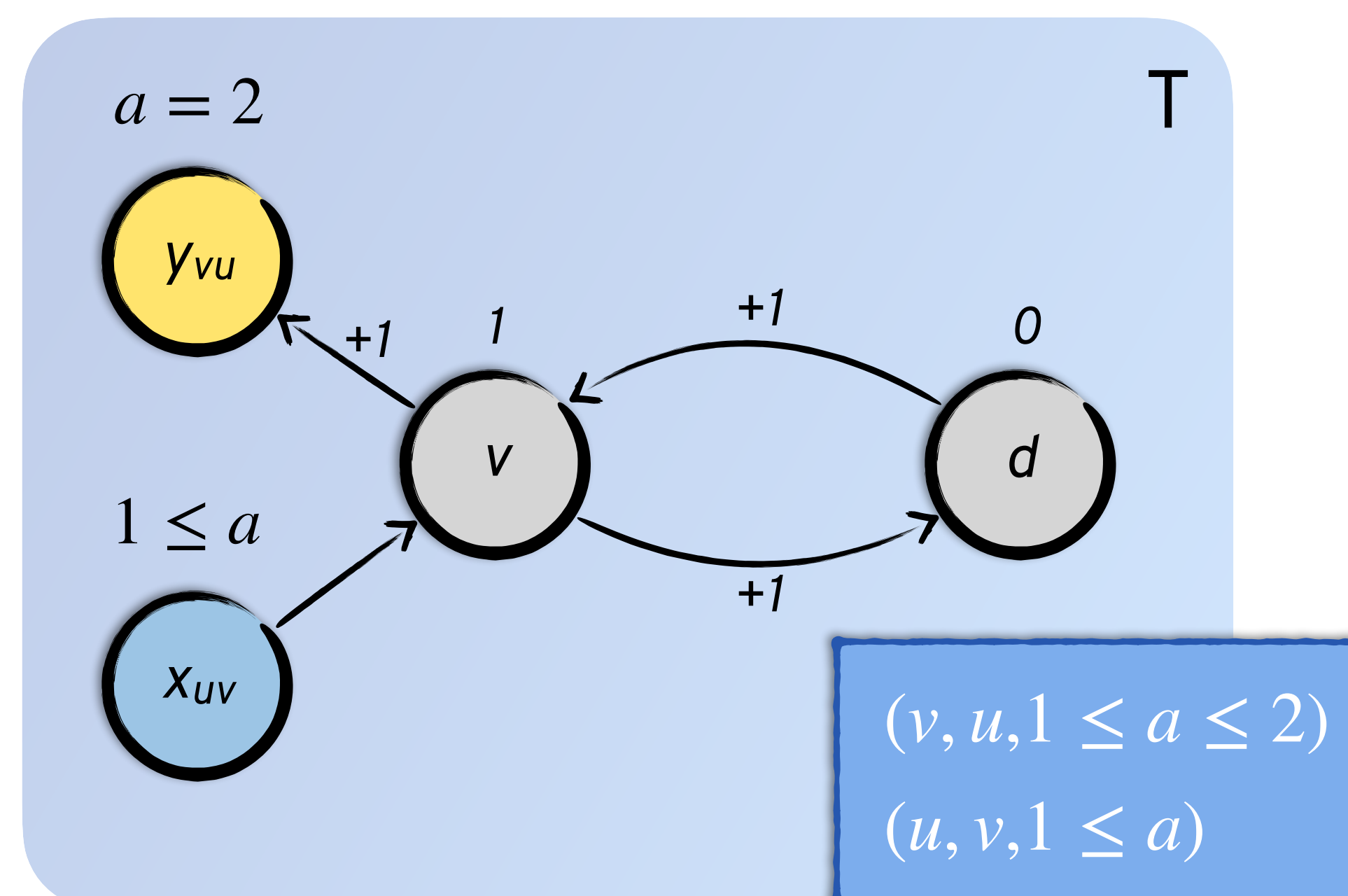
- To start, show that for all nodes v , $\mathcal{L}_R(v) \subseteq \mathcal{L}_T(v)$ (or S)

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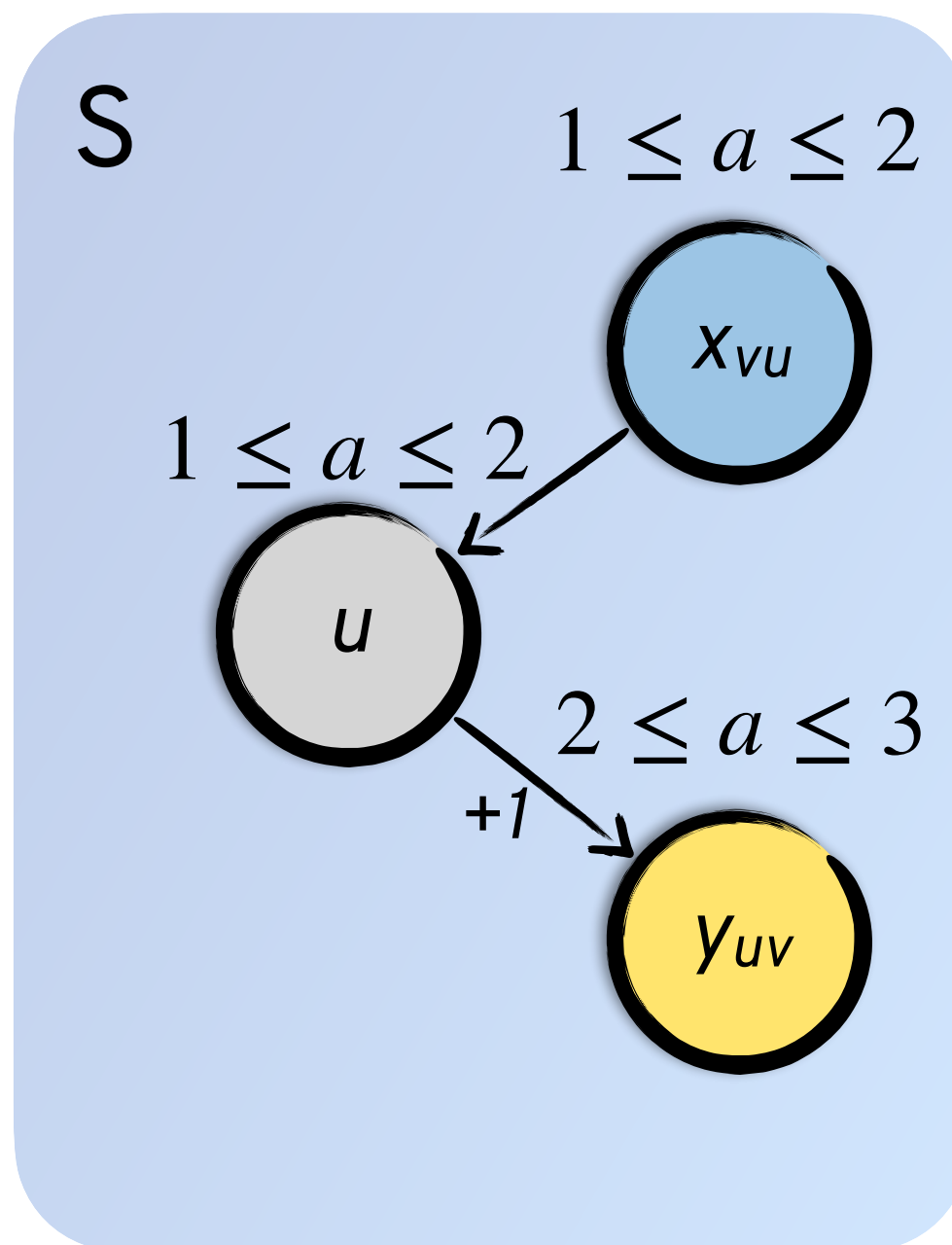


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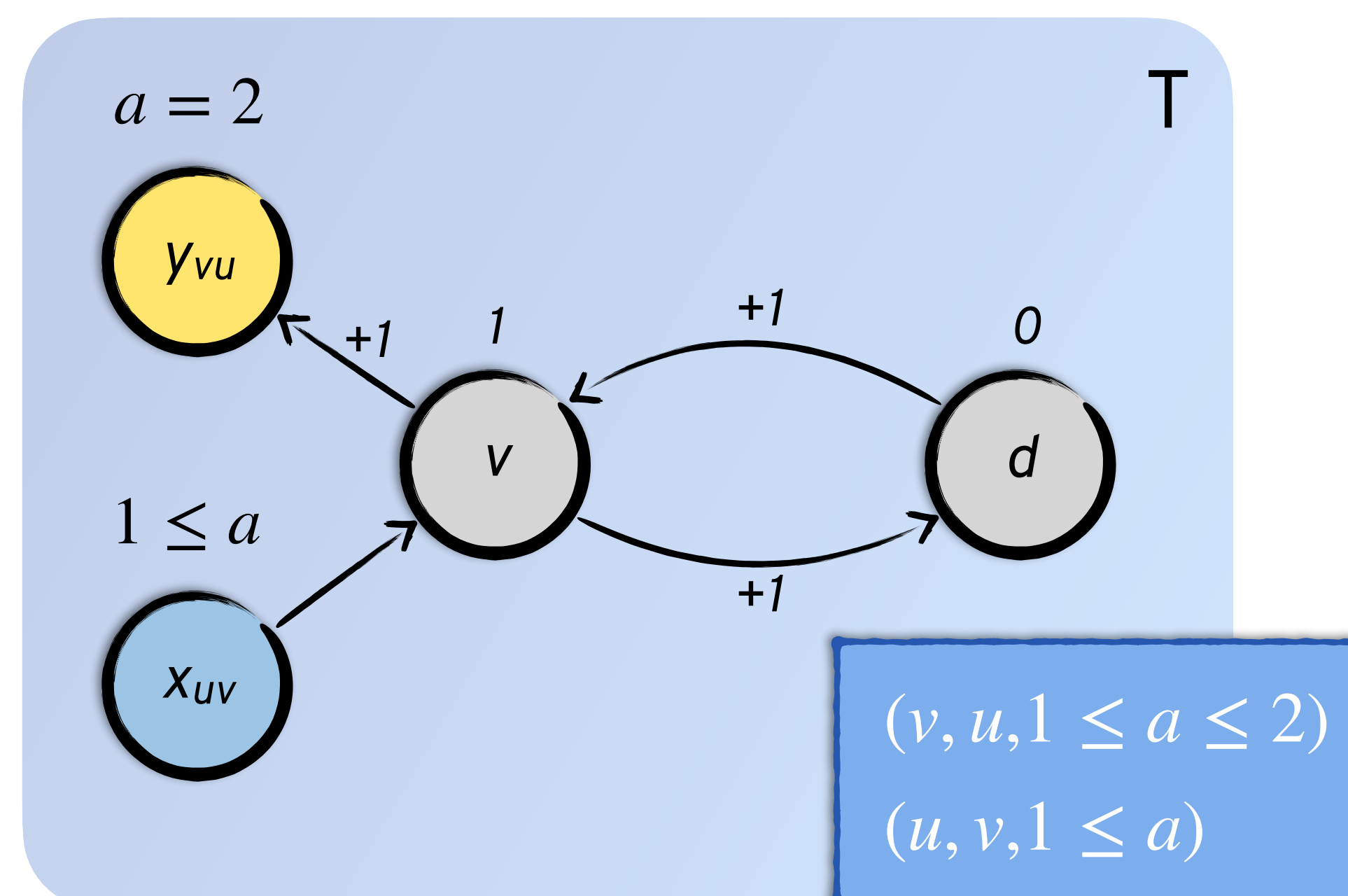
- To start, show that for all nodes v , $\mathcal{L}_R(v) \subseteq \mathcal{L}_T(v)$ (or S)
- Node's solution in terms of neighbors' transferred solutions
 $\text{trans}((u, v), \mathcal{L}(u))$

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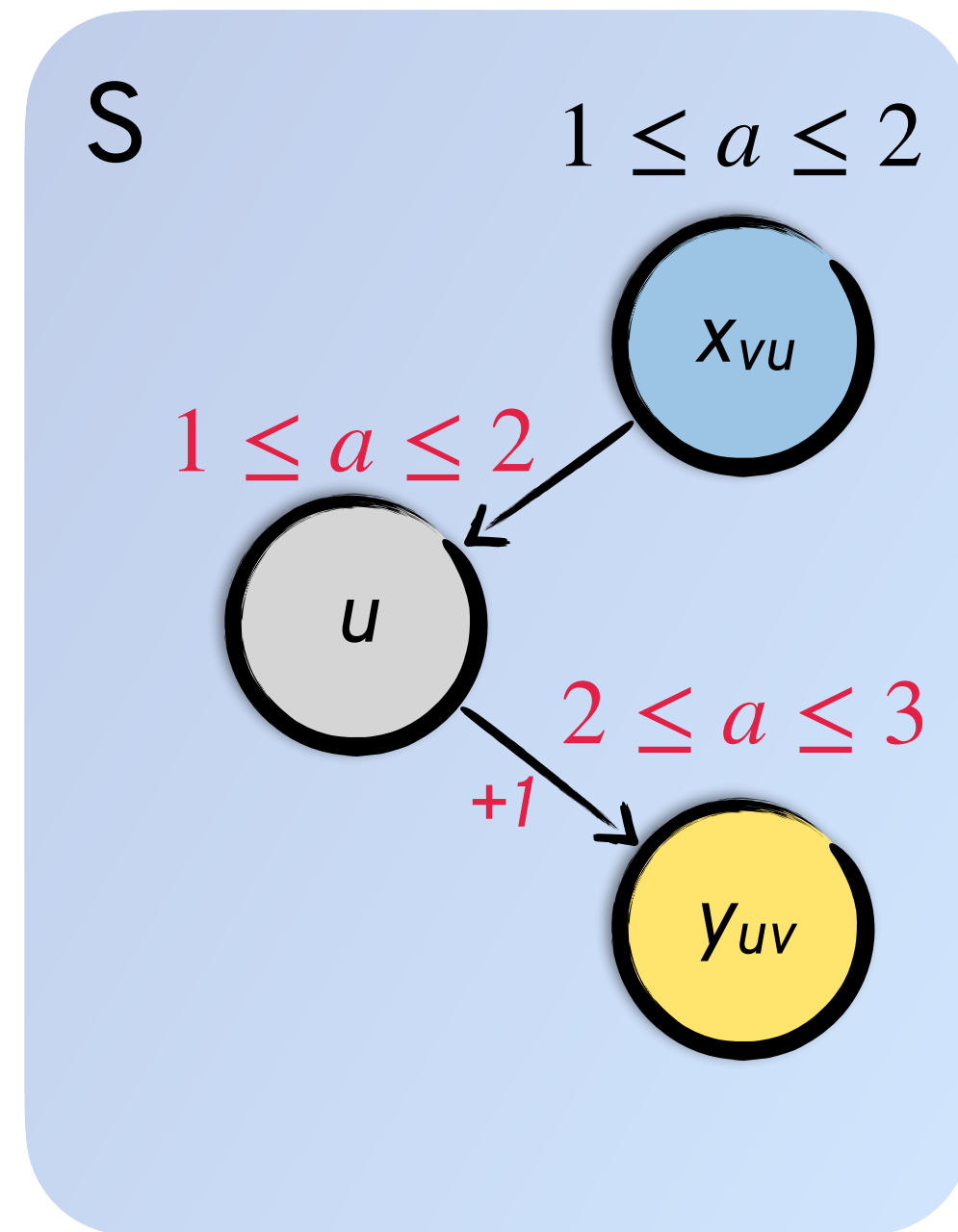


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

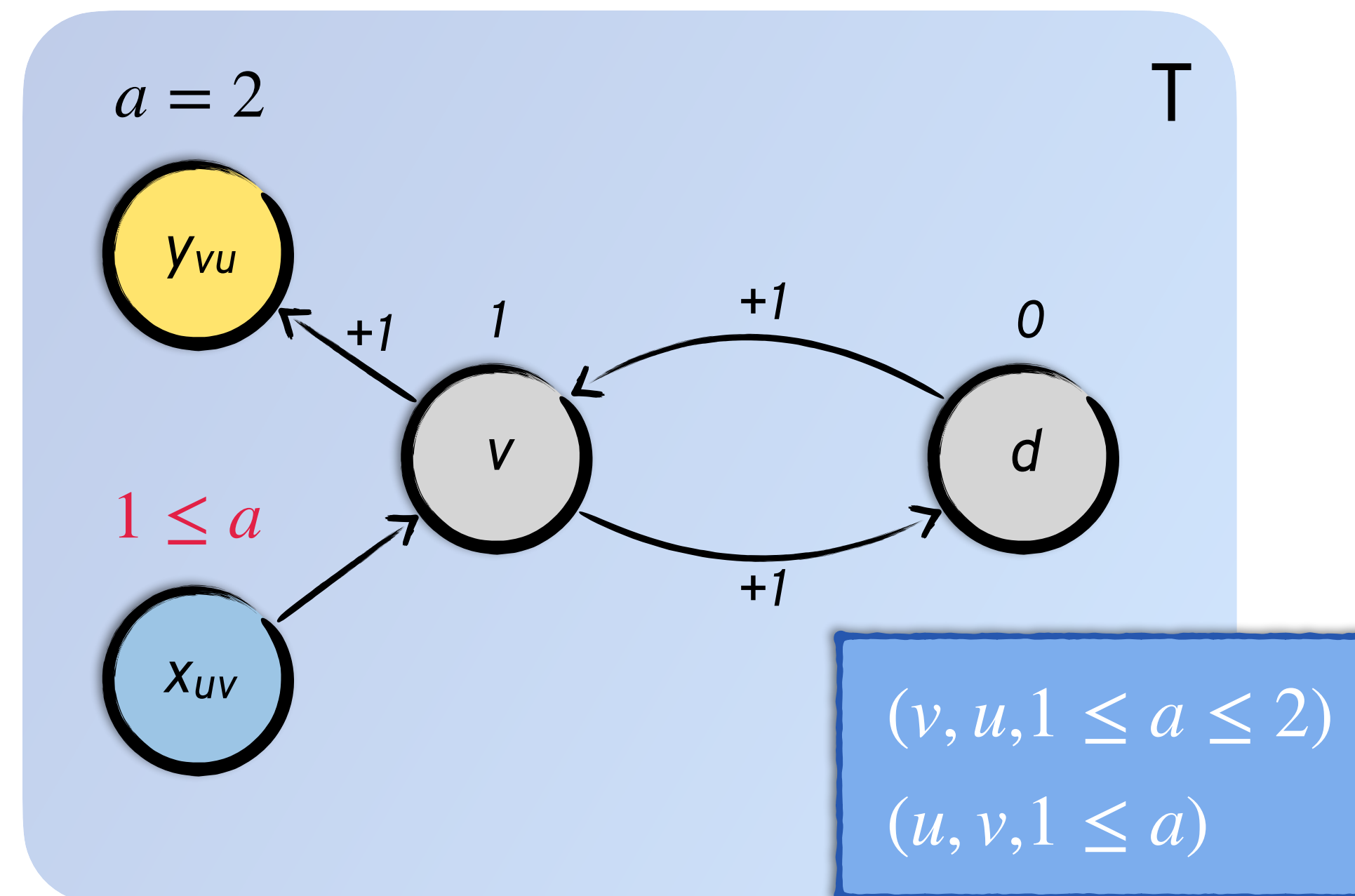
- To start, show that for all nodes v , $\mathcal{L}_R(v) \subseteq \mathcal{L}_T(v)$ (or S)
- Node's solution in terms of neighbors' transferred solutions
 $\text{trans}((u, v), \mathcal{L}(u))$
- Case analysis on the neighbors of v in T (or S)

Kirigami Is Sound!

Theorem: if Kirigami returns true, then property P holds for monolithic network R



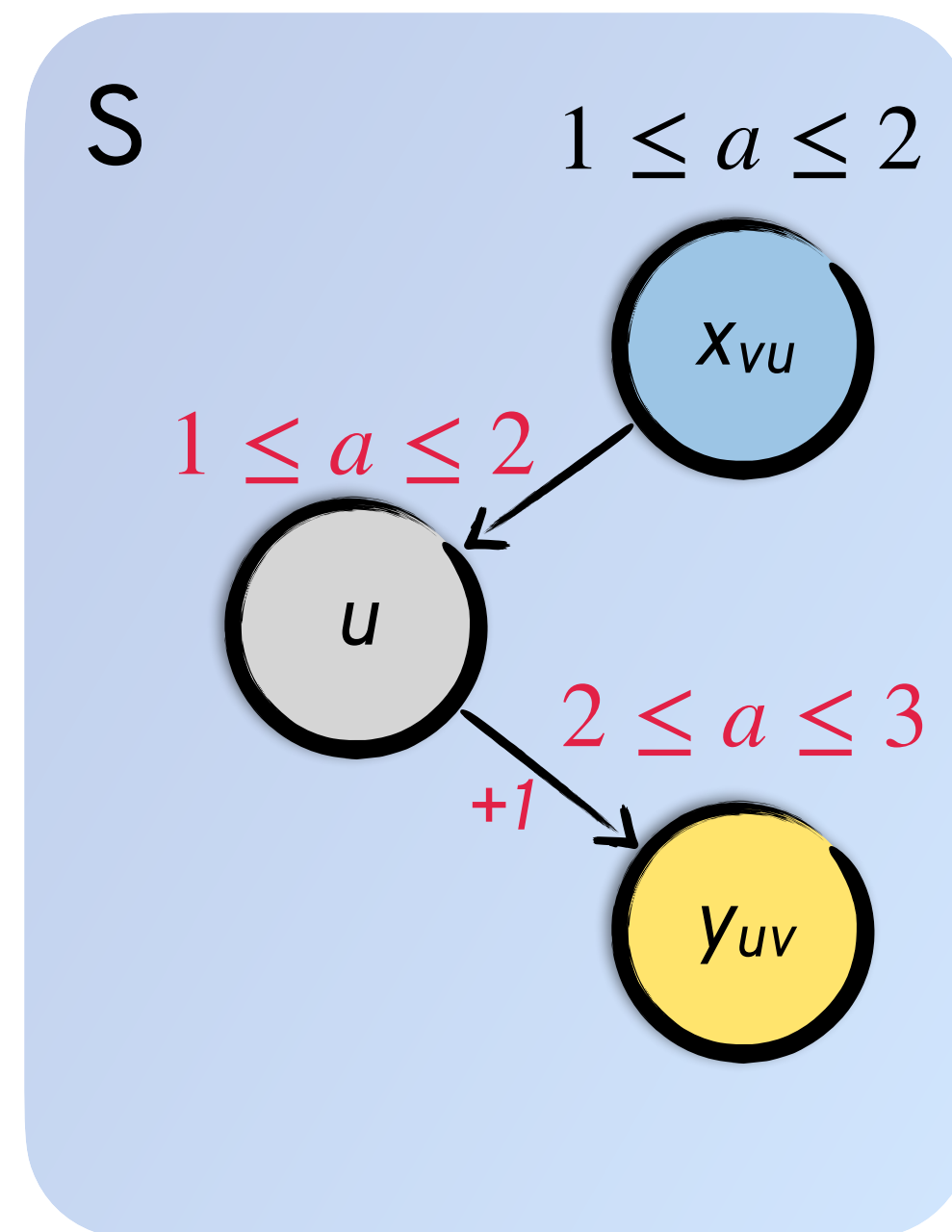
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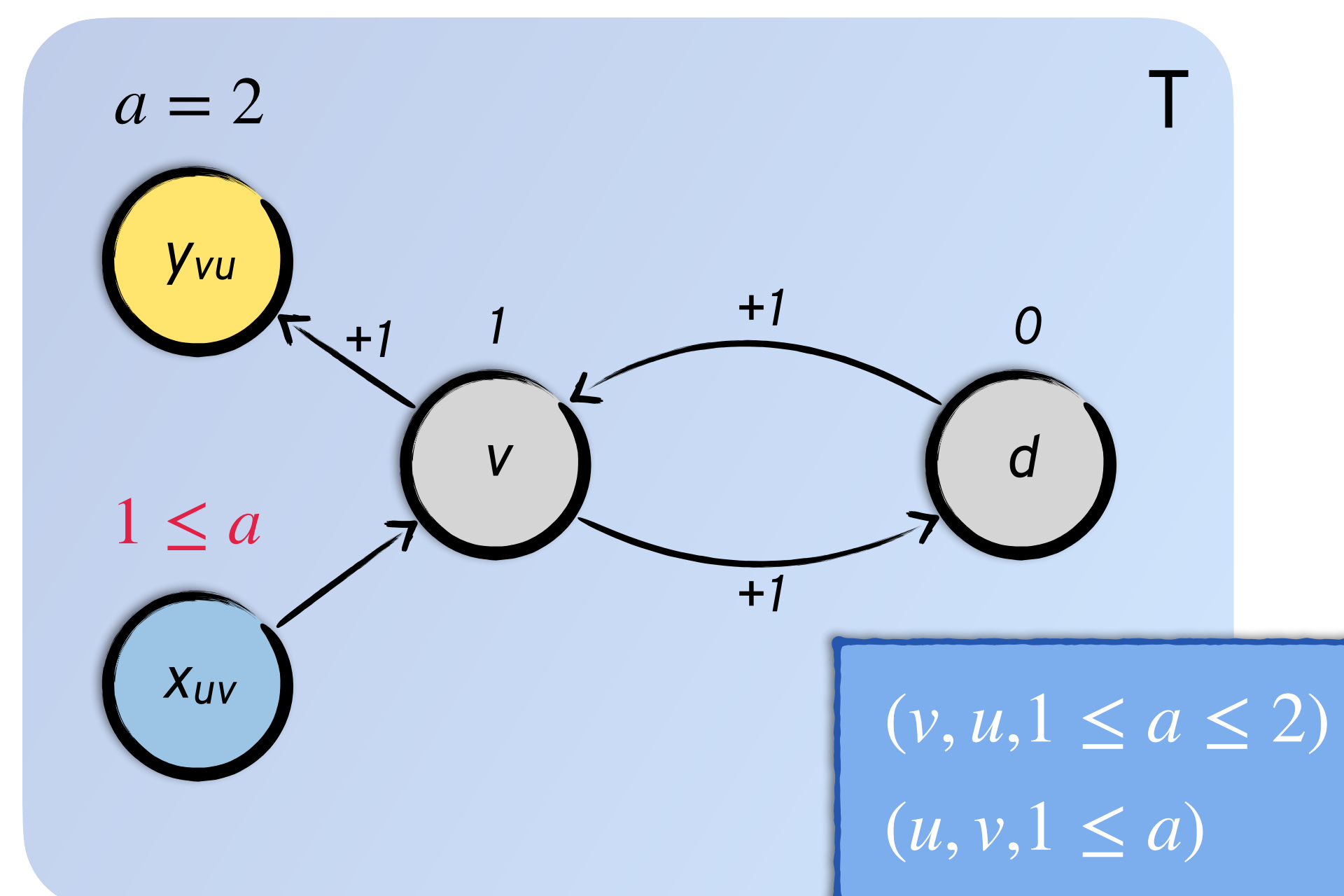
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$$P_S = \mathcal{L}(u) < 10$$



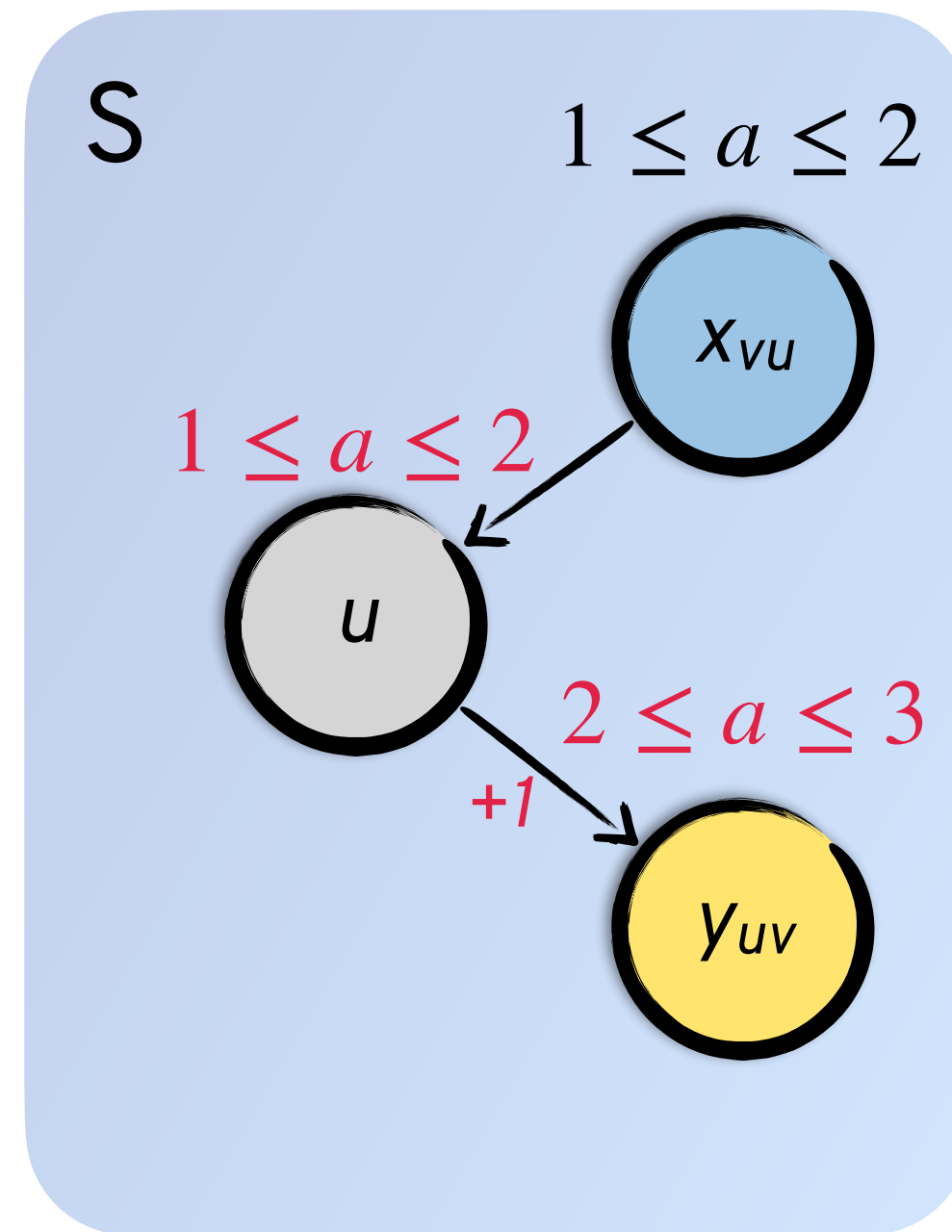
$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

Case 1: neighbor is input node

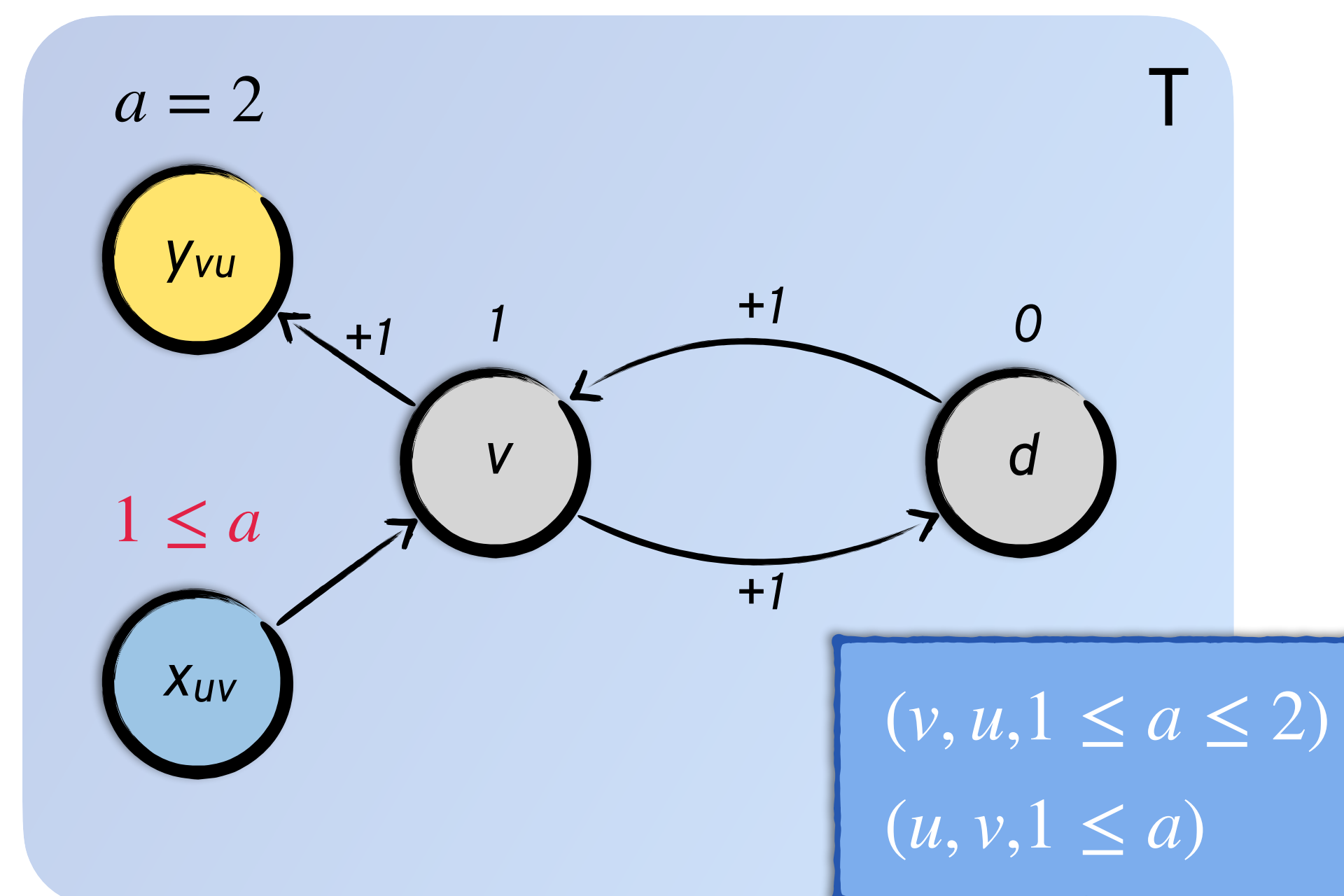
x_{uv} :

Kirigami Is Sound!

Theorem: if Kirigami returns true, then property P holds for monolithic network R



$$P_S = \mathcal{L}(u) < 10$$



$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

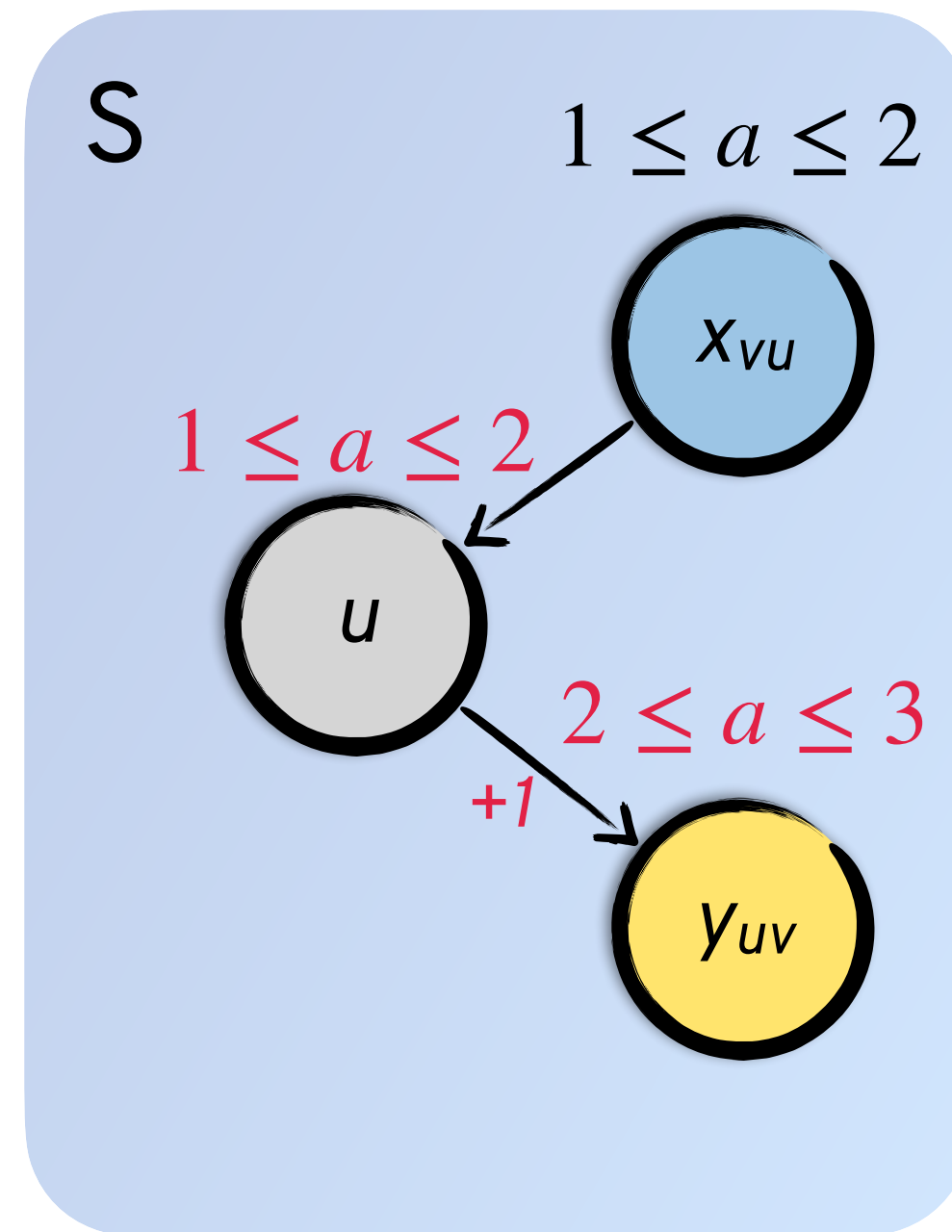
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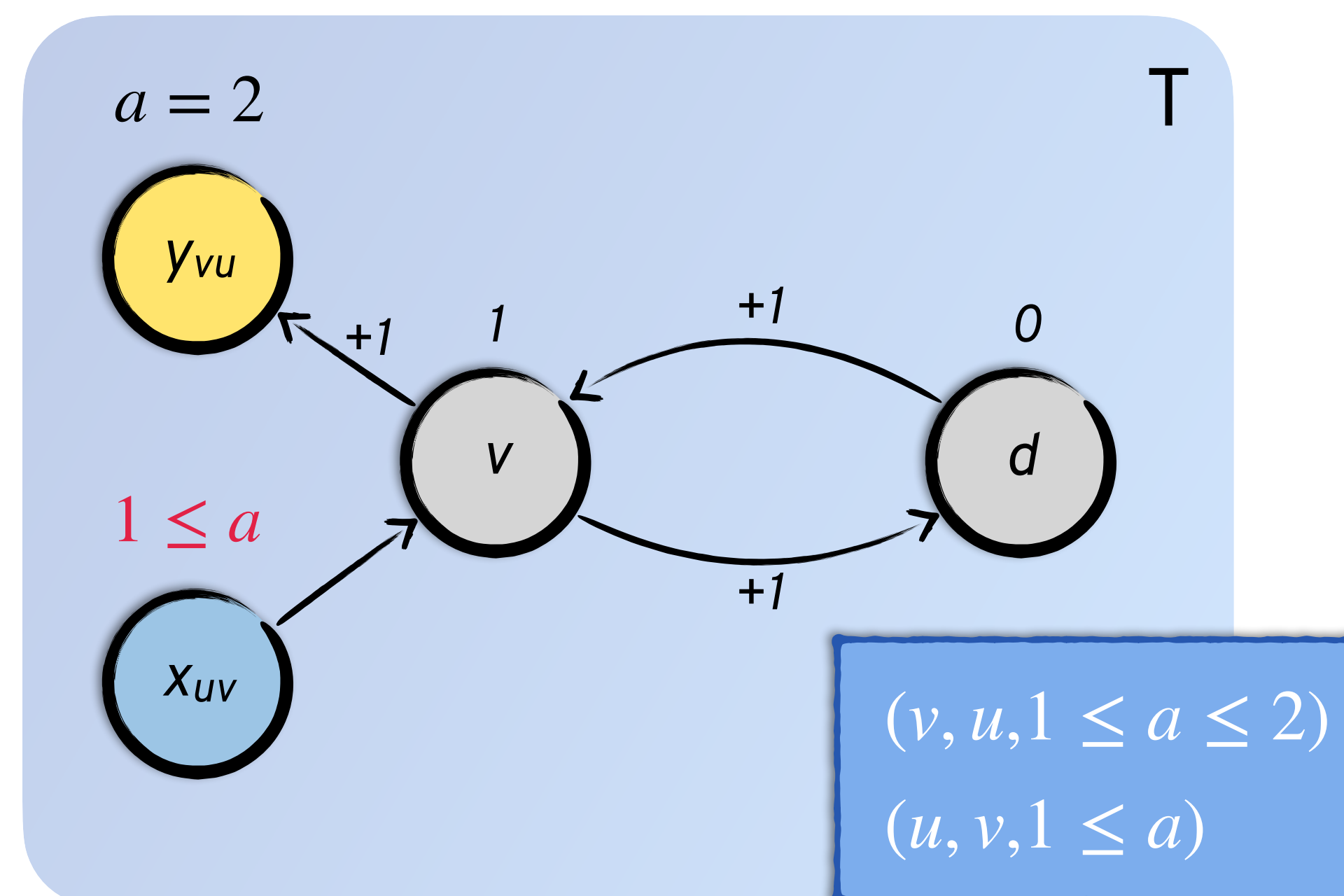
- By inductiveness check,
 $\text{trans}((u, y_{uv}), \mathcal{L}_S(u)) \subseteq \text{trans}((x_{uv}, v), \mathcal{L}_T(x_{uv}))$

Kirigami Is Sound!

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$$P_S = \mathcal{L}(u) < 10$$



$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

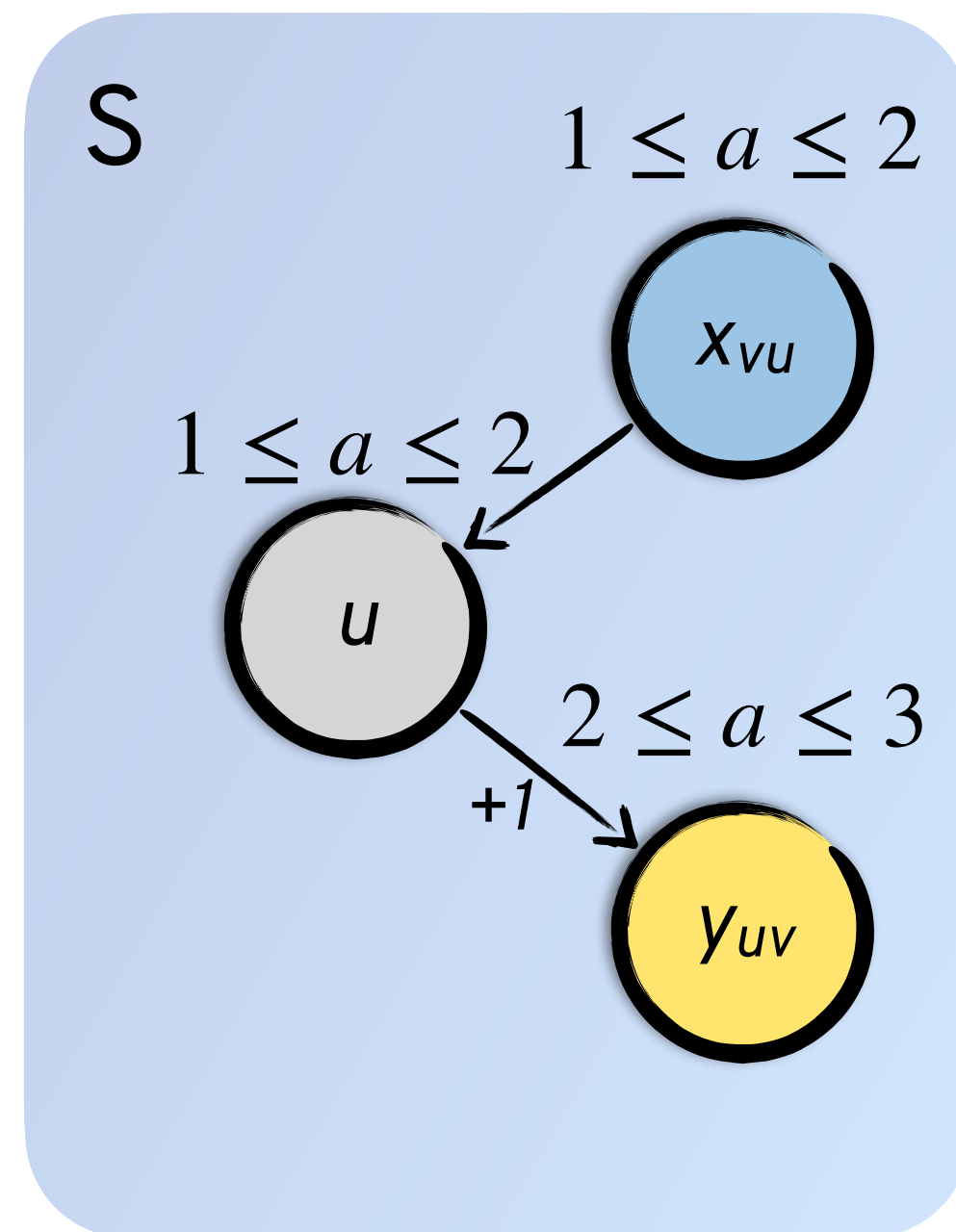
Case 1: neighbor is input node

x_{uv} :

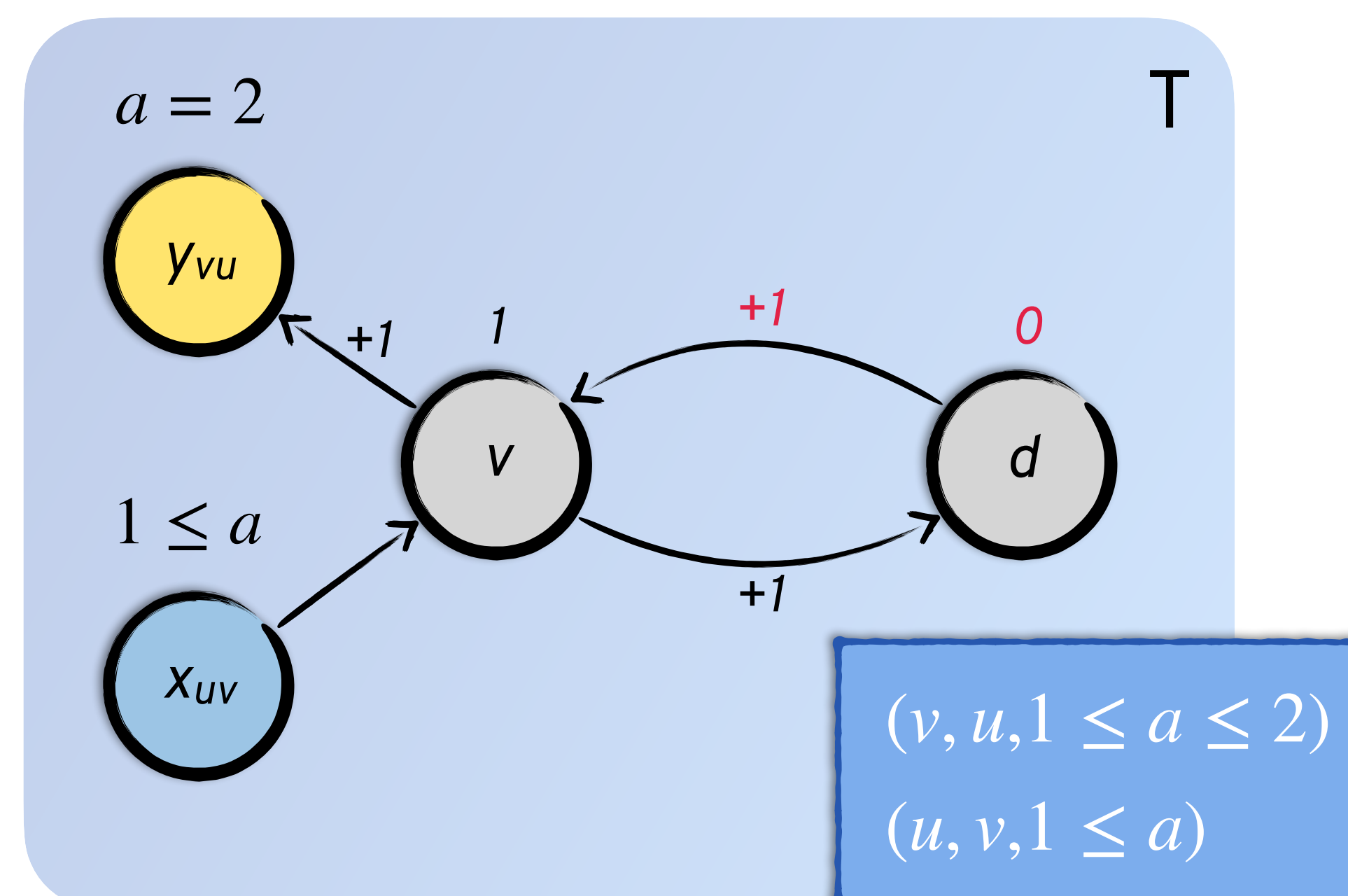
- By inductiveness check,
 $\text{trans}((u, y_{uv}), \mathcal{L}_S(u)) \subseteq \text{trans}((x_{uv}, v), \mathcal{L}_T(x_{uv}))$
- By co-induction,
 $\mathcal{L}_R(u) \subseteq \mathcal{L}_S(u)$

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$$P_S = \mathcal{L}(u) < 10$$



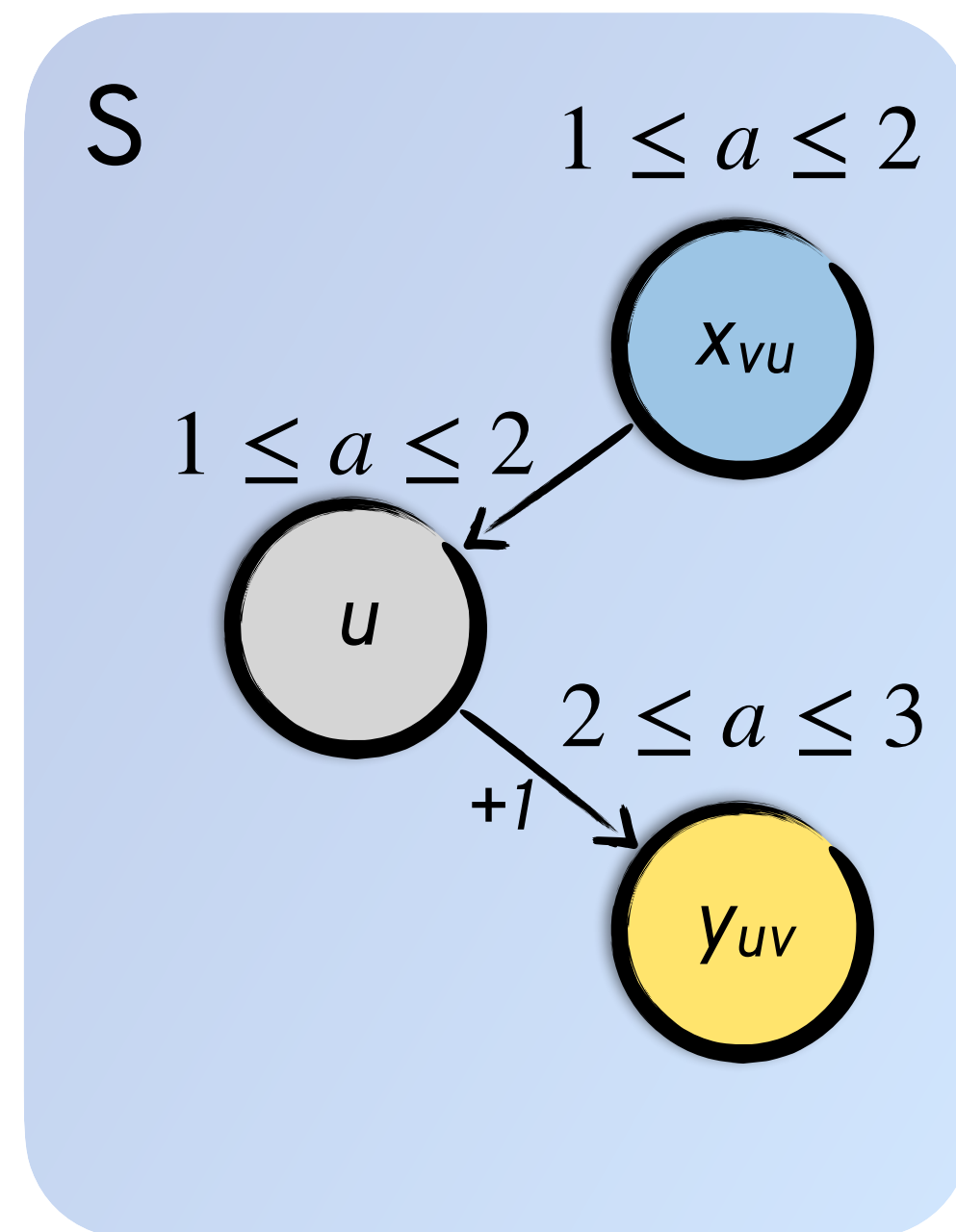
$$(v, u, 1 \leq a \leq 2)$$

$$(u, v, 1 \leq a)$$

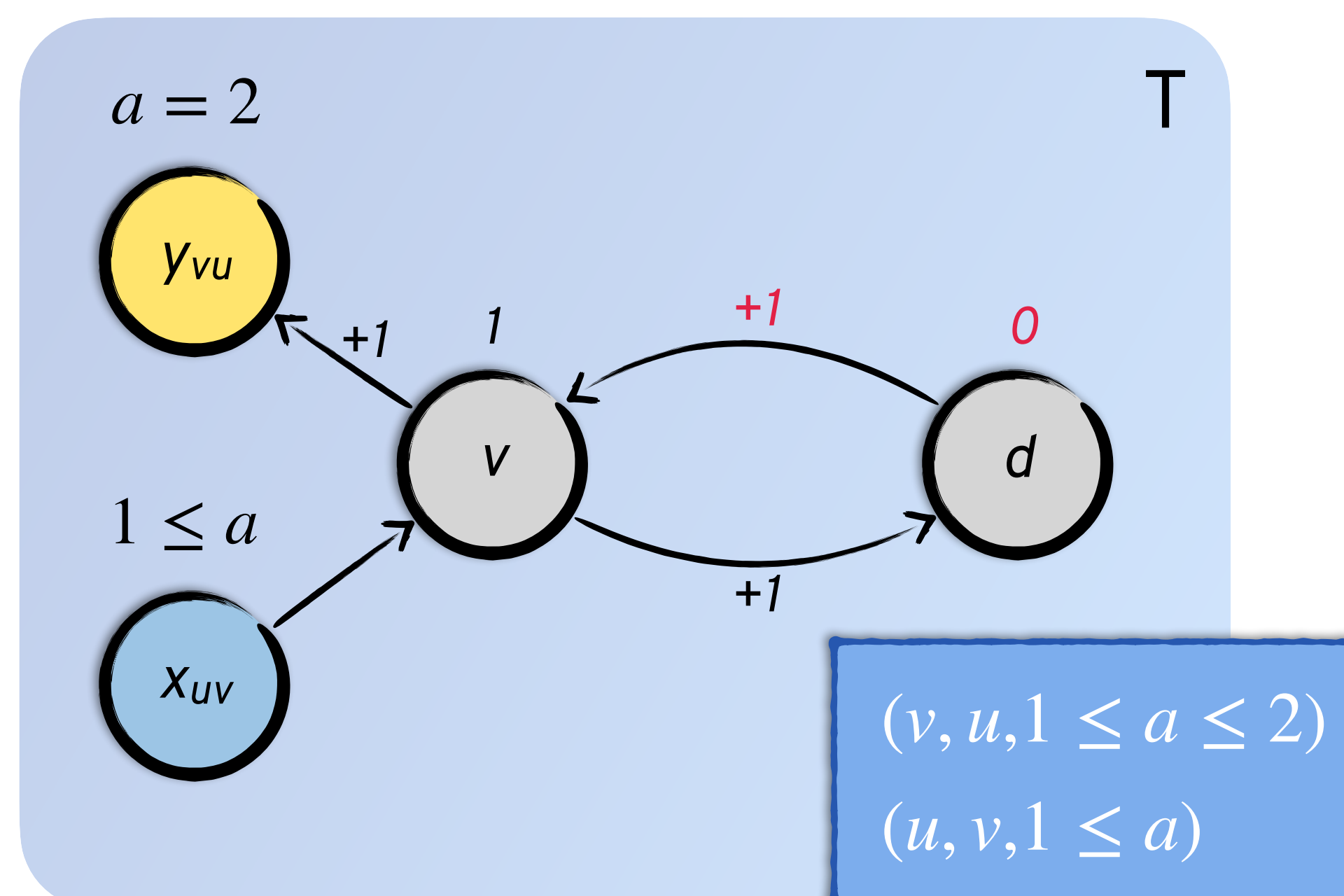
$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

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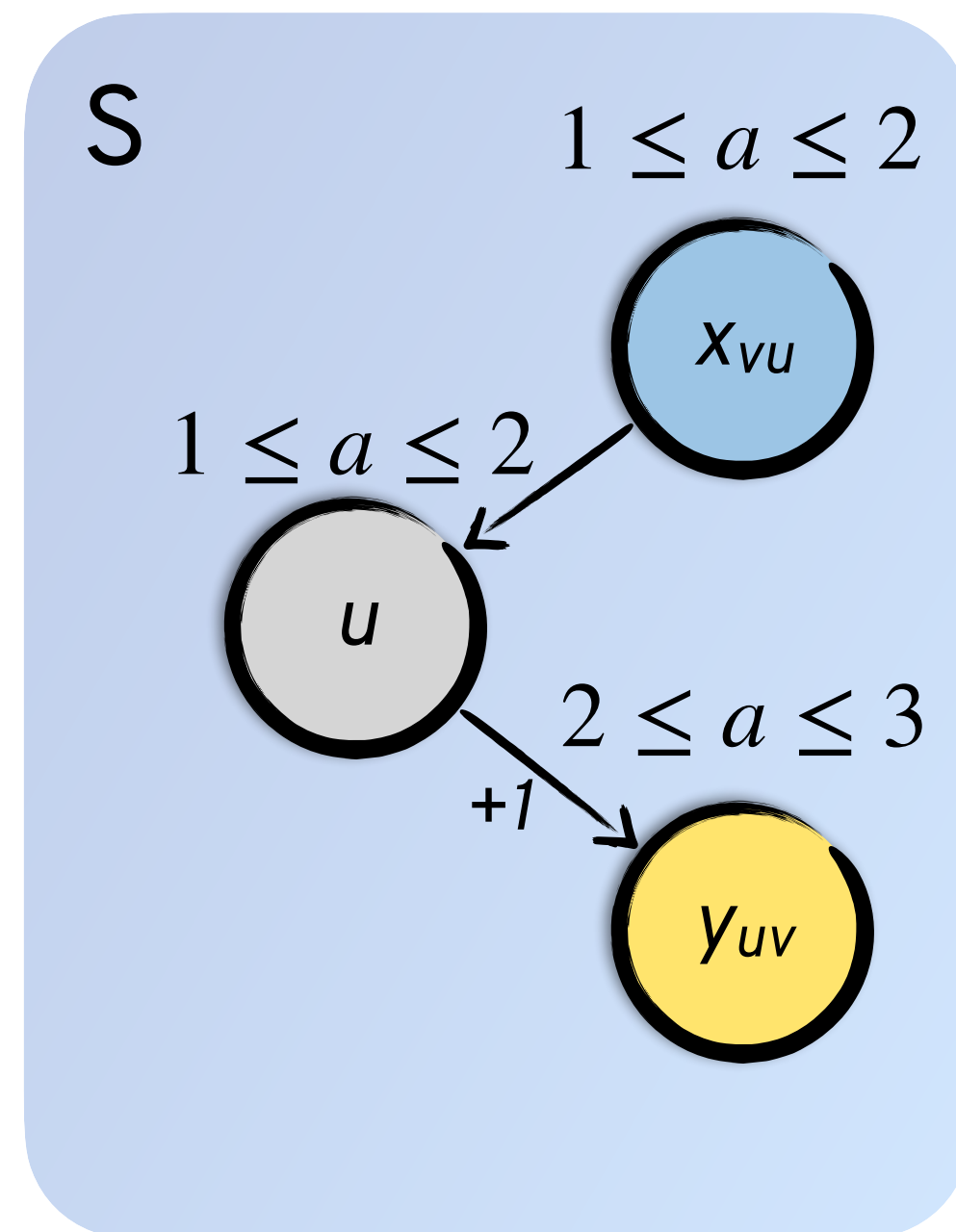


$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

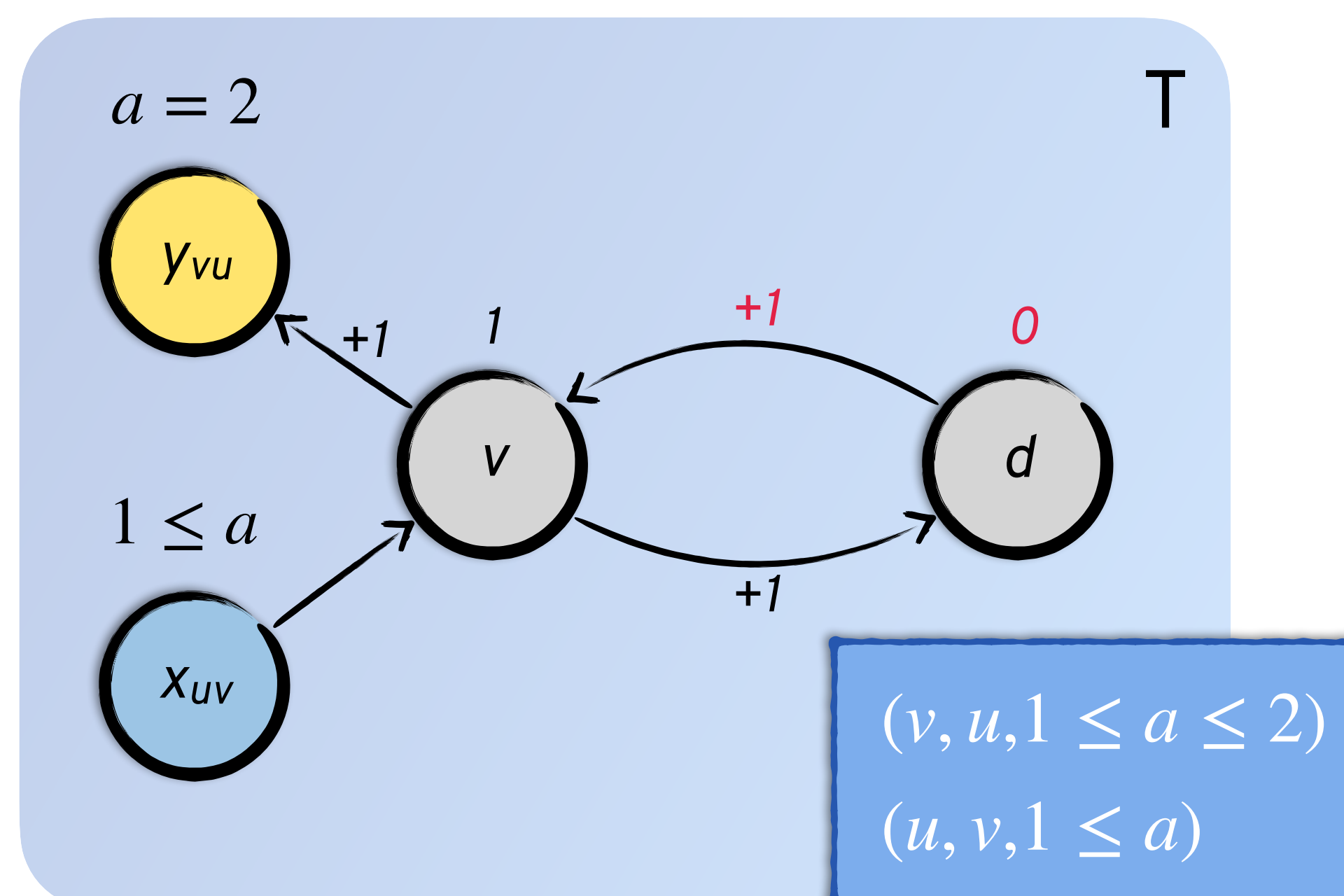
Case 2: neighbor is a base node d :

Kirigami Is Sound!

Theorem: if Kirigami returns true, then property P holds for monolithic network R



$$P_S = \mathcal{L}(u) < 10$$



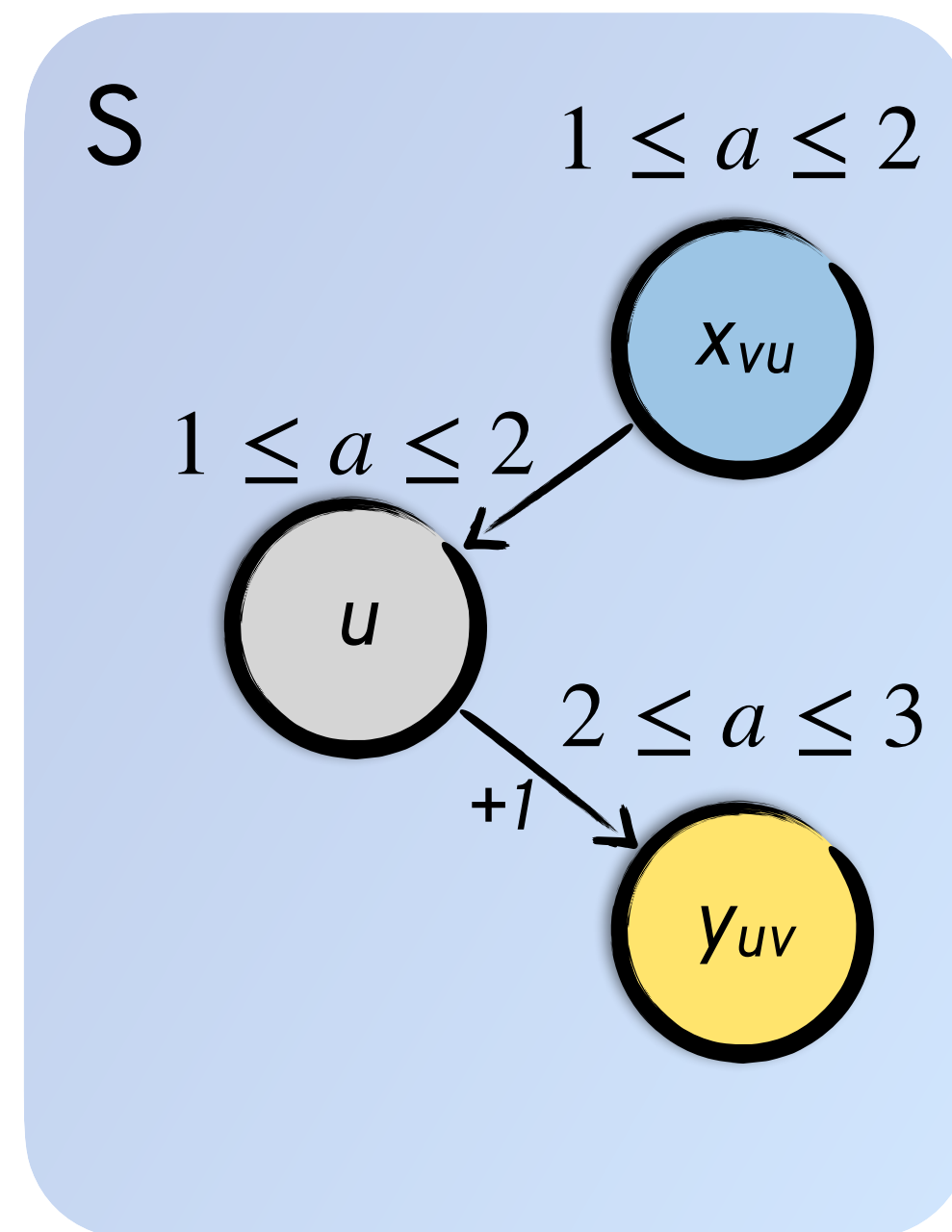
$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

Case 2: neighbor is a base node d :

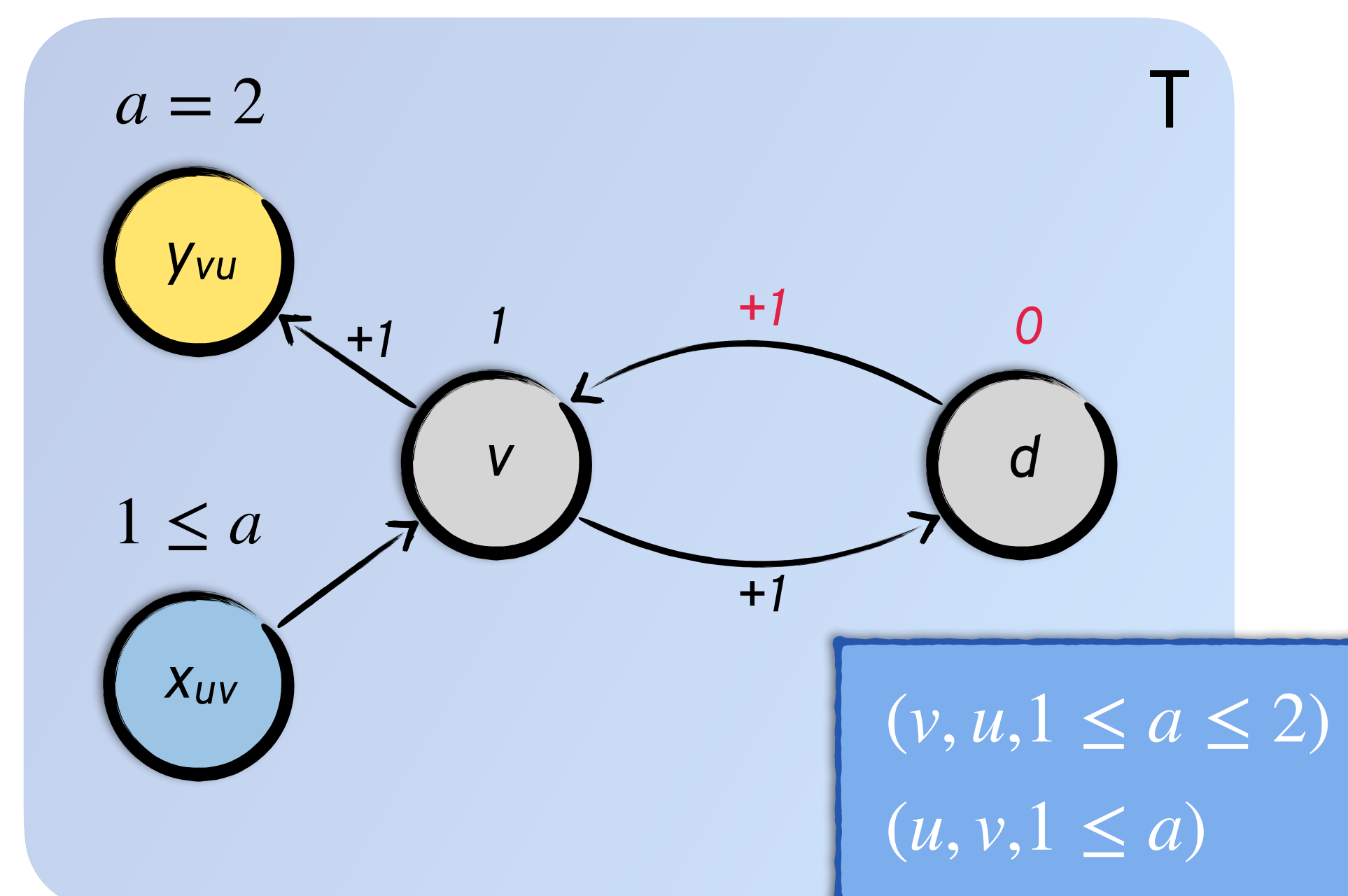
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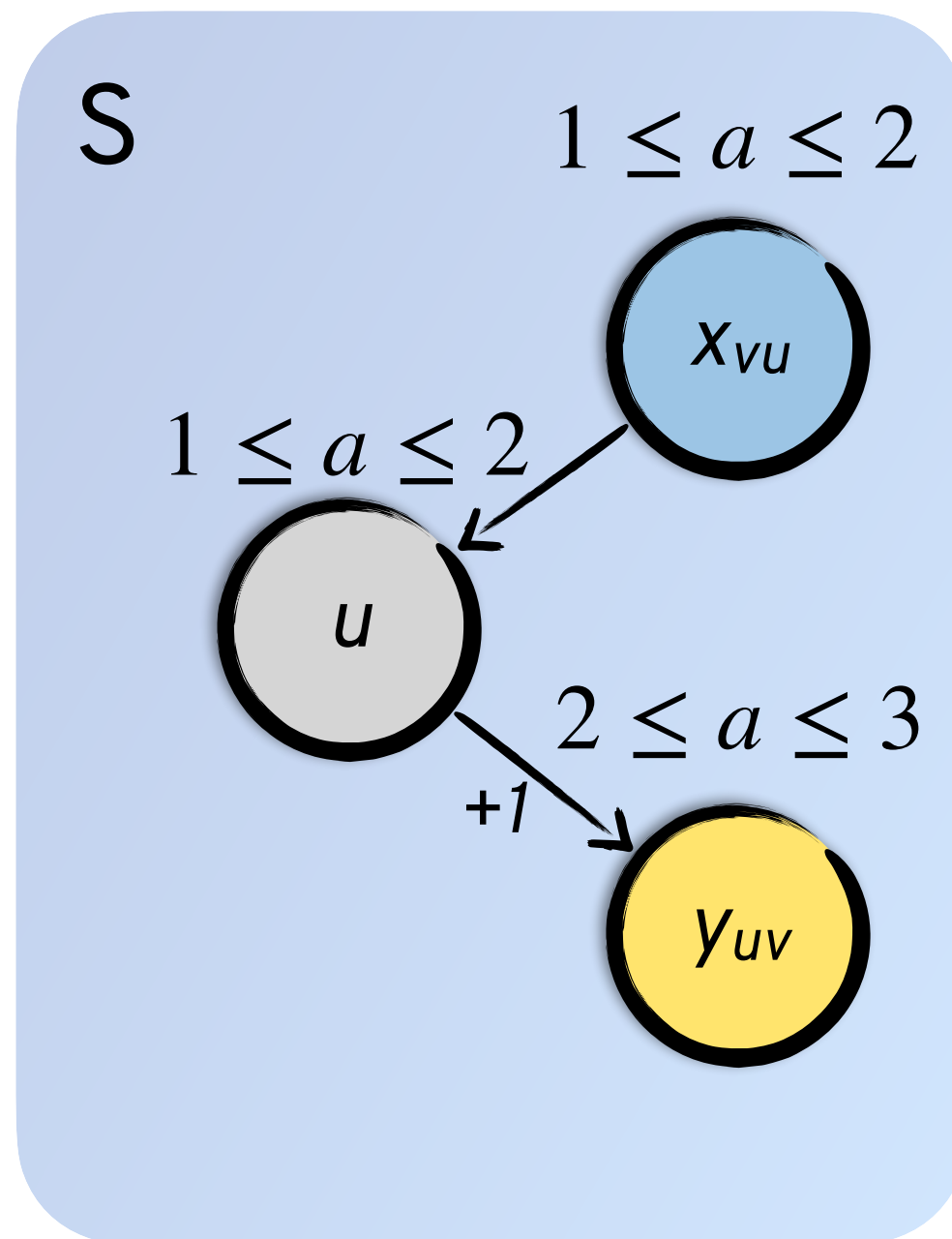
$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

Case 2: neighbor is a base node d :

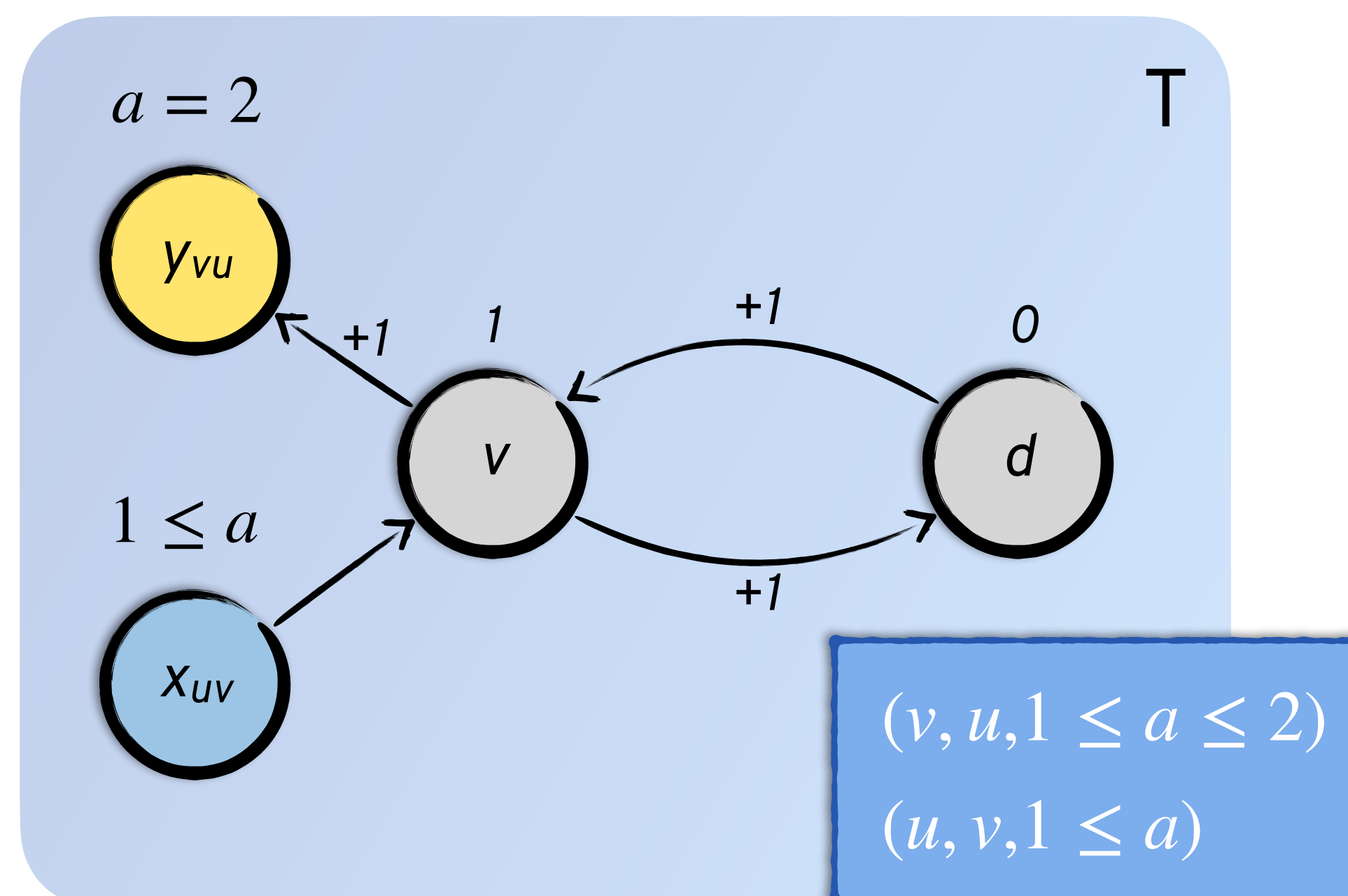
- By co-induction,
 $\mathcal{L}_R(d) \subseteq \mathcal{L}_T(d)$
- trans in R is the same as trans in T

Kirigami Is Sound!

Theorem: if Kirigami returns true, then property P holds for monolithic network R



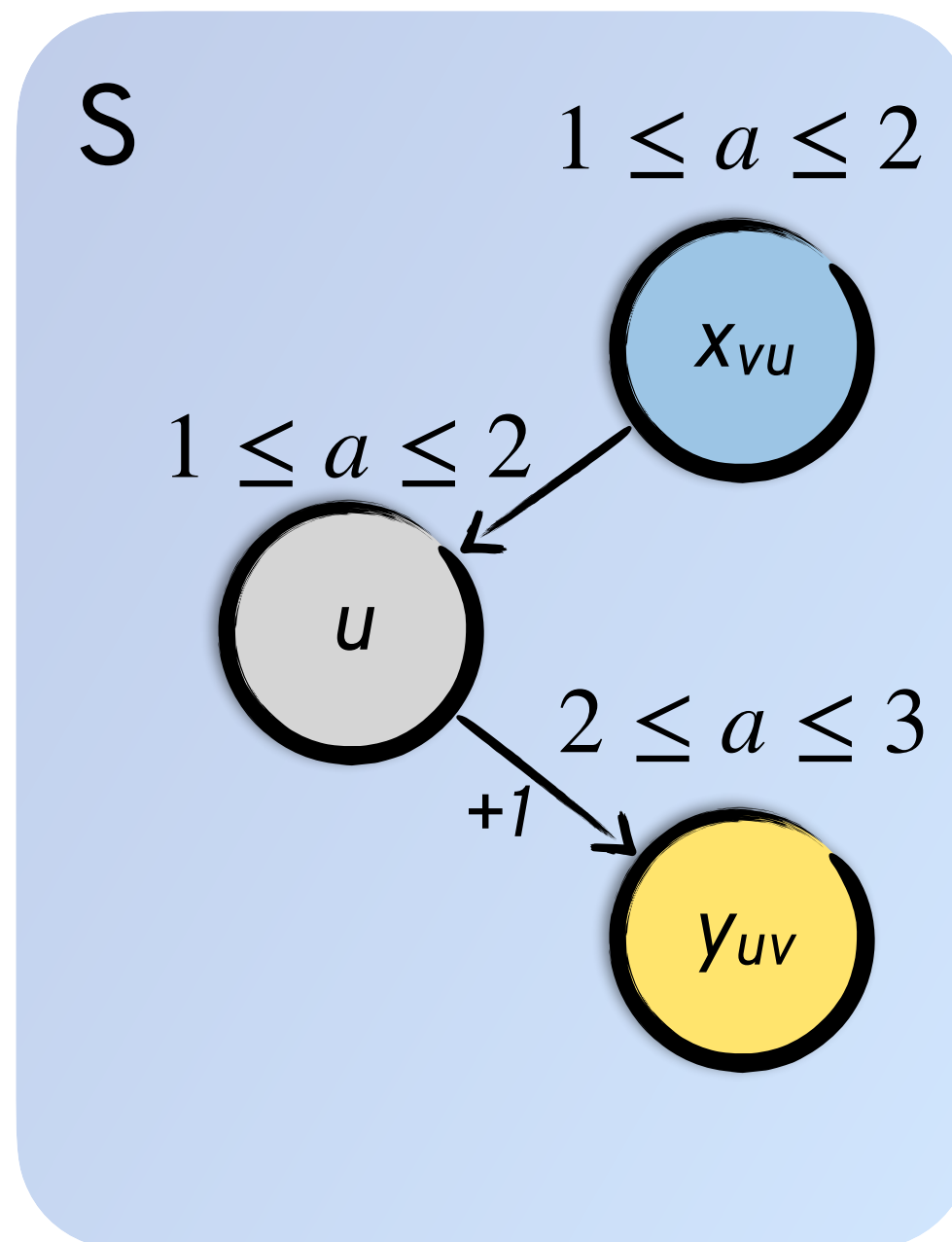
$$P_S = \mathcal{L}(u) < 10$$



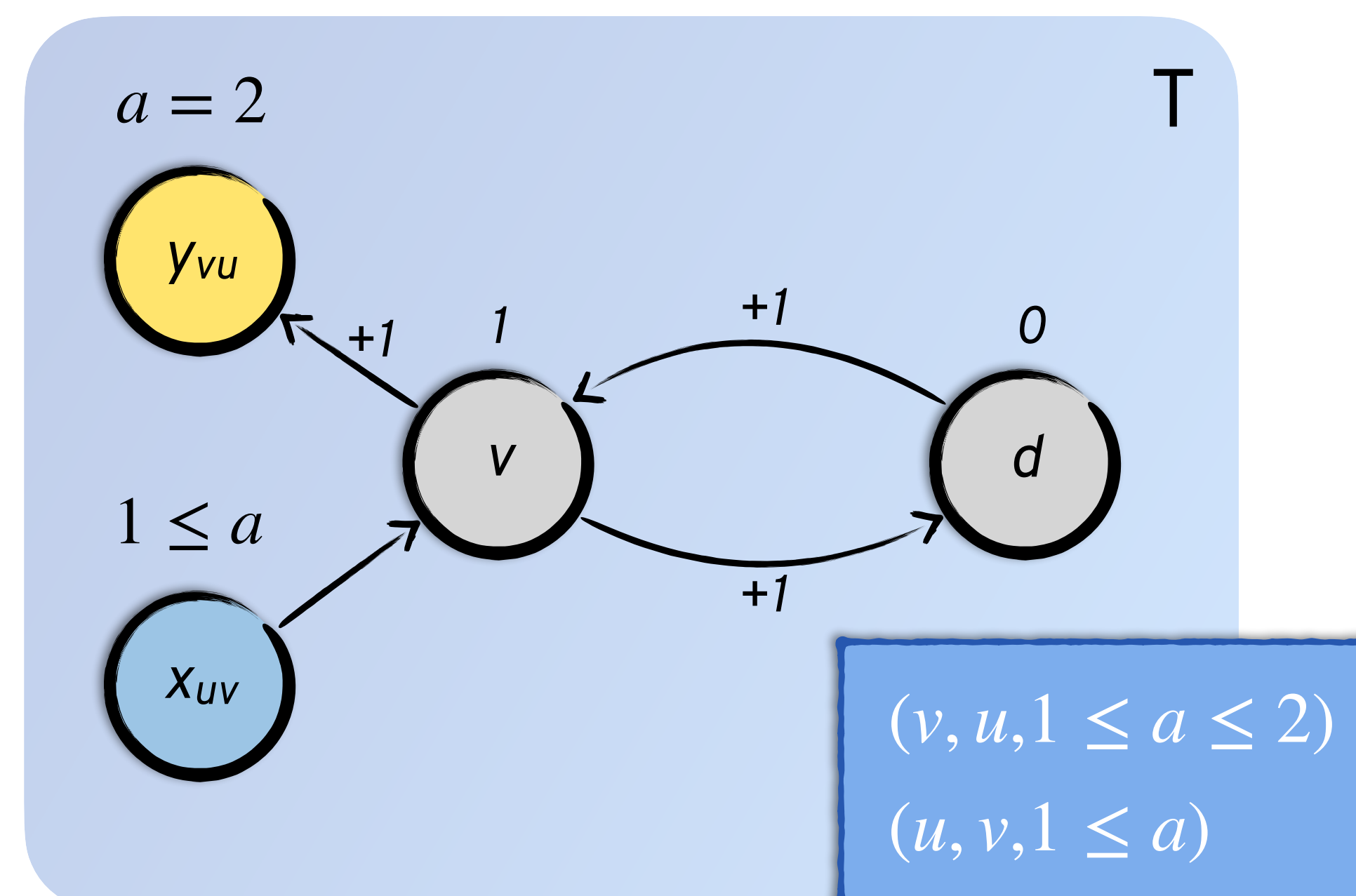
$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

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$$P_S = \mathcal{L}(u) < 10$$



$$(v, u, 1 \leq a \leq 2)$$

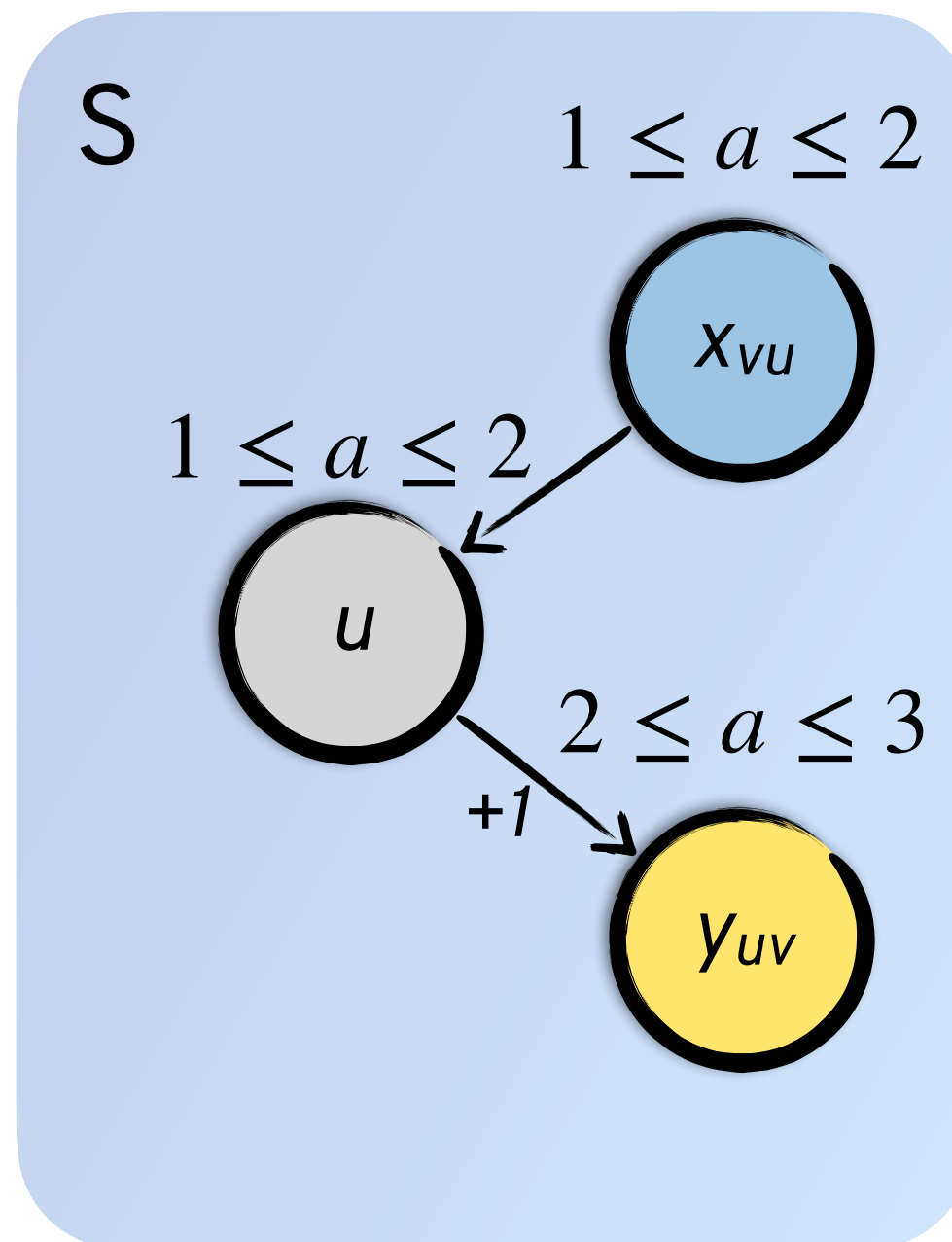
$$(u, v, 1 \leq a)$$

$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

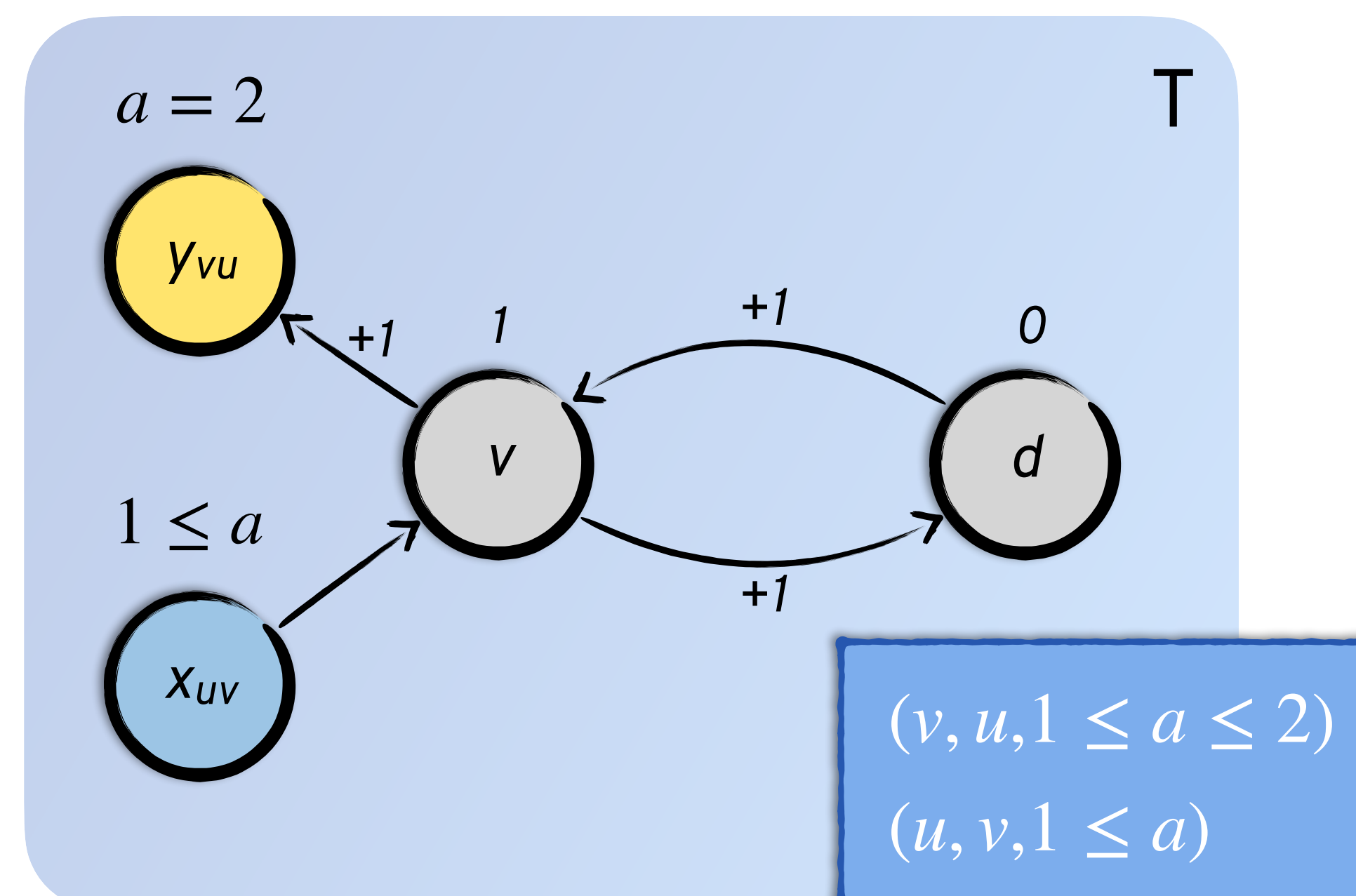
- By the two cases, for all nodes v ,
 $\mathcal{L}_R(v) \subseteq \mathcal{L}_T(v)$ (or S)

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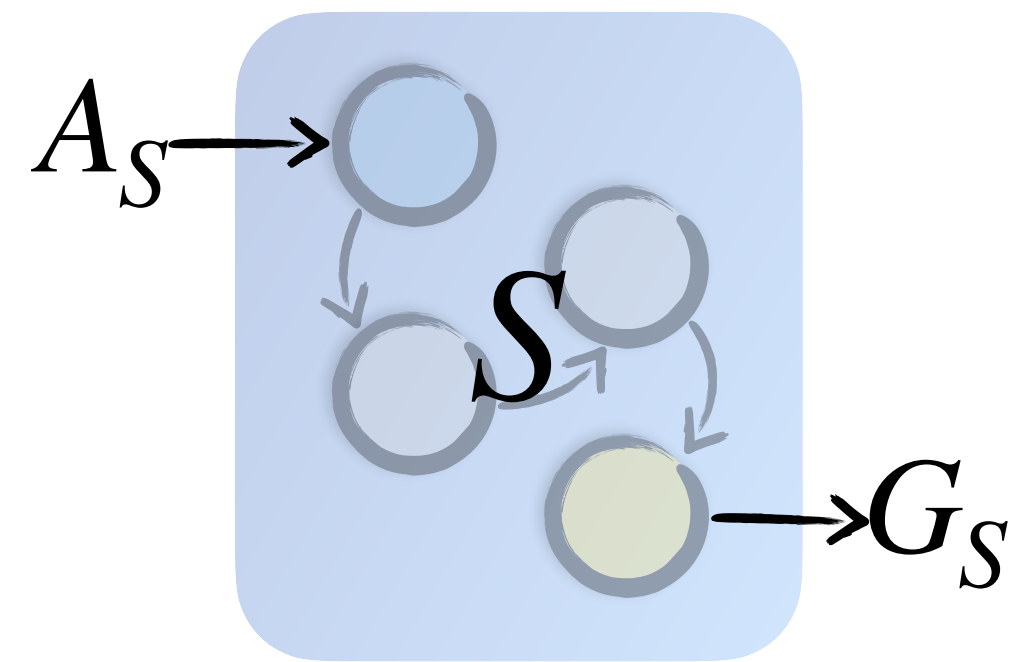
$$P_T = \mathcal{L}(v) < 10 \wedge \mathcal{L}(d) < 10$$

- By the two cases, for all nodes v ,
 $\mathcal{L}_R(v) \subseteq \mathcal{L}_T(v)$ (or S)
- Then, since property P holds for S and T (by the safety check), it must also hold for R

An **Assume-Guarantee** Proof Rule

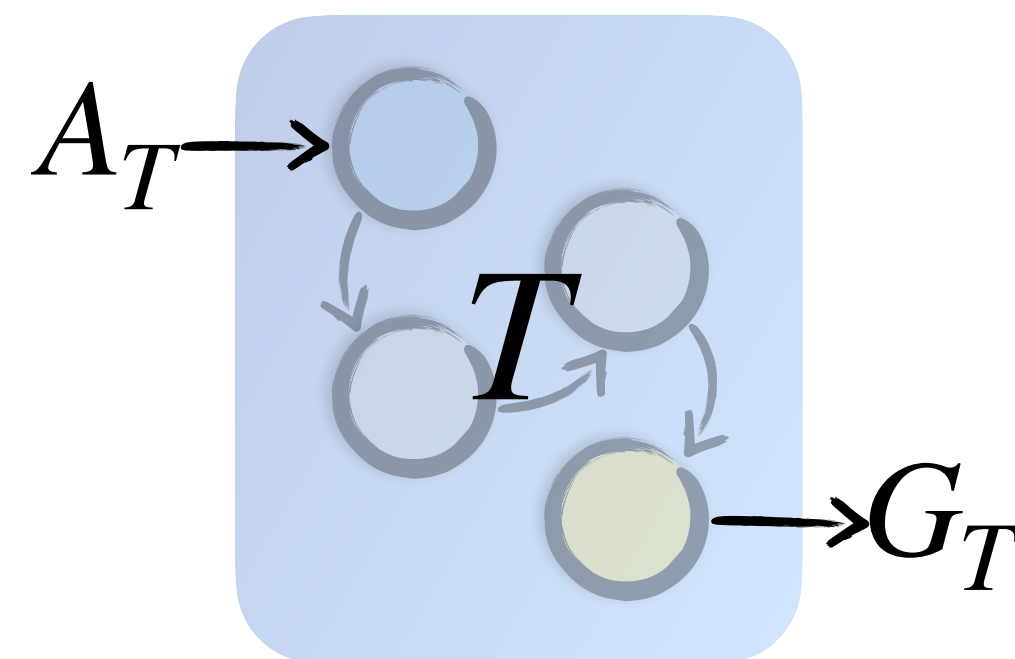
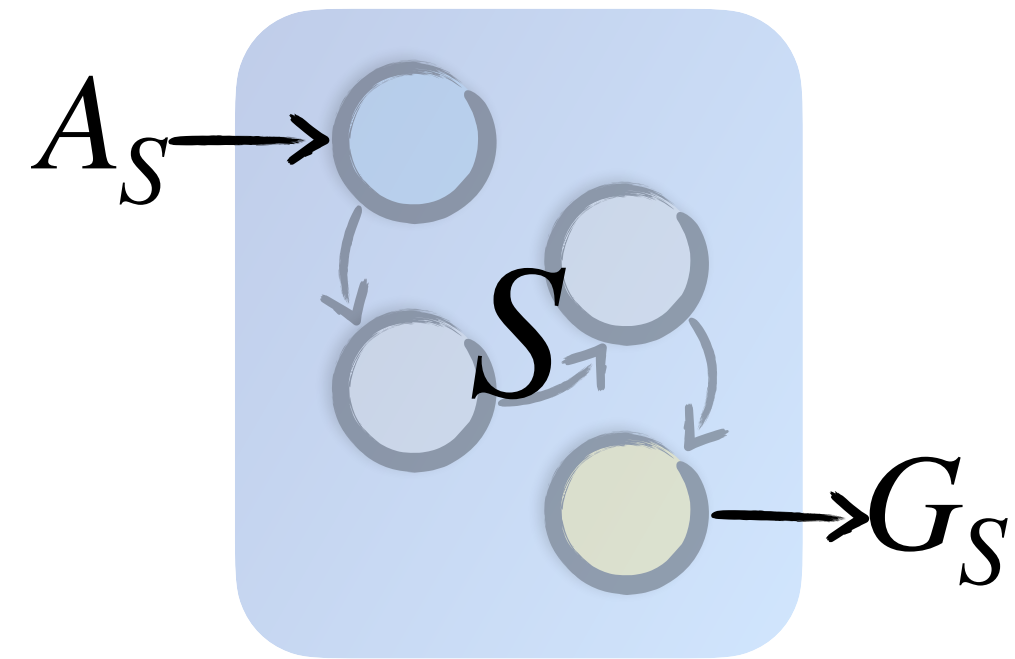
[Giannakopoulou et al., *Handbook of Model Checking*. 2018]

An **Assume-Guarantee** Proof Rule



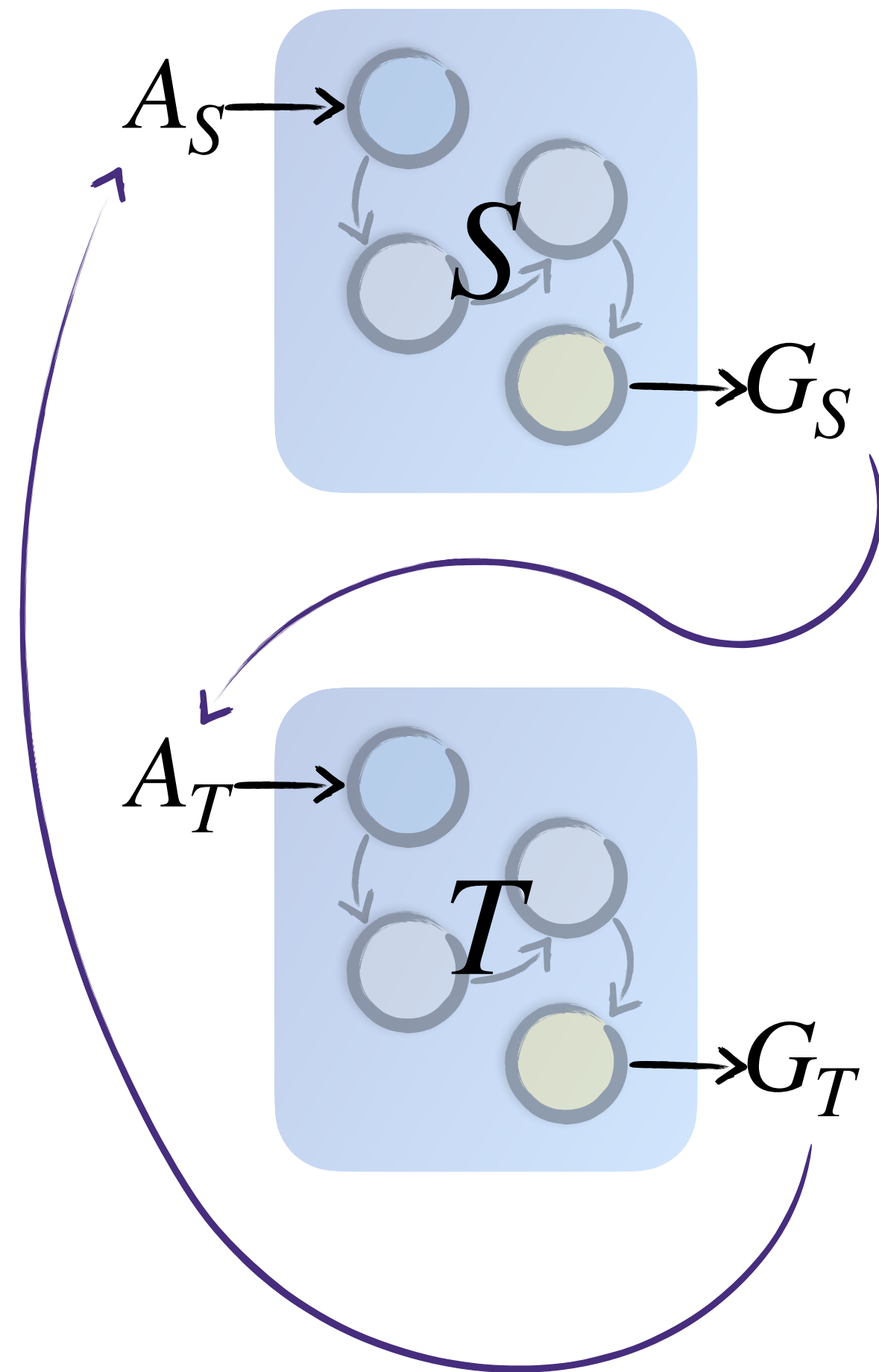
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An **Assume-Guarantee** Proof Rule



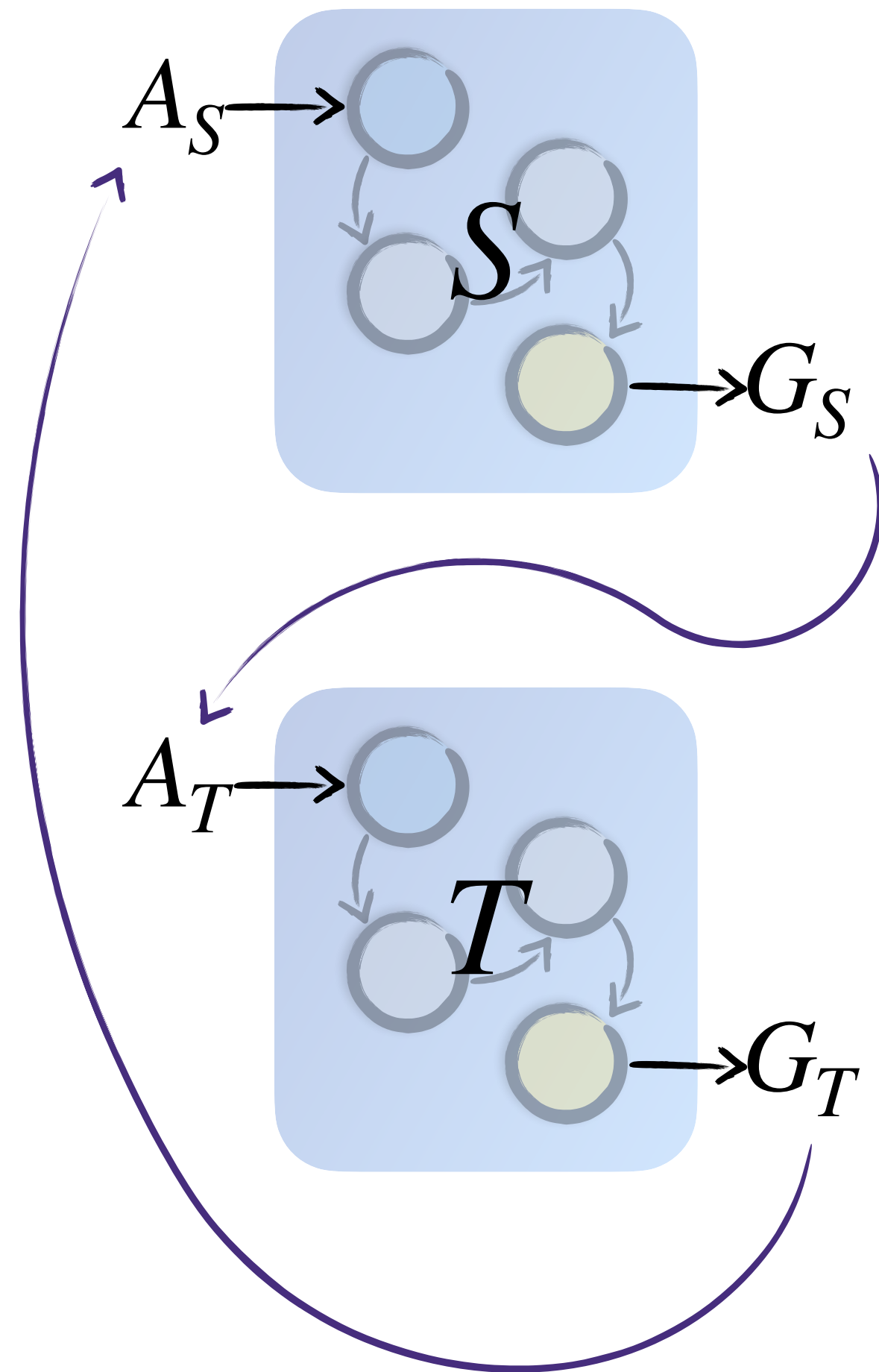
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An Assume-Guarantee Proof Rule



[Giannakopoulou et al., *Handbook of Model Checking*. 2018]

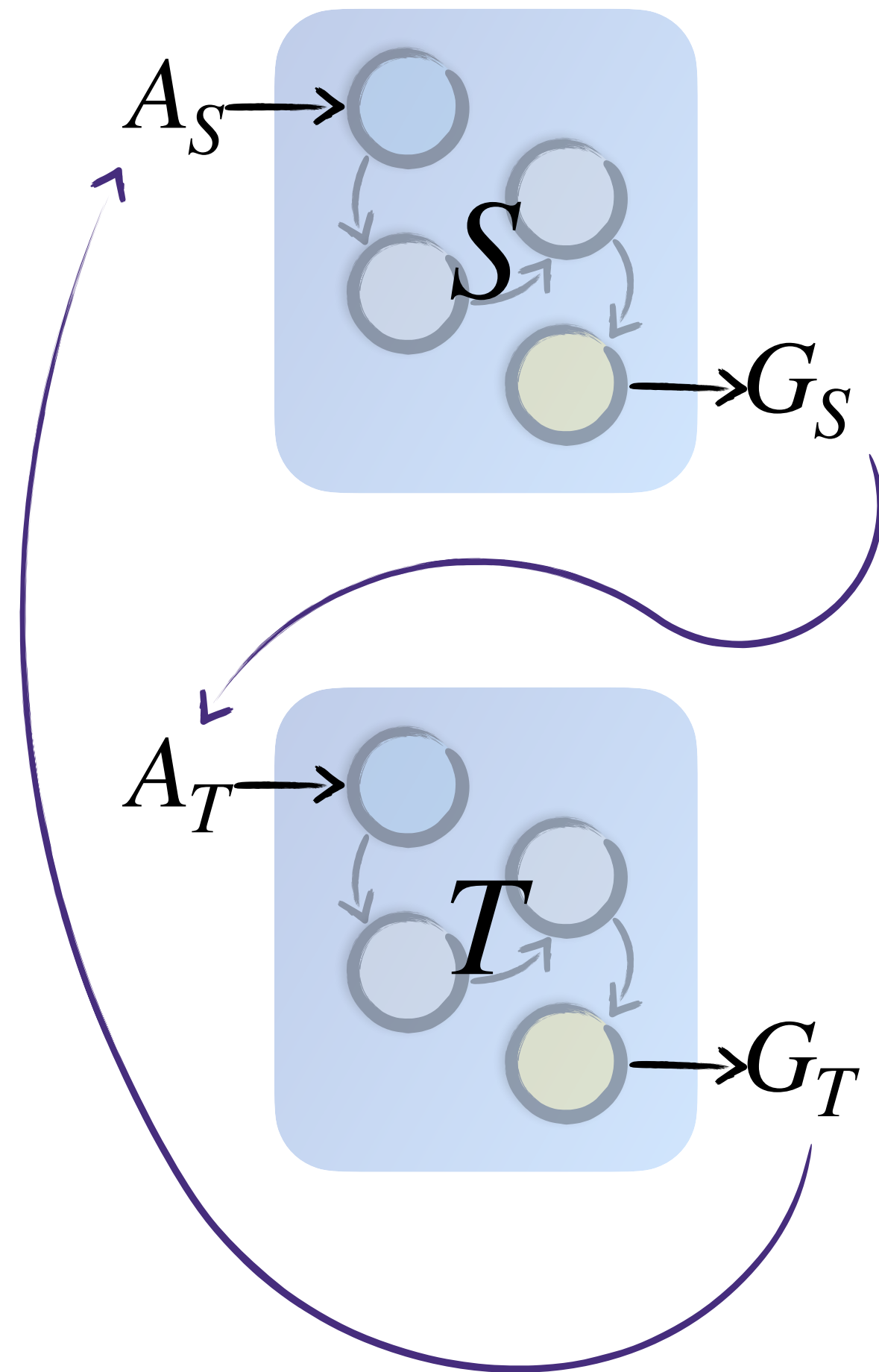
An Assume-Guarantee Proof Rule



$$\begin{array}{l}
 \langle A_S \rangle S \langle G_S \rangle \\
 \langle A_T \rangle T \langle G_T \rangle \\
 G_T \Rightarrow A_S \\
 G_S \Rightarrow A_T \\
 \langle A_S \rangle S \langle P_S \rangle \\
 \langle A_T \rangle T \langle P_T \rangle \\
 \langle \text{true} \rangle S \langle G_S \rangle \\
 \langle \text{true} \rangle T \langle G_T \rangle \\
 \hline
 \langle \text{true} \rangle S \parallel T \langle P_S \wedge P_T \rangle
 \end{array}$$

[Giannakopoulou et al., *Handbook of Model Checking*. 2018]

An Assume-Guarantee Proof Rule

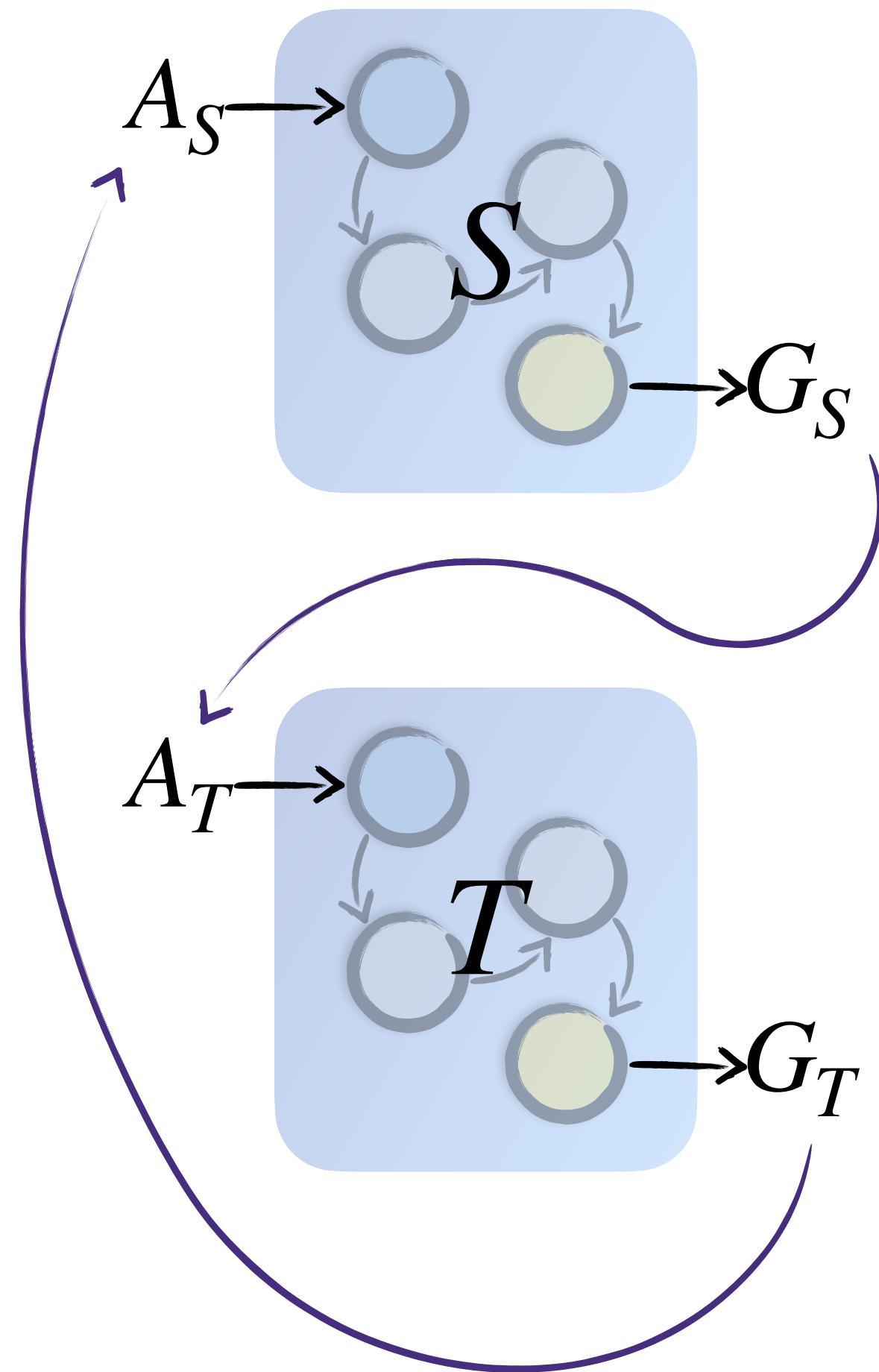


$$\left. \begin{array}{l}
 \langle A_S \rangle S \langle G_S \rangle \\
 \langle A_T \rangle T \langle G_T \rangle \\
 G_T \Rightarrow A_S \\
 G_S \Rightarrow A_T
 \end{array} \right\} \text{Inductiveness check}$$

$$\frac{
 \begin{array}{l}
 \langle A_S \rangle S \langle P_S \rangle \\
 \langle A_T \rangle T \langle P_T \rangle \\
 \langle \text{true} \rangle S \langle G_S \rangle \\
 \langle \text{true} \rangle T \langle G_T \rangle
 \end{array}
 }{
 \langle \text{true} \rangle S \parallel T \langle P_S \wedge P_T \rangle
 }$$

[Giannakopoulou et al., *Handbook of Model Checking*. 2018]

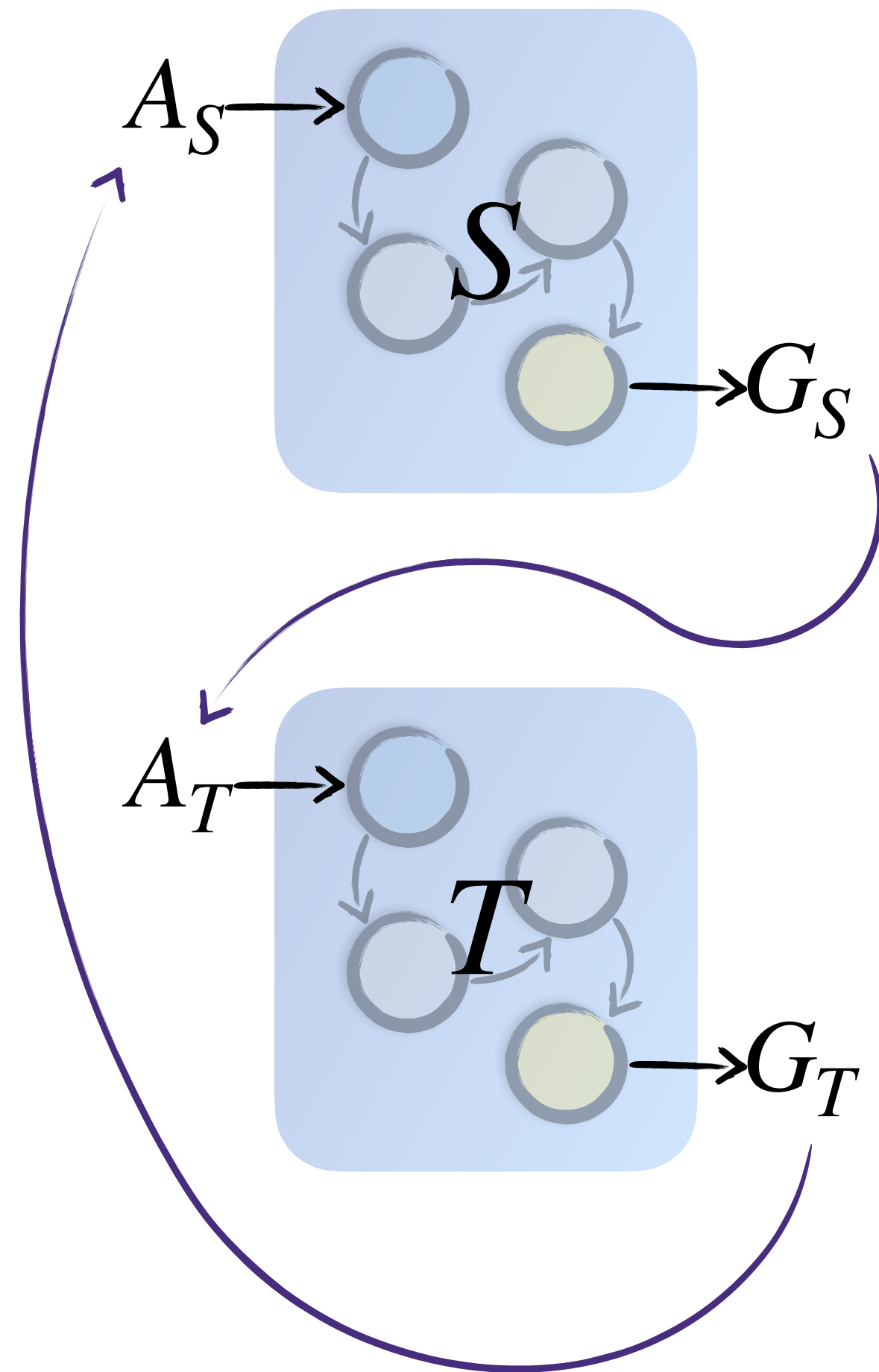
An Assume-Guarantee Proof Rule



$$\begin{array}{c}
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 G_T \Rightarrow A_S \\
 G_S \Rightarrow A_T \\
 \langle A_S \rangle S \langle P_S \rangle \\
 \langle A_T \rangle T \langle P_T \rangle \\
 \langle \text{true} \rangle S \langle G_S \rangle \\
 \langle \text{true} \rangle T \langle G_T \rangle \\
 \hline
 \langle \text{true} \rangle S \parallel T \langle P_S \wedge P_T \rangle
 \end{array}
 \left. \begin{array}{l} \\ \\ \\ \\ \\ \\ \\ \end{array} \right\} \begin{array}{l} \text{Inductiveness check} \\ \\ \text{Safety check} \end{array}$$

[Giannakopoulou et al., *Handbook of Model Checking*. 2018]

An Assume-Guarantee Proof Rule

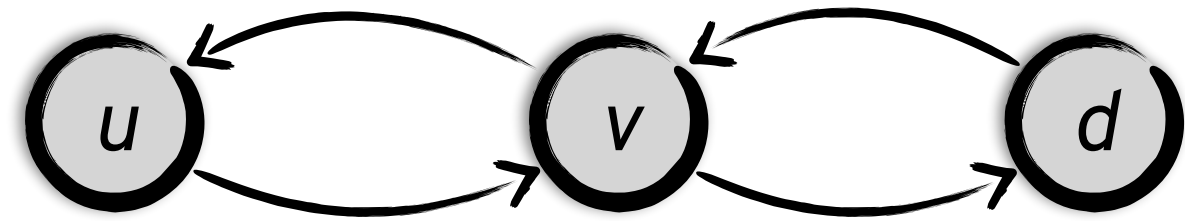


$$\begin{array}{l}
 \left. \begin{array}{l}
 \langle A_S \rangle S \langle G_S \rangle \\
 \langle A_T \rangle T \langle G_T \rangle \\
 G_T \Rightarrow A_S \\
 G_S \Rightarrow A_T
 \end{array} \right\} \text{Inductiveness check} \\
 \\
 \left. \begin{array}{l}
 \langle A_S \rangle S \langle P_S \rangle \\
 \langle A_T \rangle T \langle P_T \rangle
 \end{array} \right\} \text{Safety check} \\
 \\
 \left. \begin{array}{l}
 \langle \text{true} \rangle S \langle G_S \rangle \\
 \langle \text{true} \rangle T \langle G_T \rangle
 \end{array} \right\} \text{Initial check} \\
 \hline
 \langle \text{true} \rangle S \parallel T \langle P_S \wedge P_T \rangle
 \end{array}$$

[Giannakopoulou et al., *Handbook of Model Checking*. 2018]

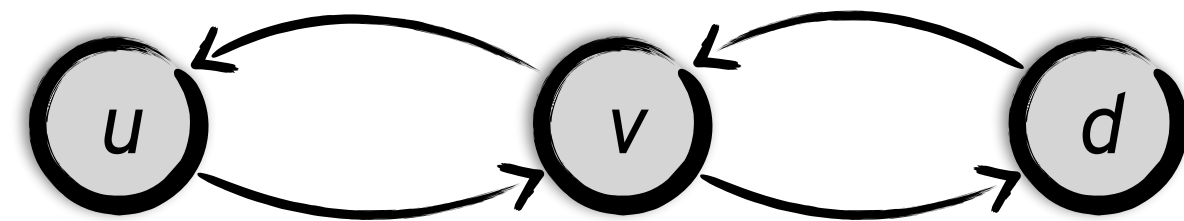
Implementing Kirigami in NV

Implementing Kirigami in NV



[Beckett et al., NetPL 2019]
[Giannarakis et al., PLDI 2020]

Implementing Kirigami in NV



uvd.nv

```
type attribute = int

(* Number of nodes in network topology *)
(* 0 = d; 1 = v; 2 = u *)
let nodes = 3

(* List of edges in network topology *)
let edges = { 0=1; 1=2; }

(* The merge function for receiving attributes *)
let merge node x y =
  if x < y then x else y

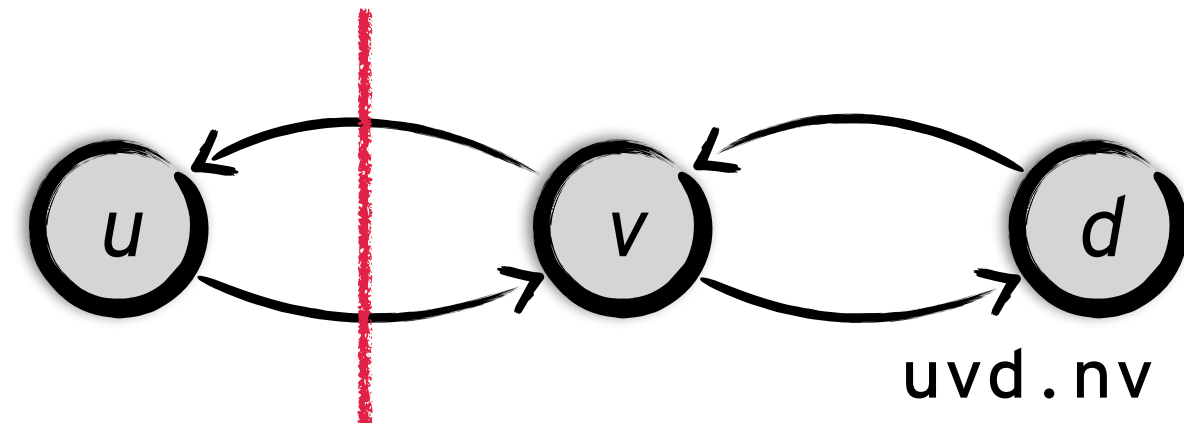
(* The trans function for sending attributes *)
let trans edge x = x + 1

(* The initial state of the network *)
let init node =
  match node with
  | 0n -> 0
  | _ -> 10

(* The assertion on each node's solution *)
let assert node x = x < 10
```

[Beckett et al., NetPL 2019]
[Giannarakis et al., PLDI 2020]

Implementing Kirigami in NV



u v d.nv

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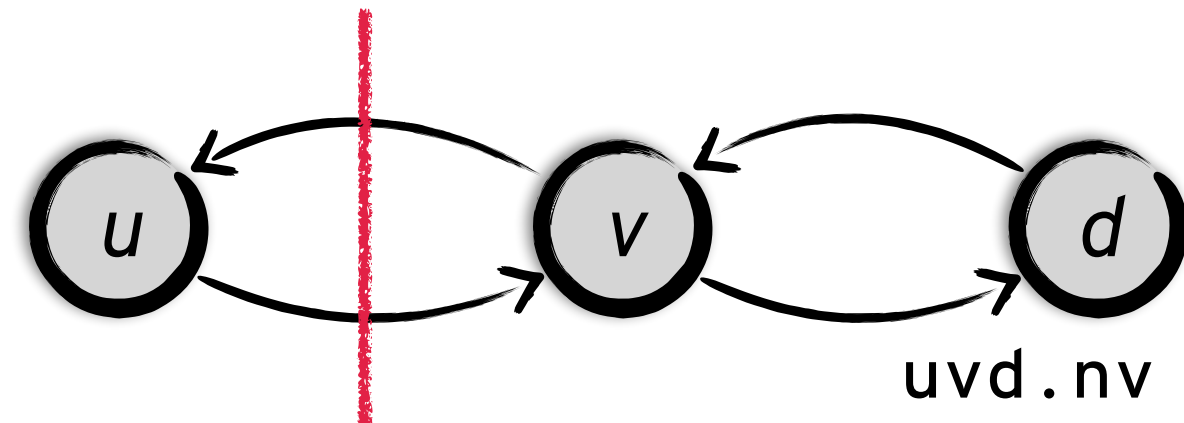
(* The assertion on each node's solution *)
let assert node x = x < 10
```

u v d-kirigami.nv

```
include "u v d.nv"
```

[Beckett et al., NetPL 2019]
[Giannarakis et al., PLDI 2020]

Implementing Kirigami in NV



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uvd-kirigami.nv

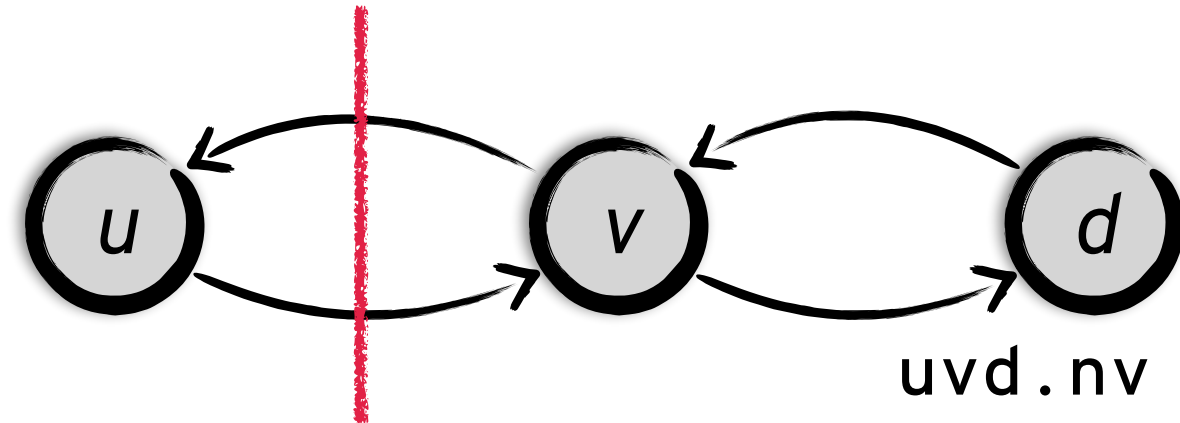
```
include "uvd.nv"

(* Associate each node with a partition *)
let partition node = match node with
| 0n -> 0
| 1n -> 0
| 2n -> 1
```

[Beckett et al., NetPL 2019]

[Giannarakis et al., PLDI 2020]

Implementing Kirigami in NV



```
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let assert node x = x < 10
```

uvd-kirigami.nv

```
include "uvd.nv"

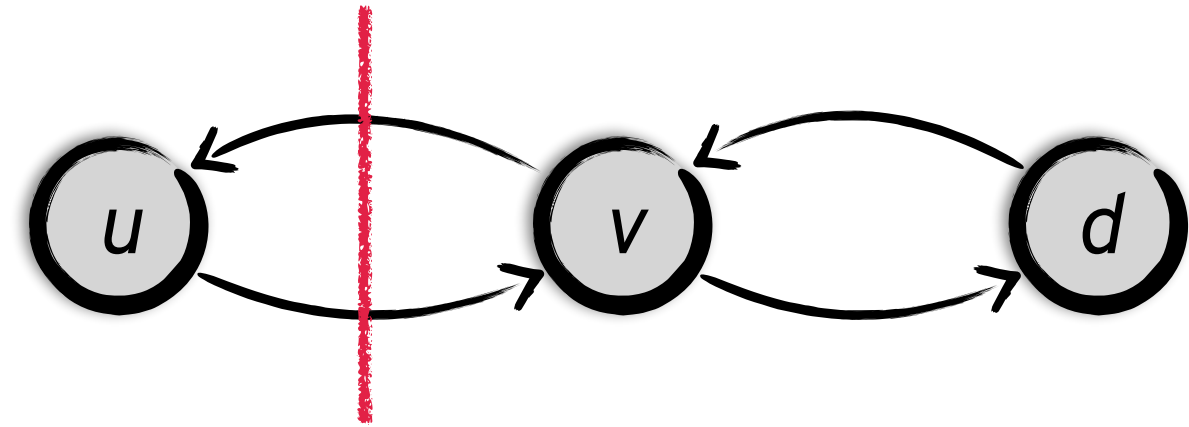
(* Associate each node with a partition *)
let partition node = match node with
| 0n -> 0
| 1n -> 0
| 2n -> 1

(* Associate each edge with a hypothesis *)
let interface edge = match edge with
| 1~2 -> Some (fun a -> a = 1 || a = 2)
| 2~1 -> Some (fun a -> a >= 1)
| _ -> None
```

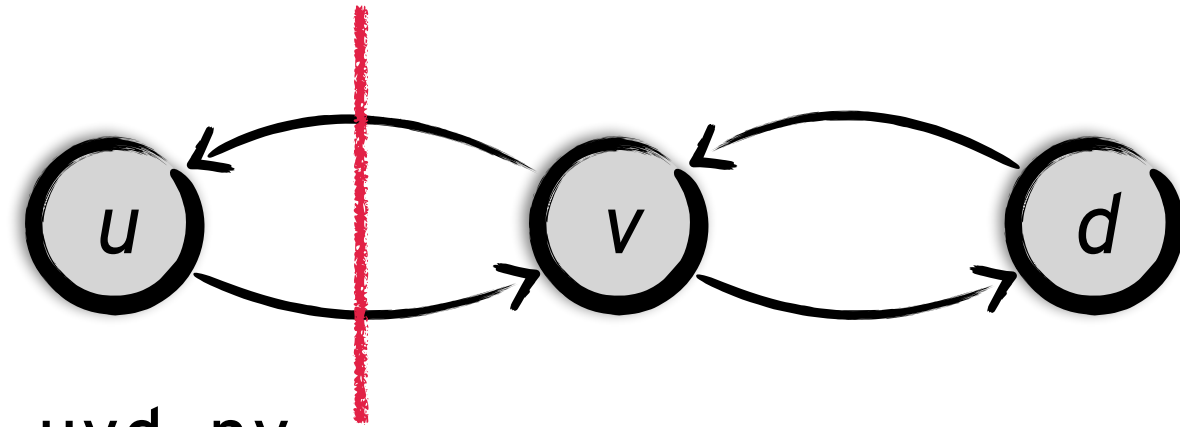
[Beckett et al., NetPL 2019]

[Giannarakis et al., PLDI 2020]

Implementing Kirigami in NV



Implementing Kirigami in NV



uvd.nv

```
type attribute = int

(* Number of nodes in network topology *)
(* 0 = d; 1 = v; 2 = u *)
let nodes = 3

(* List of edges in network topology *)
let edges = { 0=1; 1=2; }

(* The merge function for receiving attributes *)
let merge node x y =
  if x < y then x else y

(* The trans function for sending attributes *)
let trans edge x = x + 1

(* The initial state of the network *)
let init node =
  match node with
  | 0n -> 0
  | _ -> 10

(* The assertion on each node's solution *)
let assert node x = x < 10
```

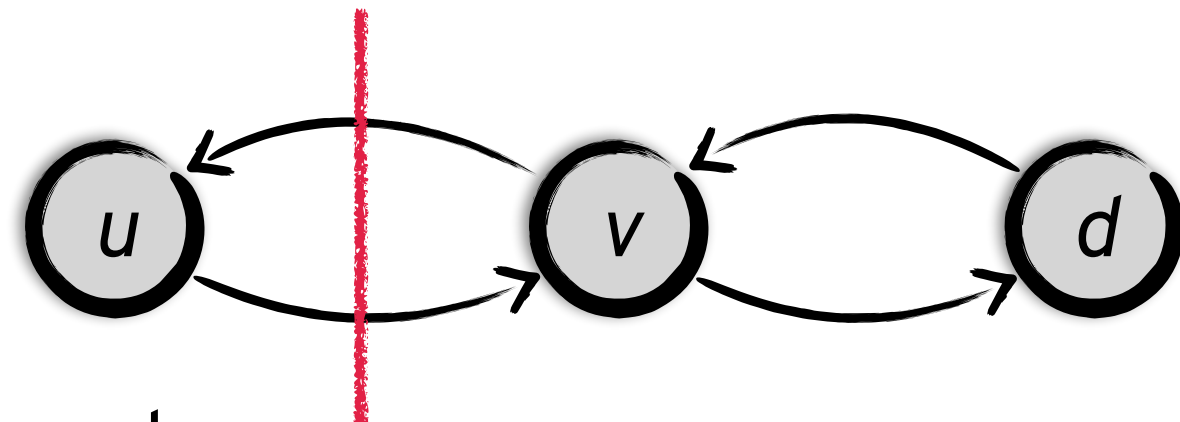
uvd-part.nv

```
include "uvd.nv"

(* Associate each node with a partition *)
let partition node = match node with
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| 1n -> 0
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Implementing Kirigami in NV



uvd.nv

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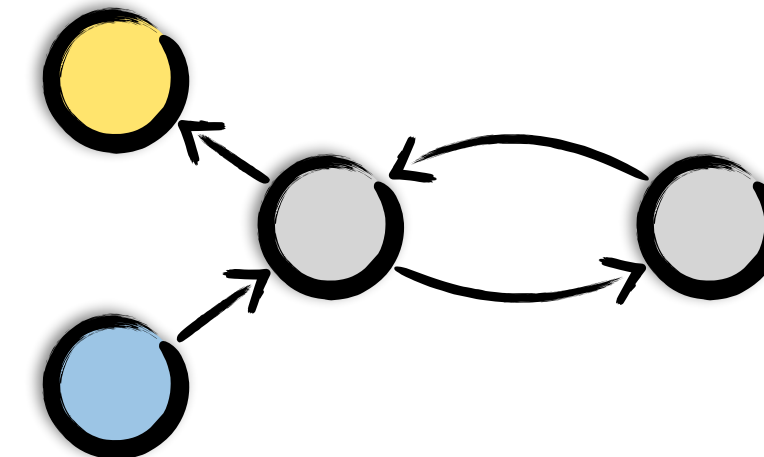
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let interface edge = match edge with
| 1~2 -> Some (fun a -> a = 1 || a = 2)
| 2~1 -> Some (fun a -> a >= 1)
| _ -> None
```



```
(* Partition 0 *)
(* Constraints over inputs *)
symbolic h_2_1 : int
require (fun a -> a >= 1) h_2_1

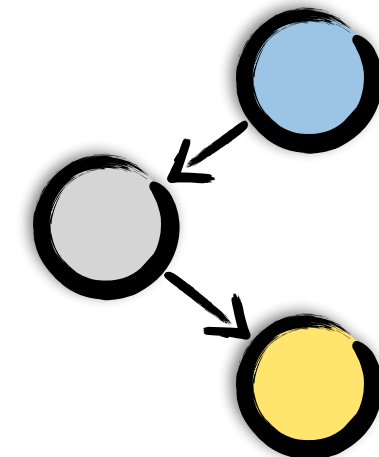
let nodes = (* updated nodes *)
let edges = (* updated edges *)
```

```
let merge node x y =
  if x < y then x else y

let trans edge x =
  match edge with
  | (* input edge *) -> x
  | _ -> x + 1
```

```
(* The initial state of the network *)
let init node =
  match node with
  | (* input of 2 *) -> h_2_1
  | (* ...as before... *)
```

```
(* The assertion on each node's solution *)
let assert node x =
  match node with
  | (* output of 1 *) -> (fun a -> a = 1 || a = 2) x
  | _ -> x < 10
```



```
(* Partition 1 *)
(* Constraints over inputs *)
symbolic h_1_2 : int
require (fun a -> a = 1 || a = 2) h_1_2

let nodes = (* updated nodes *)
let edges = (* updated edges *)
```

```
let merge node x y =
  if x < y then x else y

let trans edge x =
  match edge with
  | (* input edge *) -> x
  | _ -> x + 1
```

```
(* The initial state of the network *)
let init node =
  match node with
  | (* input of 1 *) -> h_1_2
  | (* ...as before... *)
```

```
(* The assertion on each node's solution *)
let assert node x =
  match node with
  | (* output of 2 *) -> (fun a -> a >= 1) x
  | _ -> x < 10
```

Internal representation

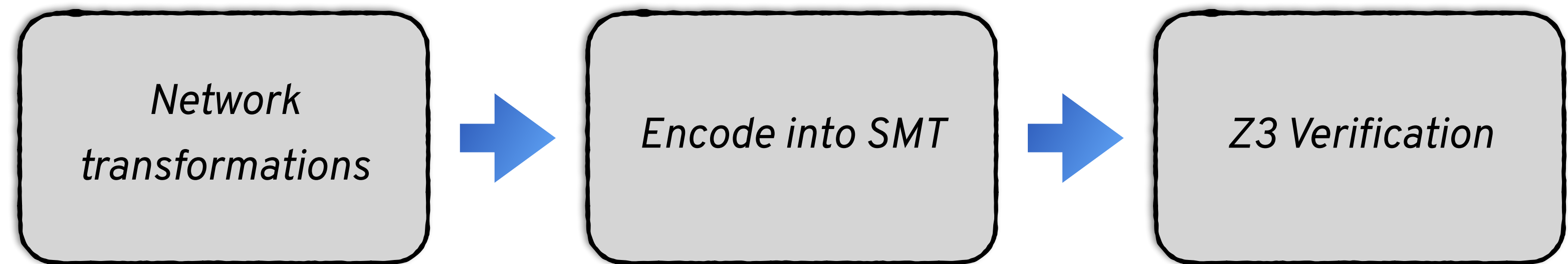
Running a Query in NV

[Beckett et al., NetPL 2019]

[Giannarakis et al., PLDI 2020]

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Running a Query in NV

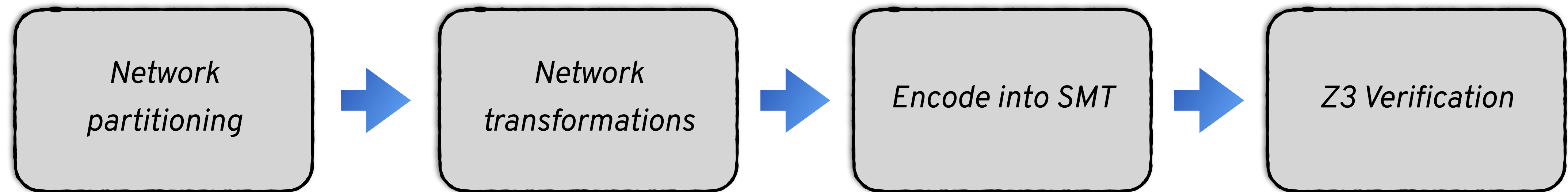


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Running a Query in NV

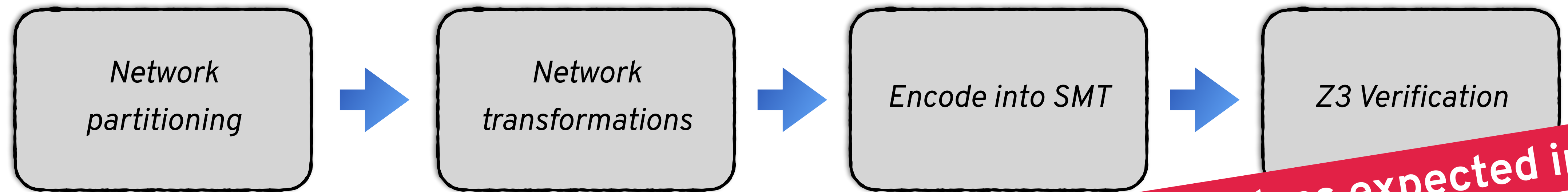


[Beckett et al., NetPL 2019]

[Giannarakis et al., PLDI 2020]

[De Moura and Bjørner, TACAS 2008]

Running a Query in NV



All the examples we've seen thus far have been verified and work as expected in NV!

[Beckett et al., NetPL 2019]

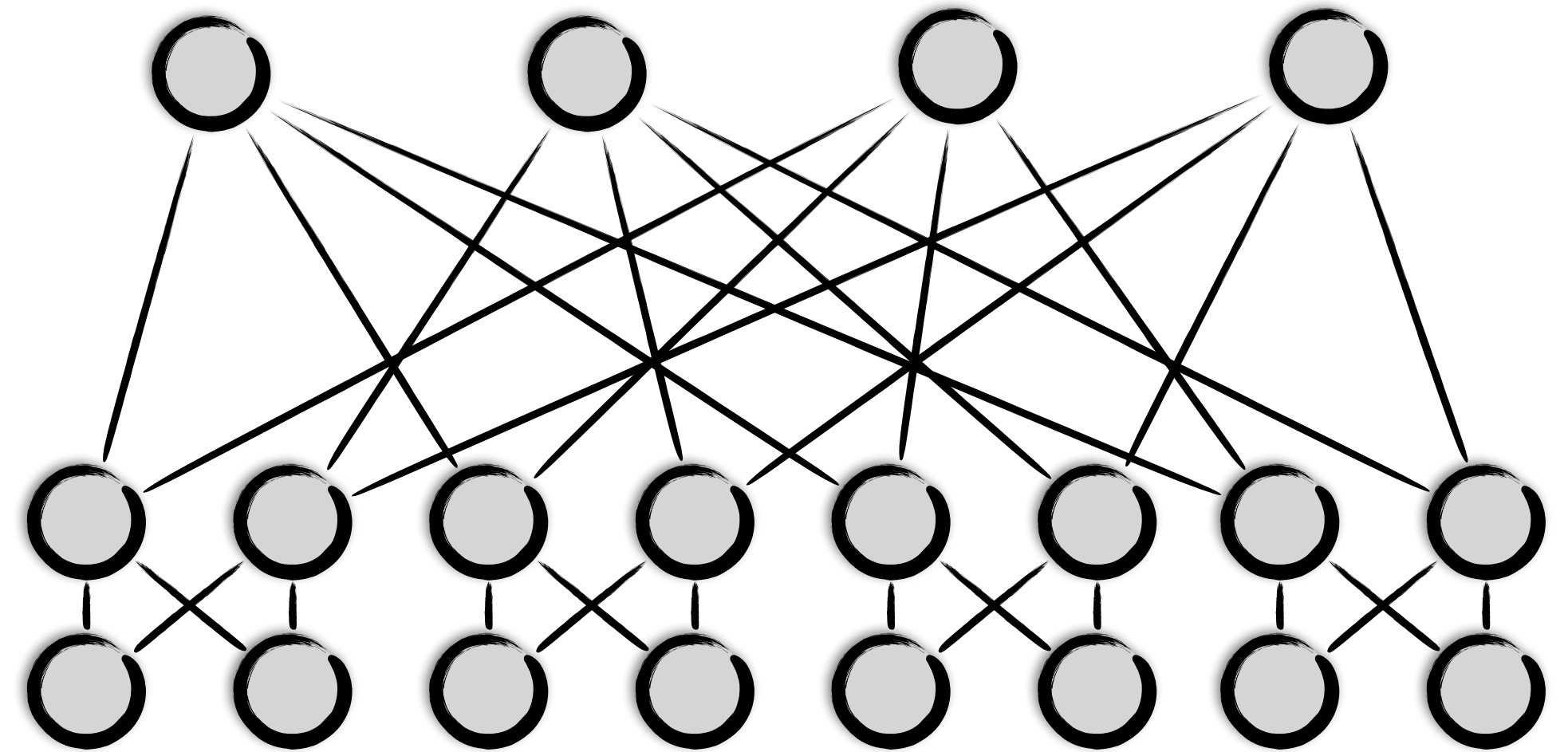
[Giannarakis et al., PLDI 2020]

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Results

Fattree Reachability

Case Study



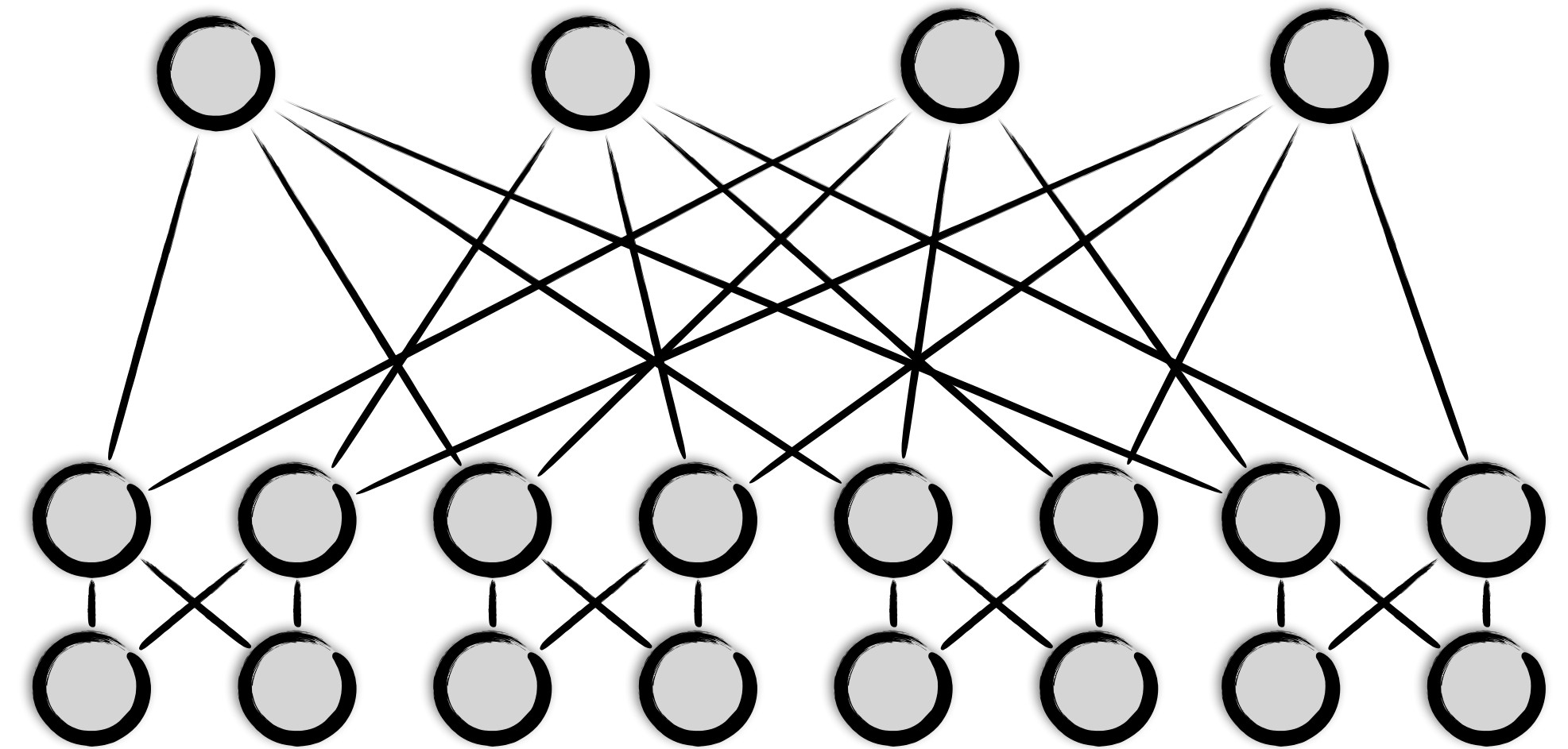
[Fogel et al., NSDI 2015]

[Leiserson, IEEE TC 1985]

Fattree Reachability

Case Study

- Common data centre network



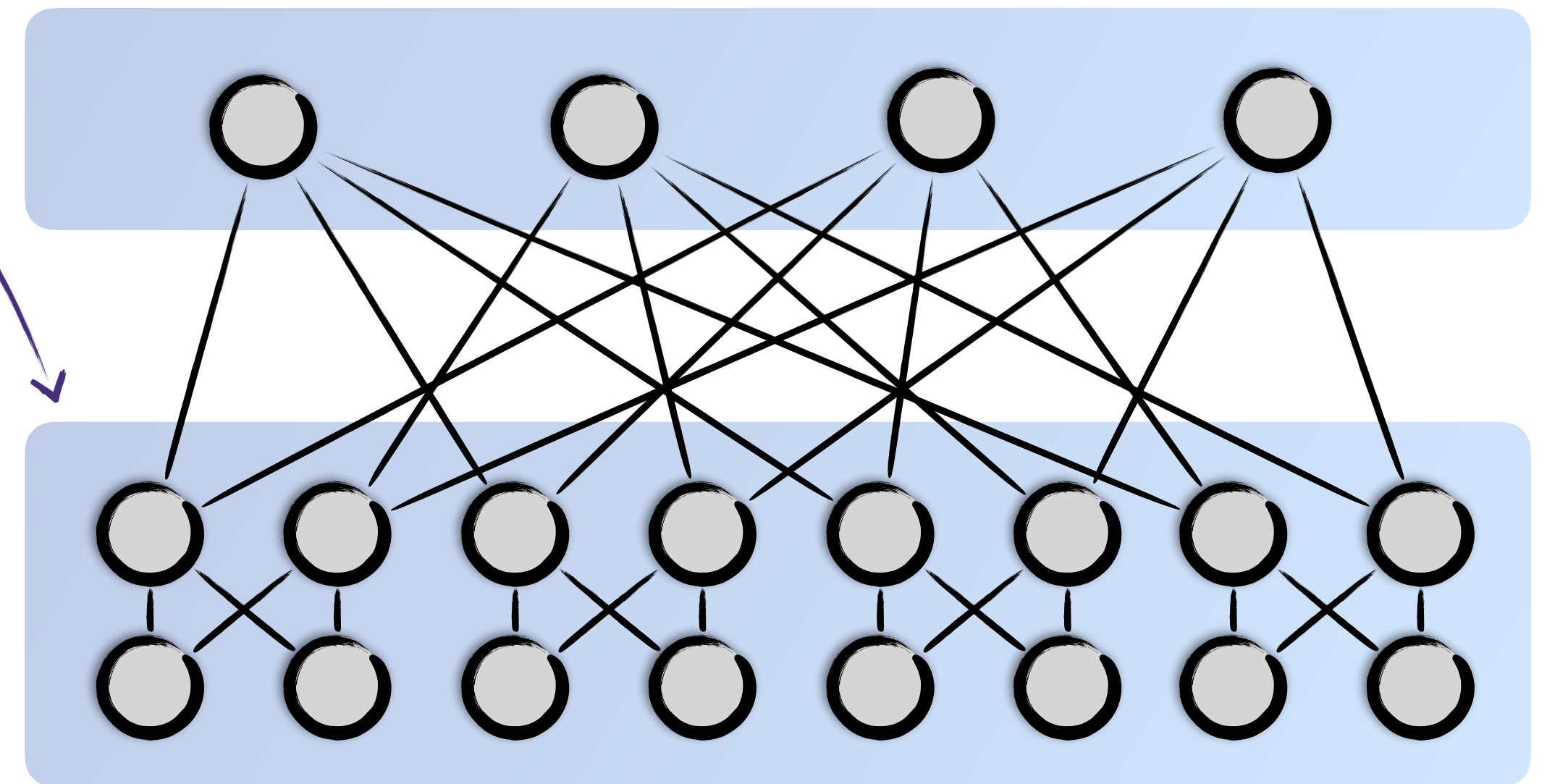
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Fattree Reachability

Case Study

- Common data centre network
- Hierarchical design using *spines* and *pods*

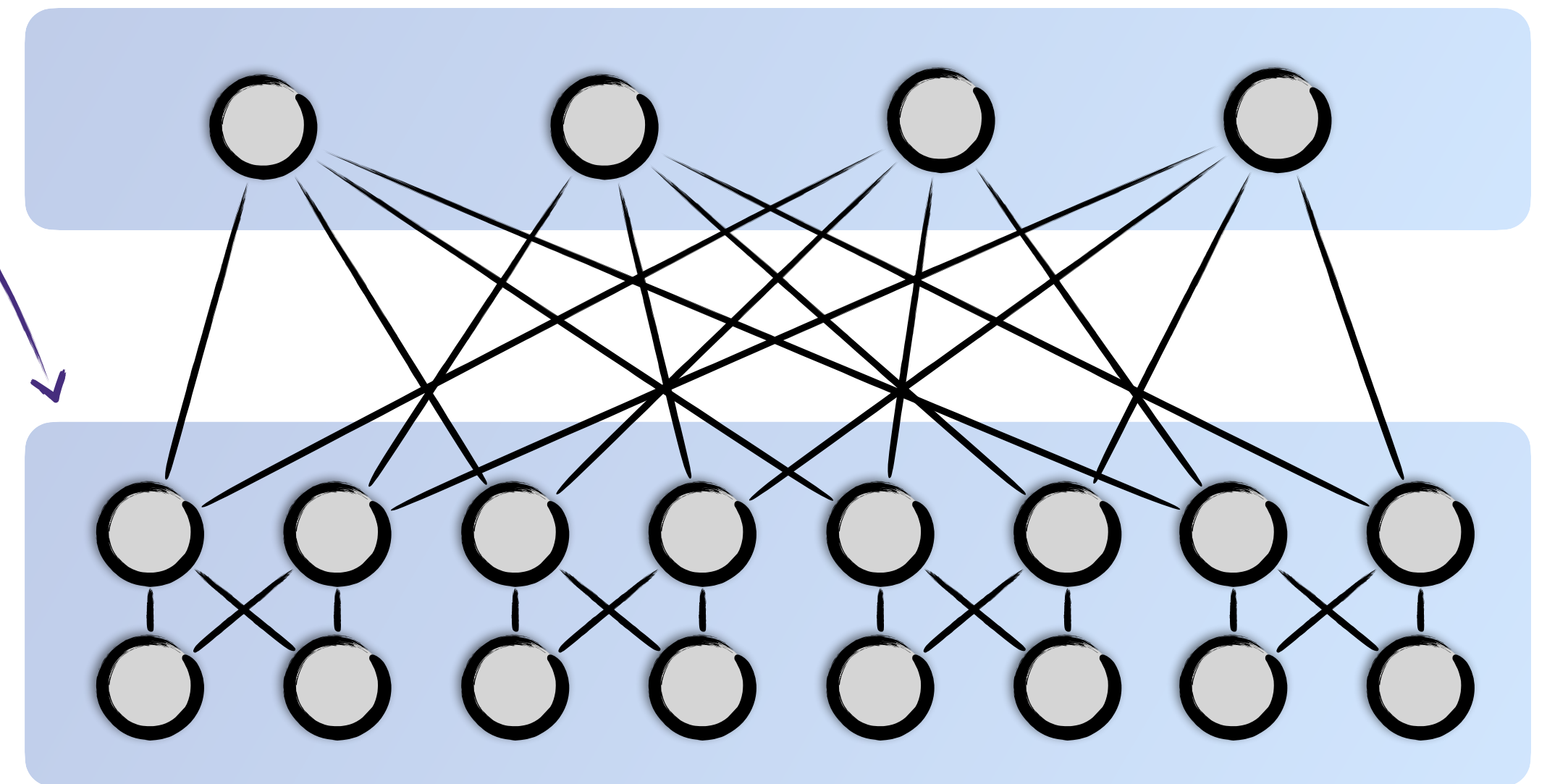


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Fattree Reachability

Case Study

- Common data centre network
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- Real-world infrastructures can have over 10,000 nodes

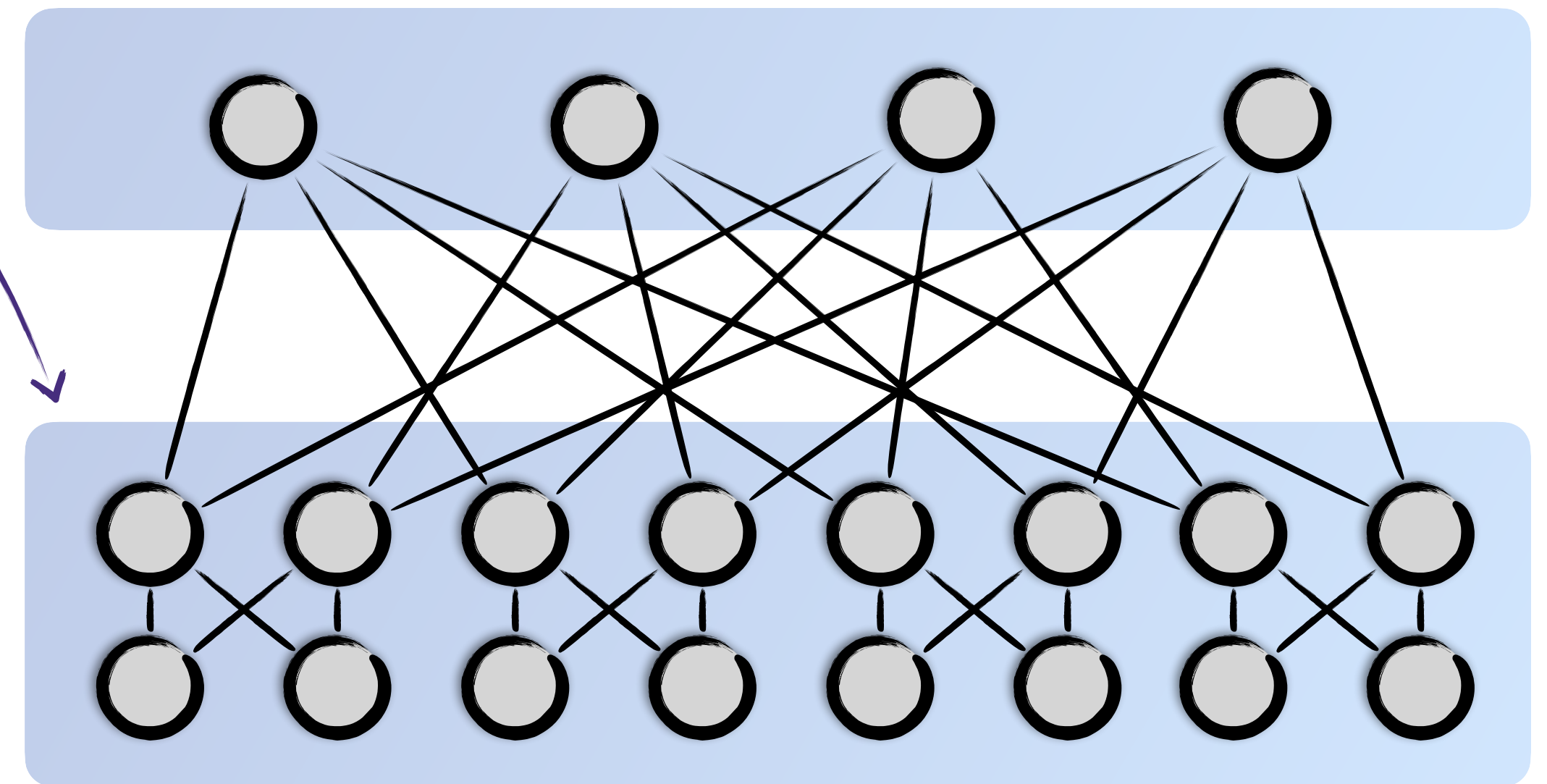


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Fattree Reachability

Case Study

- Common data centre network
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- Real-world infrastructures can have over 10,000 nodes
- Synthetic networks ranged from 20 to 500 nodes

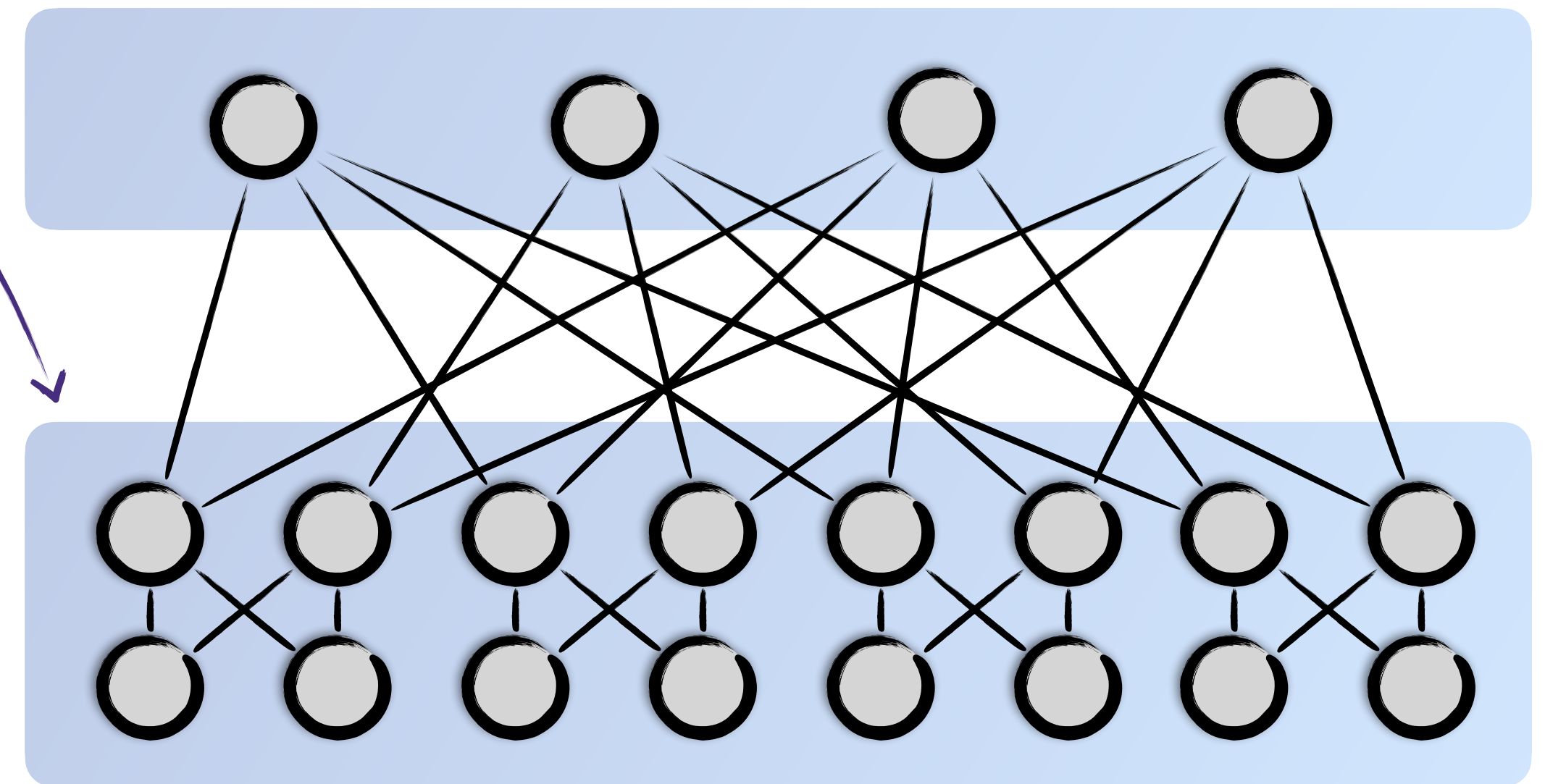


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Fattree Reachability

Case Study

- Common data centre network
- Hierarchical design using *spines* and *pods*
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- Modelled shortest-path routing

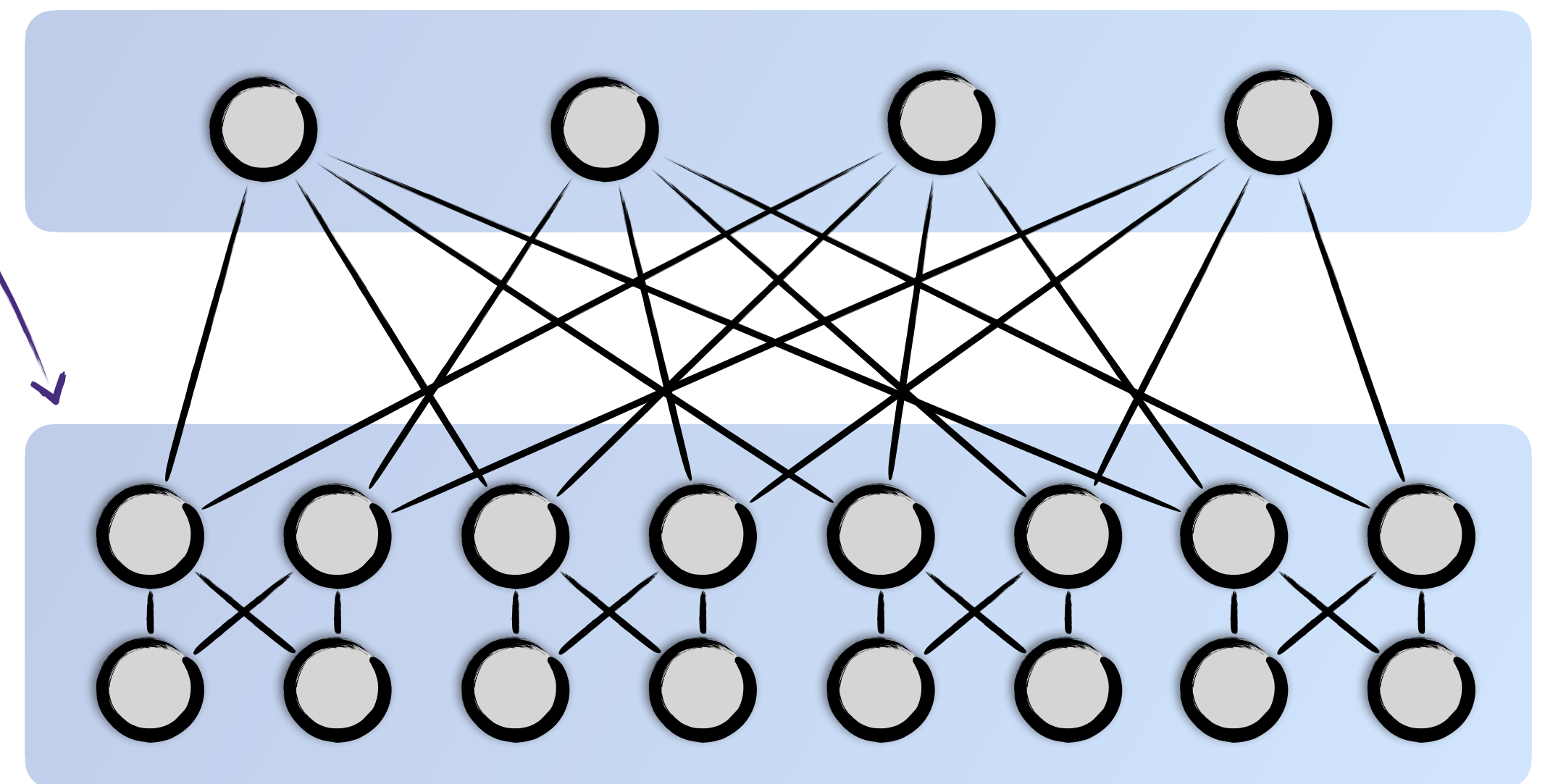


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Fattree Reachability

Case Study

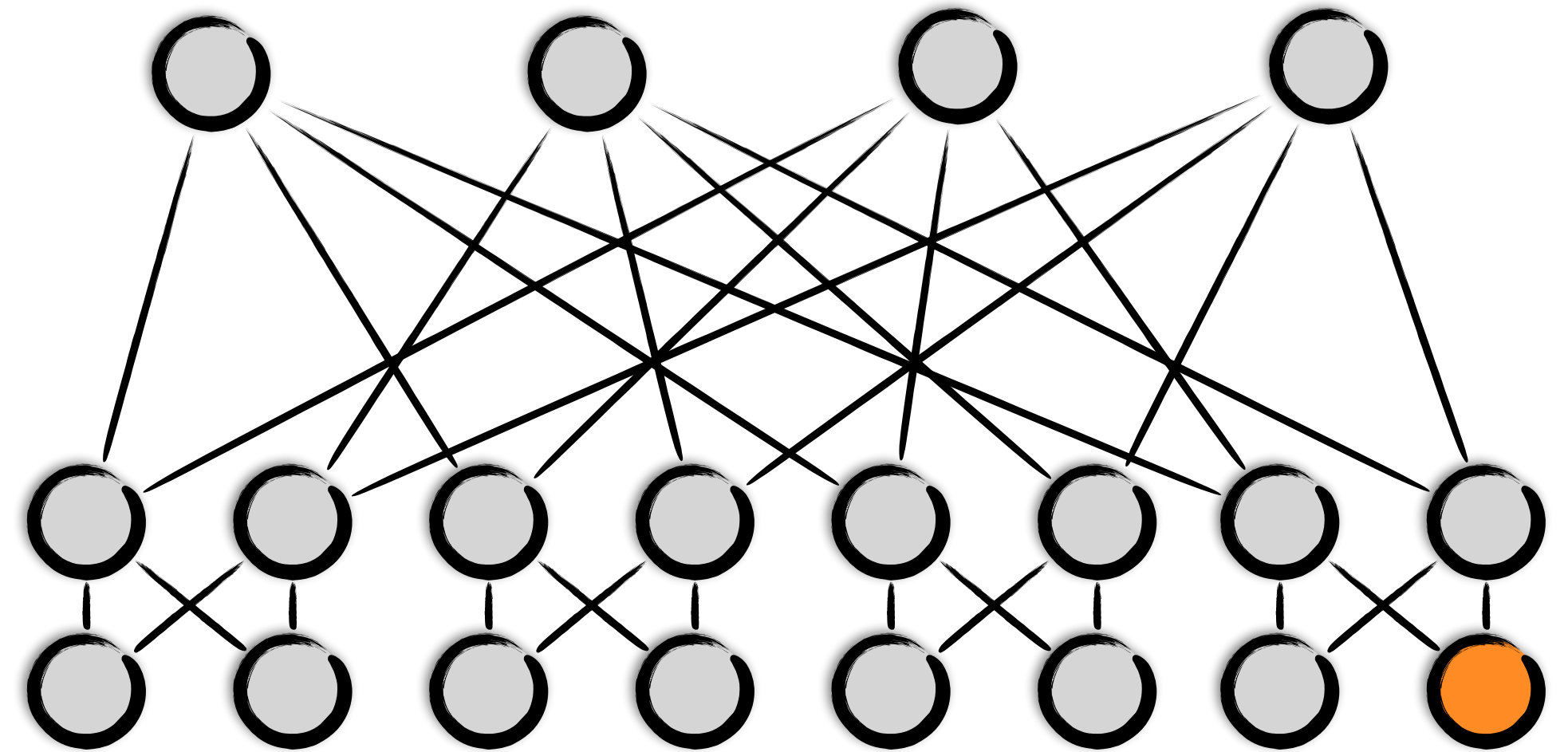
- Common data centre network
- Hierarchical design using *spines* and *pods*
- Real-world infrastructures can have over 10,000 nodes
 - Synthetic networks ranged from 20 to 500 nodes
- Modelled shortest-path routing
- Property: *reachability of a single destination*



[Fogel et al., NSDI 2015]
[Leiserson, IEEE TC 1985]

Fattree Reachability

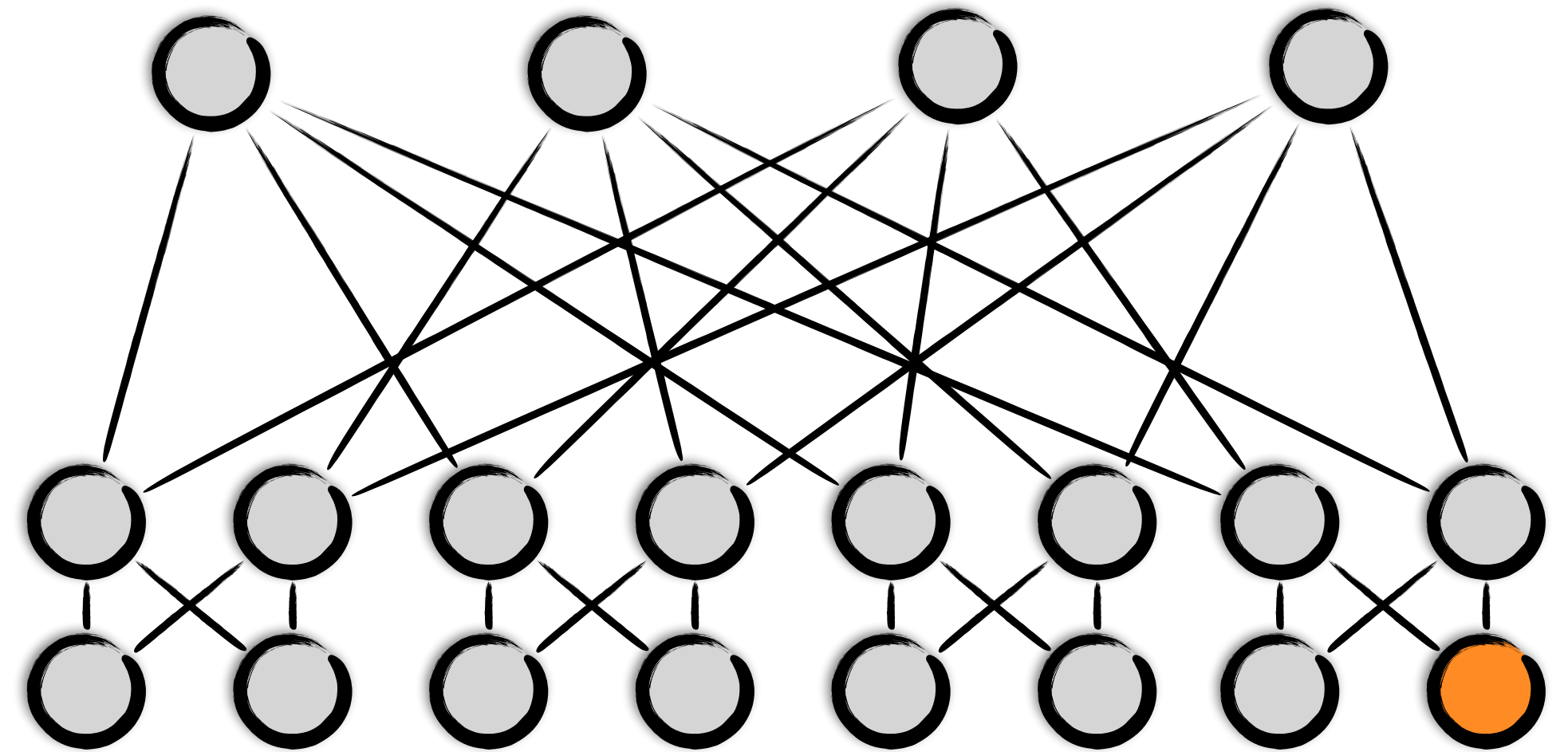
Case Study



Fattree Reachability

Case Study

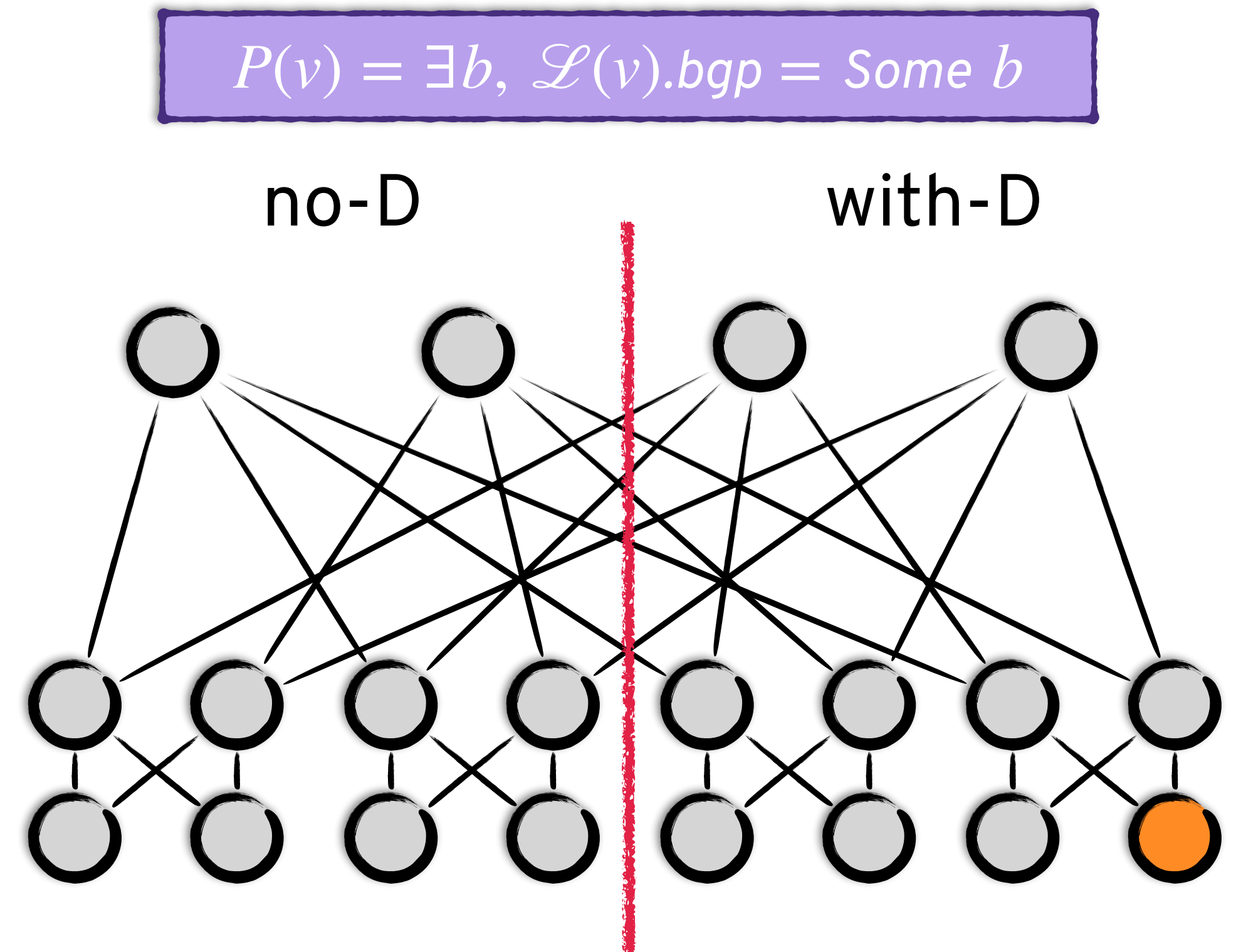
$$P(v) = \exists b, \mathcal{L}(v).bgp = \text{Some } b$$



Fattree Reachability

Case Study

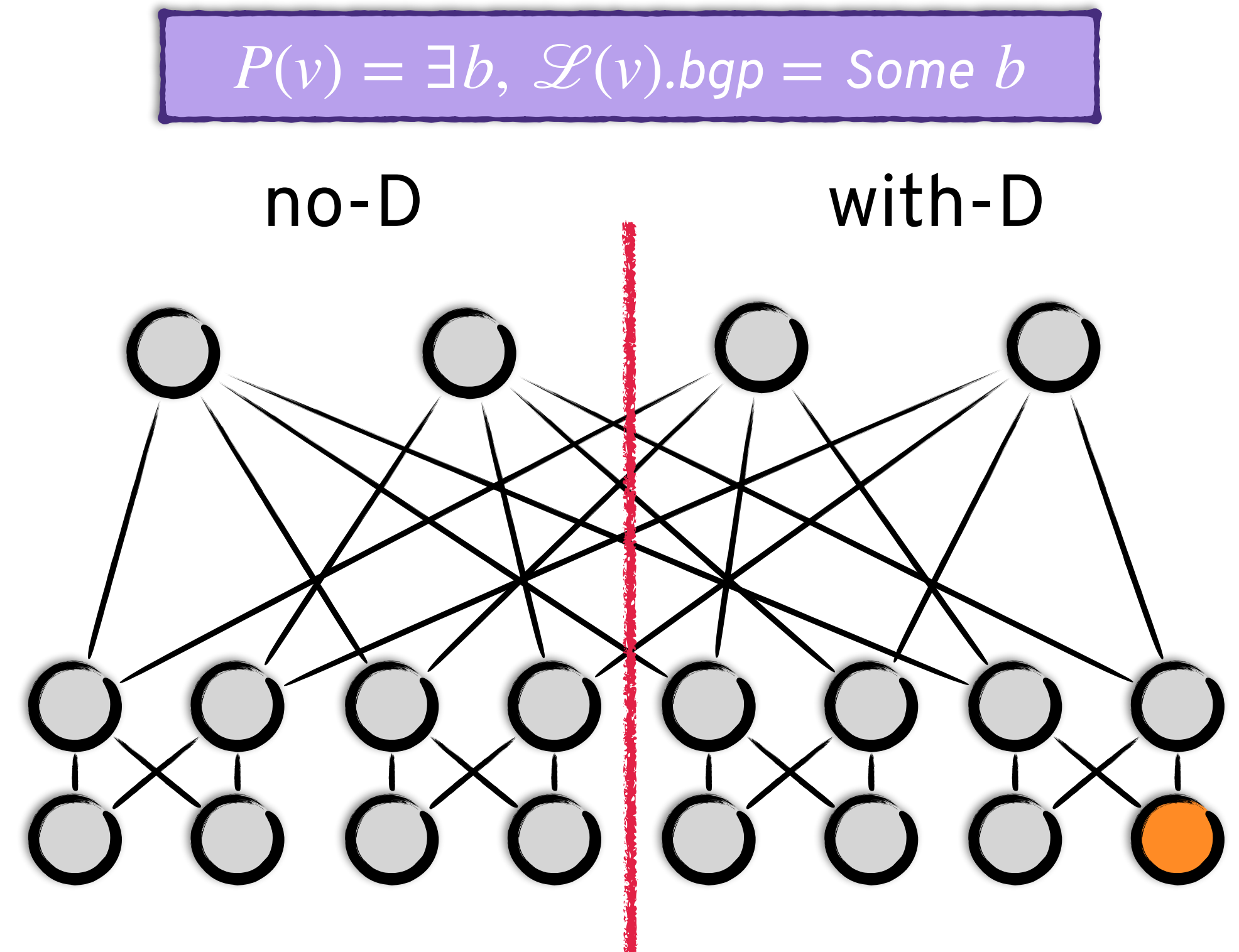
- Partition vertically in half
 - Destination side (with-D)
 - Non-destination side (no-D)



Fattree Reachability

Case Study

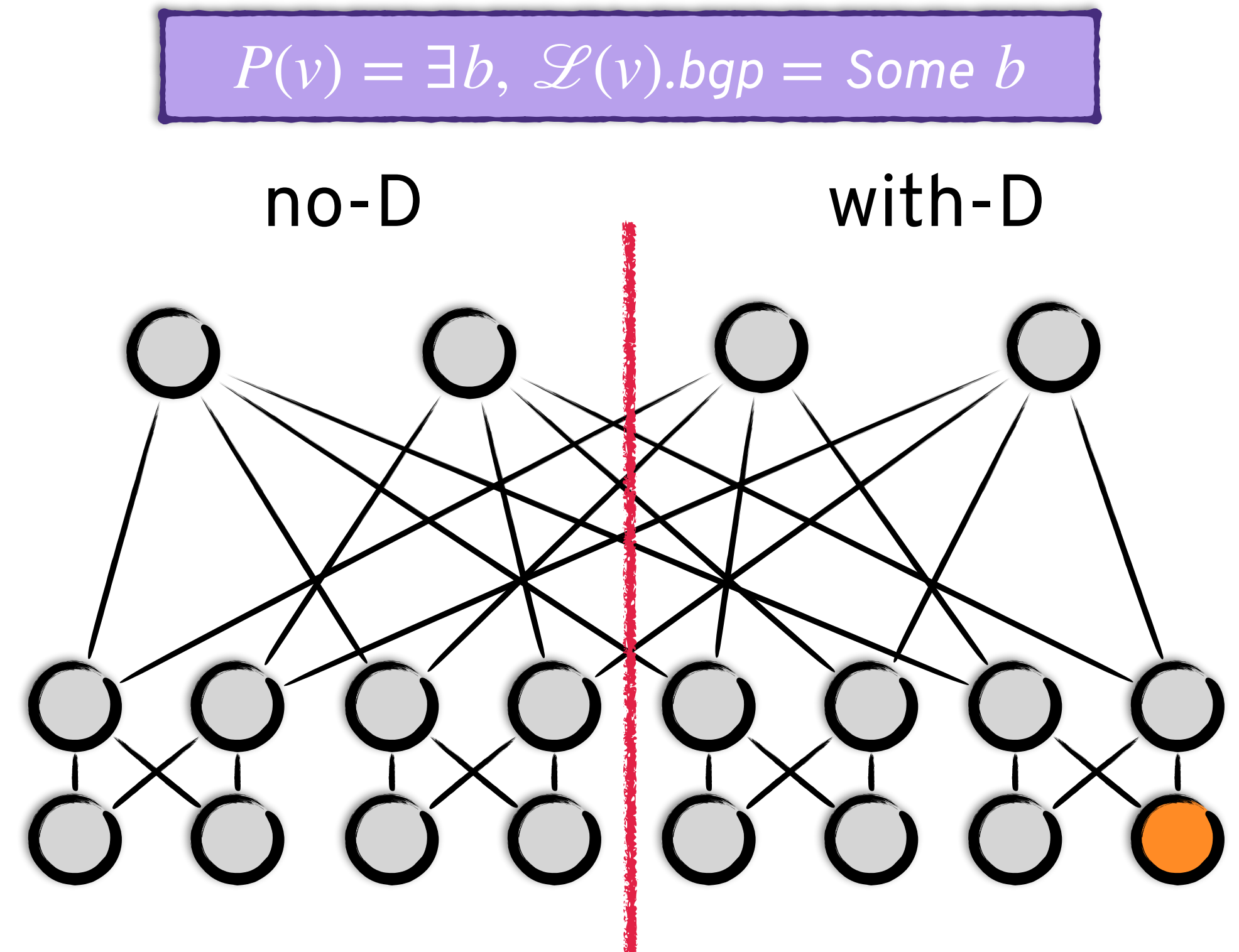
- Partition vertically in half
 - Destination side (with-D)
 - Non-destination side (no-D)
- Hypotheses needed are quite simple!



Fattree Reachability

Case Study

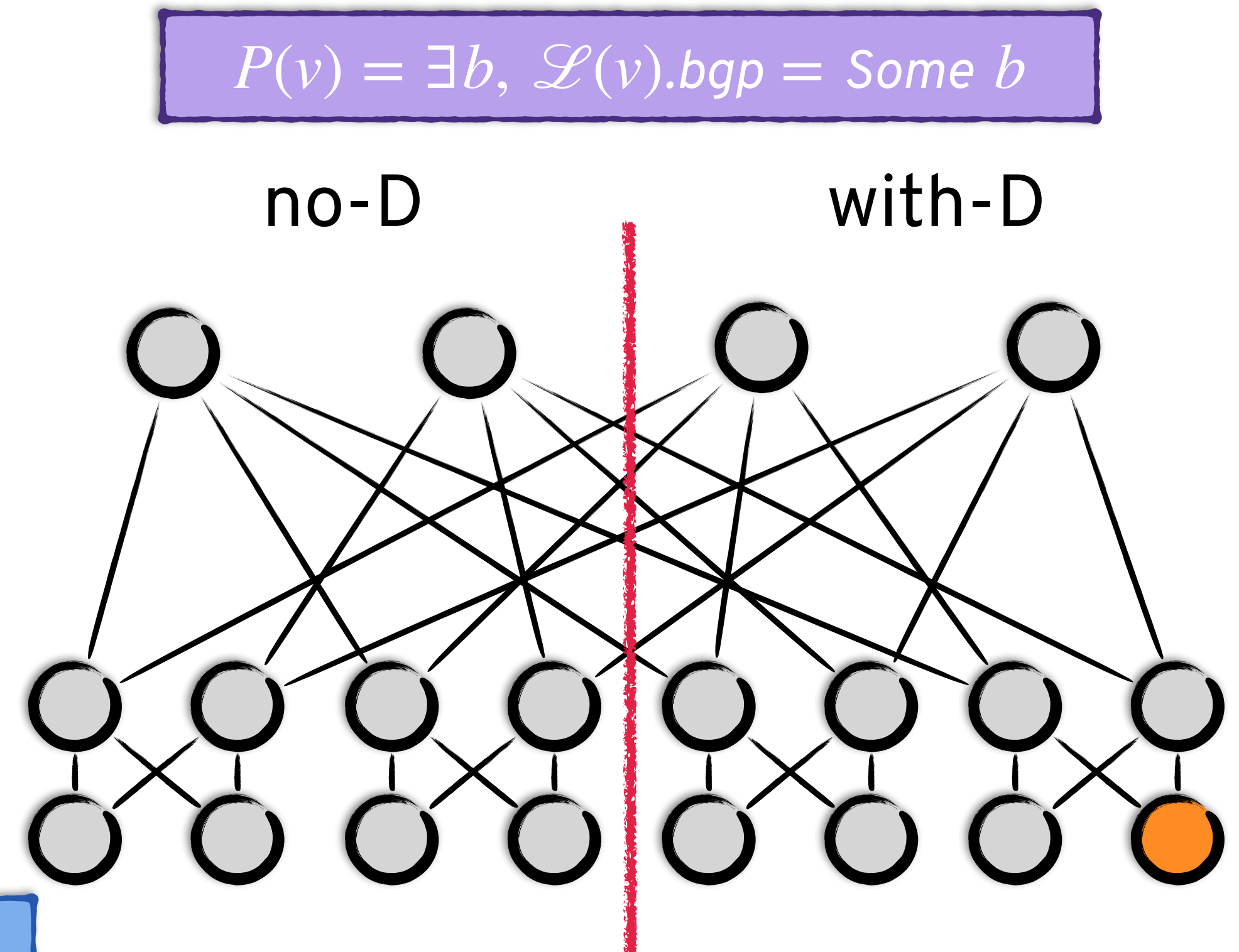
- Partition vertically in half
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 - Only need to be specific enough that the property holds



Fattree Reachability

Case Study

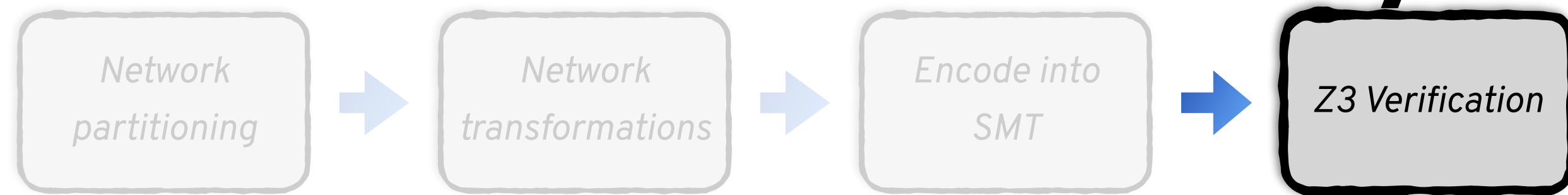
- Partition vertically in half
 - **Destination** side (with-D)
 - Non-destination side (no-D)
- Hypotheses needed are quite simple!
 - Only need to be specific enough that the property holds



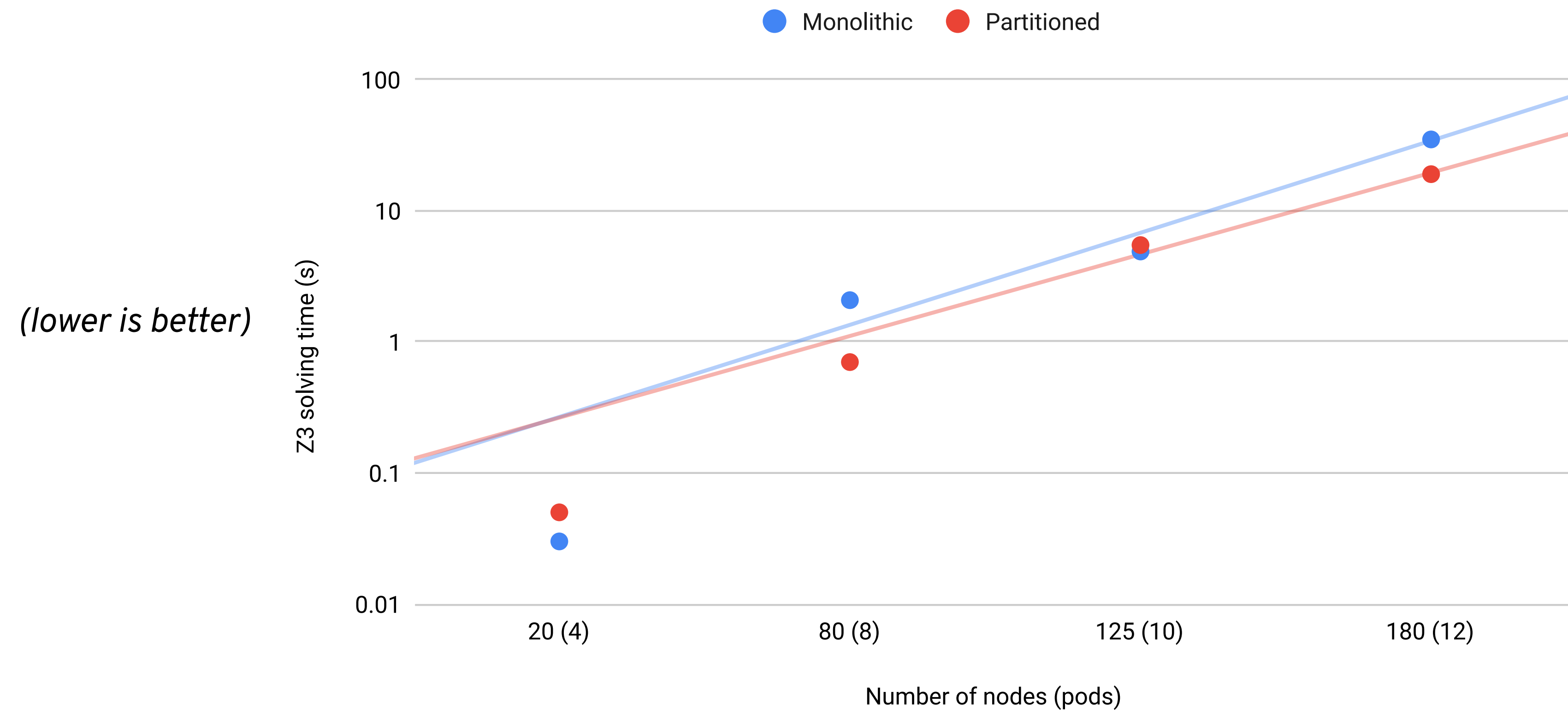
from no-D to with-D: (... , true)

from with-D to no-D: (... , $a = (\exists b, a.bgp = \text{Some } b)$)

Fattree Reachability



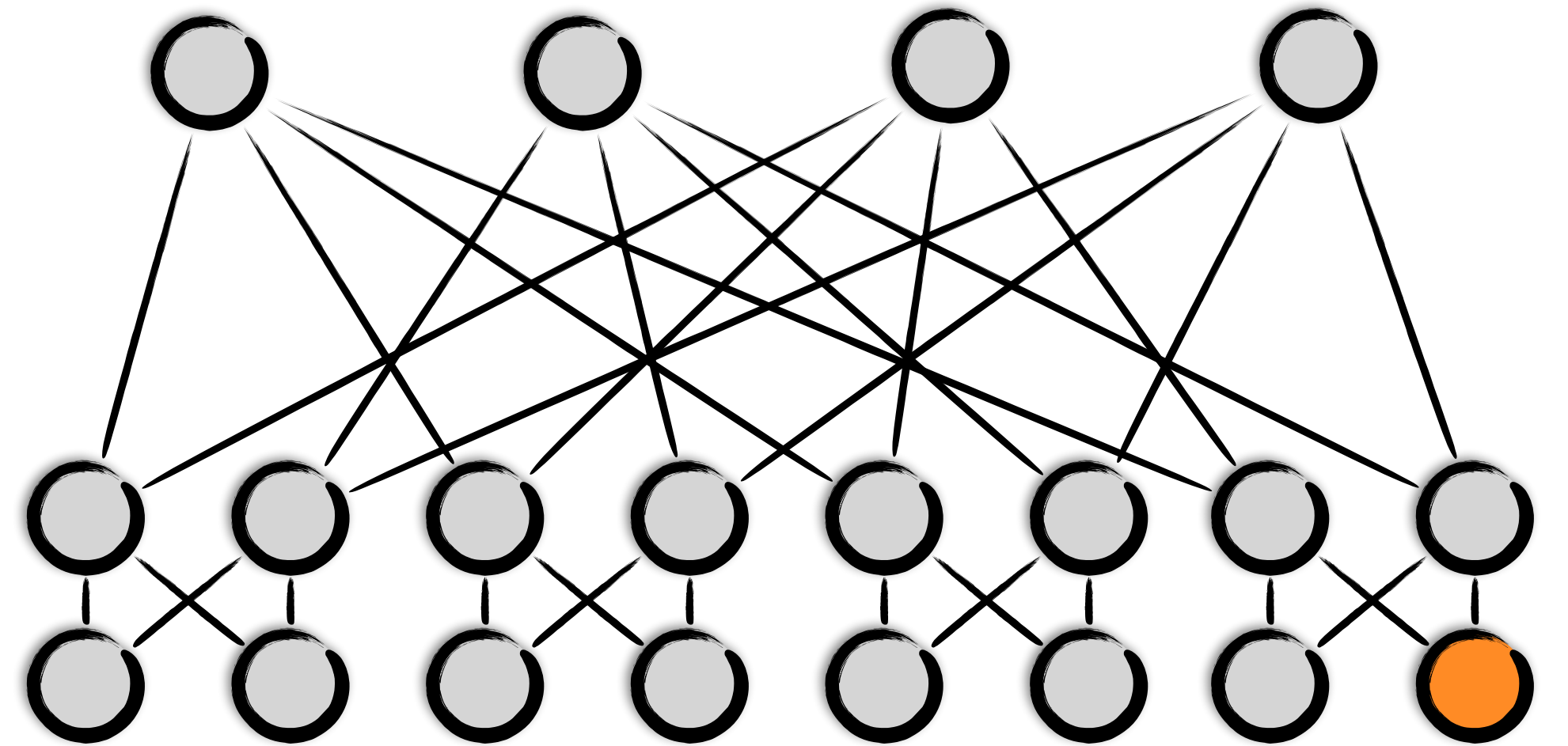
Vertically-Partitioned Fattree Verification Time



Fattree Reachability

An alternative cut

$$P(v) = \exists b, \mathcal{L}(v).bgp = \text{Some } b$$

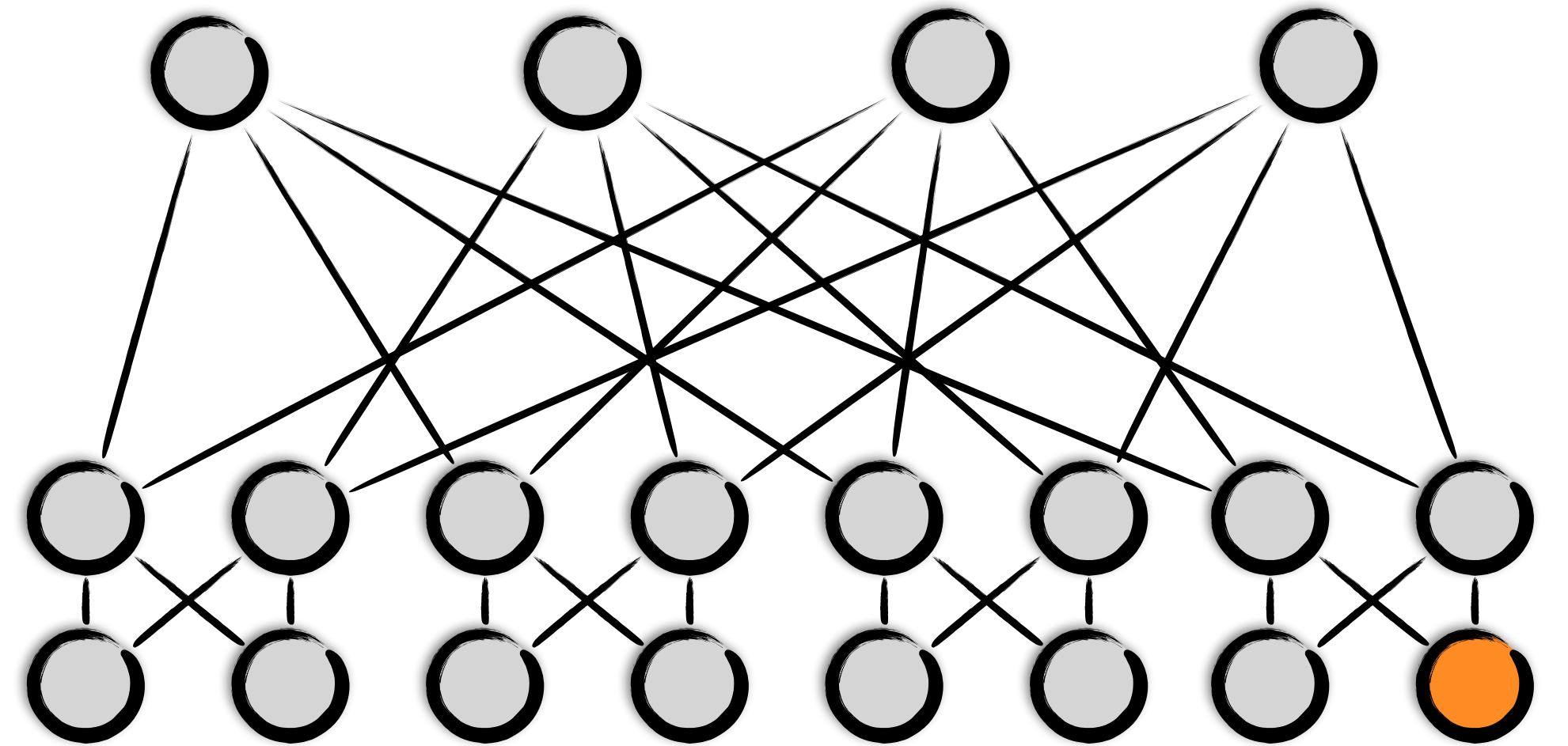


Fattree Reachability

An alternative cut

- Consider an alternative partition scheme

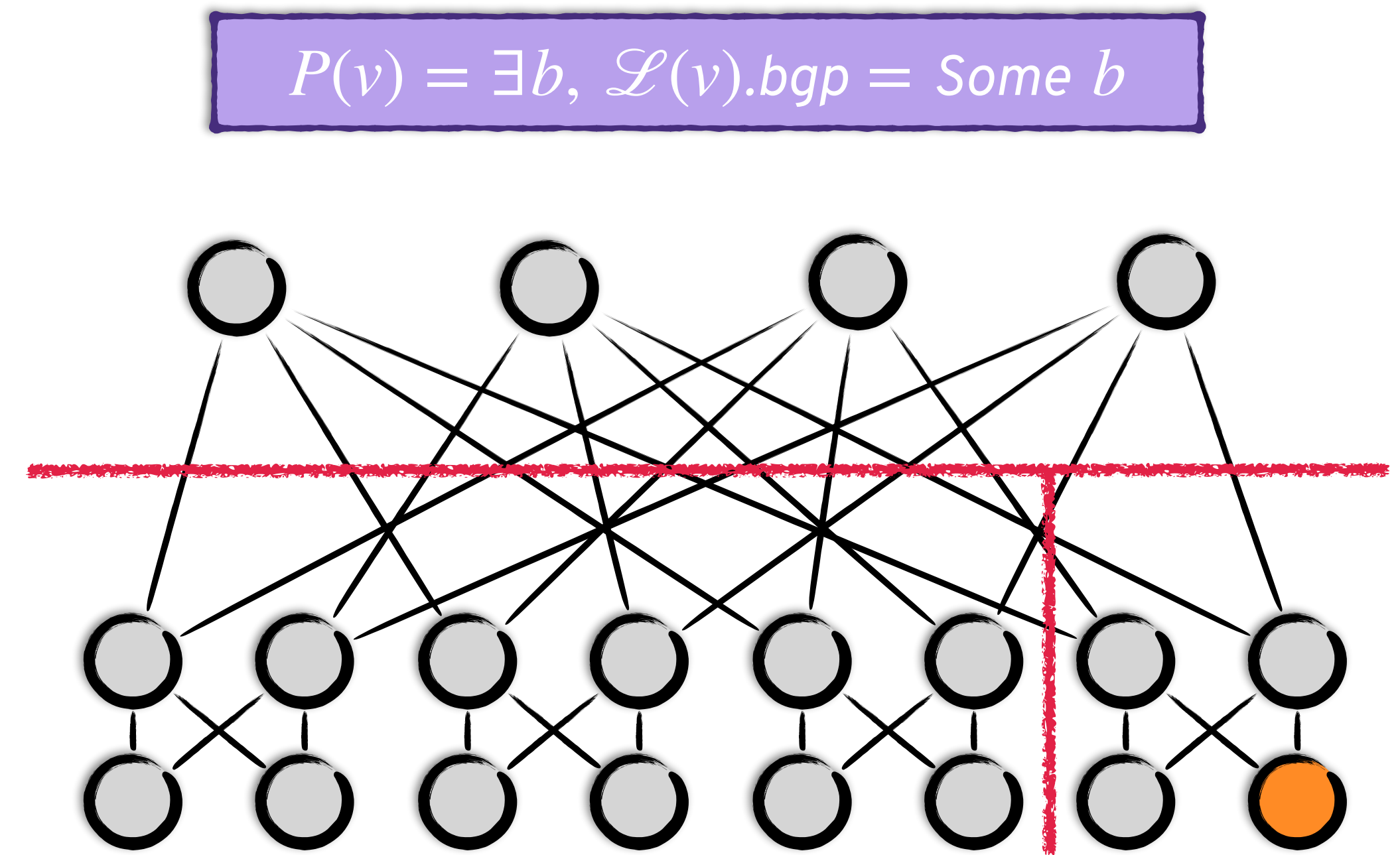
$$P(v) = \exists b, \mathcal{L}(v).bgp = \text{Some } b$$



Fattree Reachability

An alternative cut

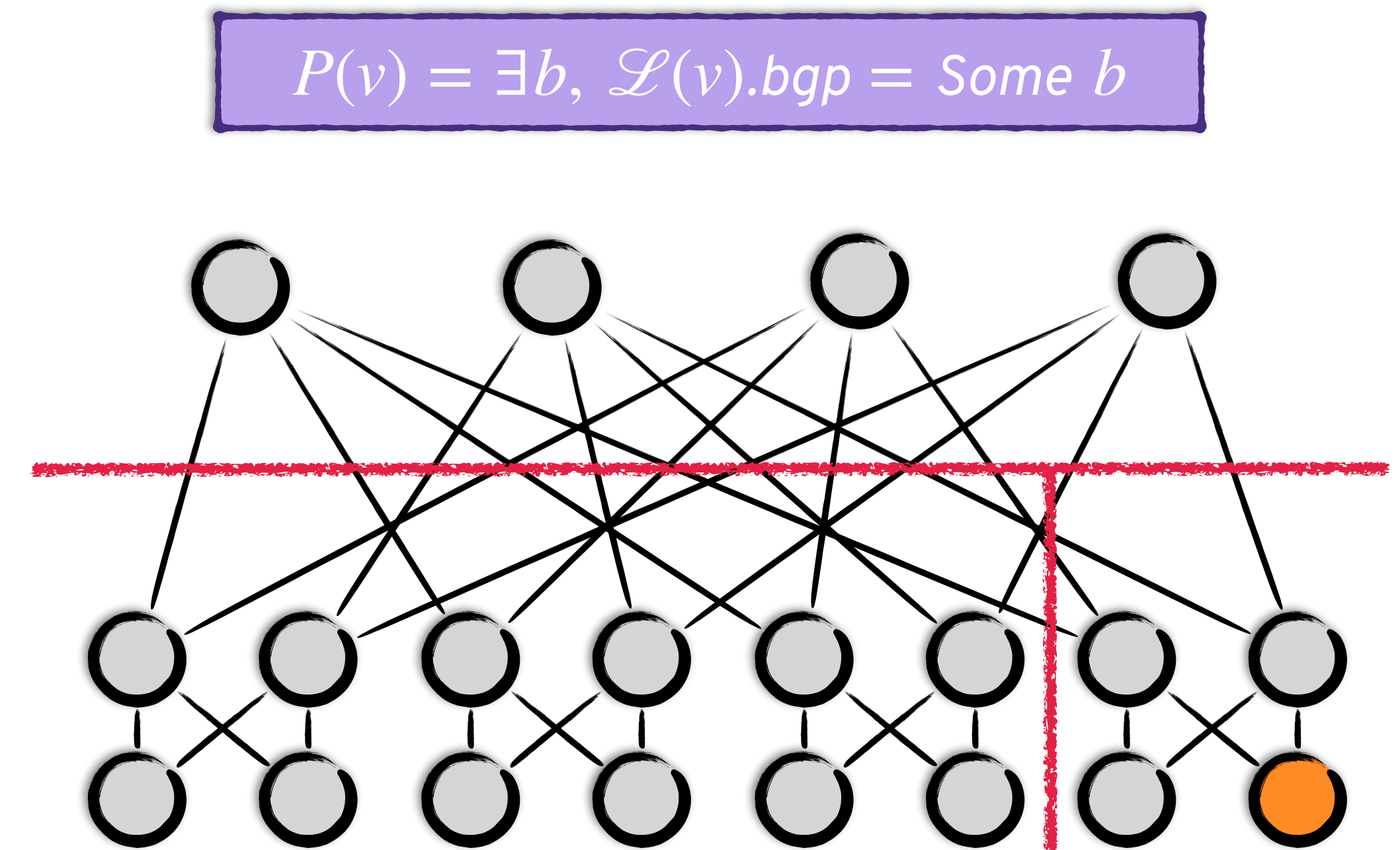
- Consider an alternative partition scheme
 - Partition horizontally into spines and pods: pod with destination separate from others



Fattree Reachability

An alternative cut

- Consider an alternative partition scheme
 - Partition horizontally into spines and pods: pod with destination separate from others
 - Should work just as before!

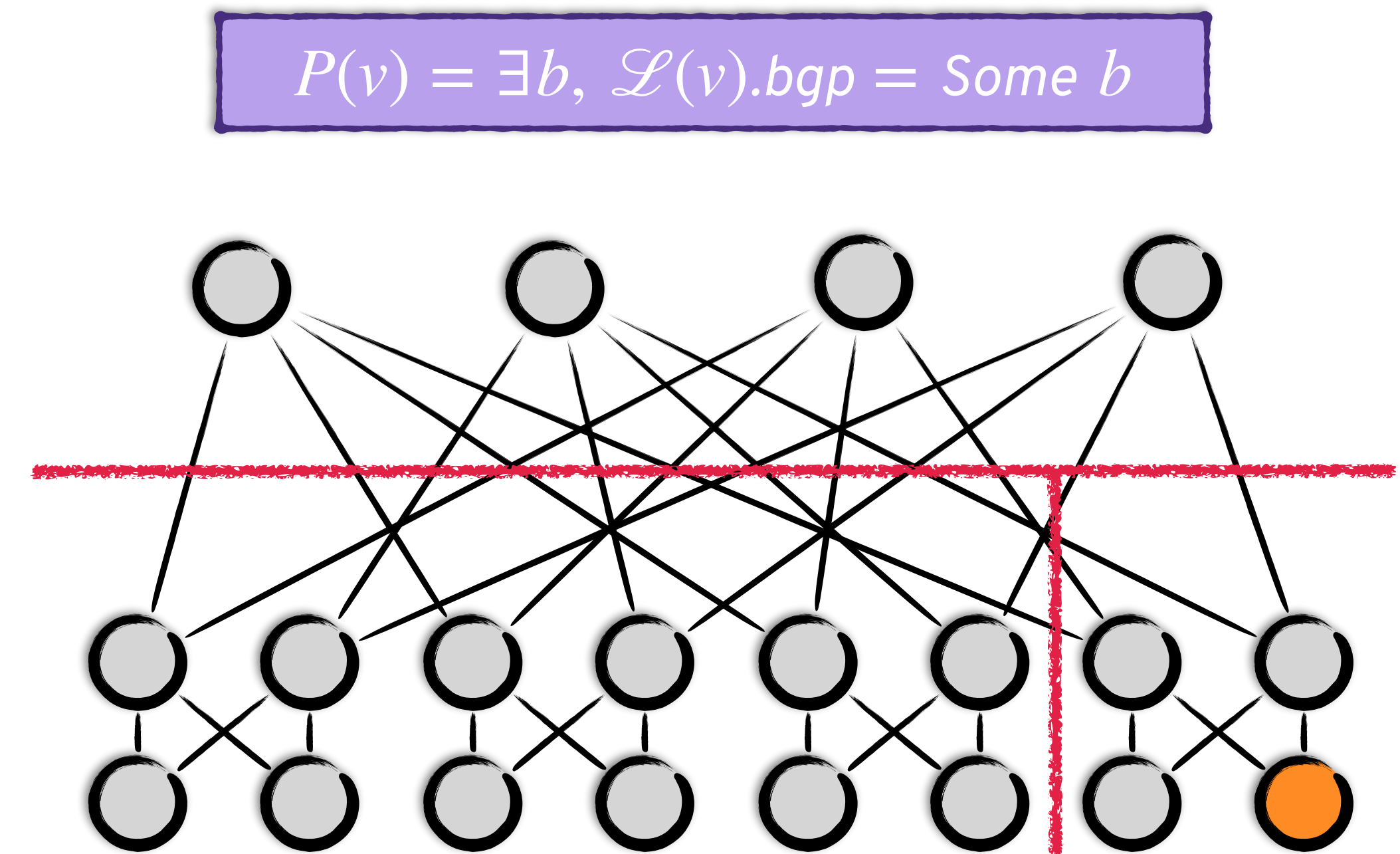


from pods-no-D to spines: (... , true); from spines to pods-no-D: (... , a = ($\exists b, a.bgp = \text{Some } b$))
from spines to pod-with-D: (... , true); from pod-with-D to spines: (... , a = ($\exists b, a.bgp = \text{Some } b$))

Fattree Reachability

An alternative cut

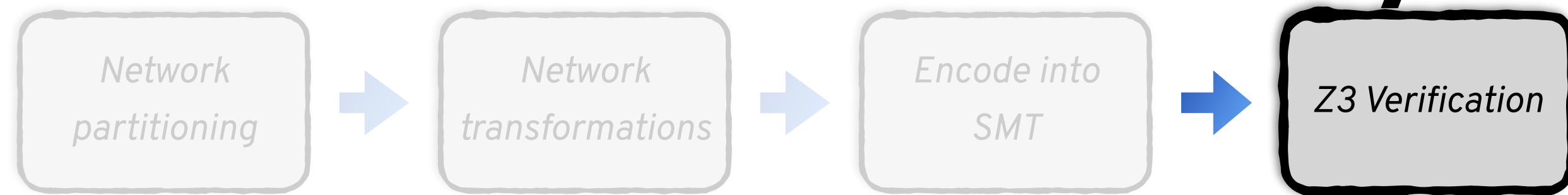
- Consider an alternative partition scheme
 - Partition horizontally into spines and pods: pod with destination separate from others
 - Should work just as before!
 - Unfortunately, initial check won't hold for spines to pods-no-D



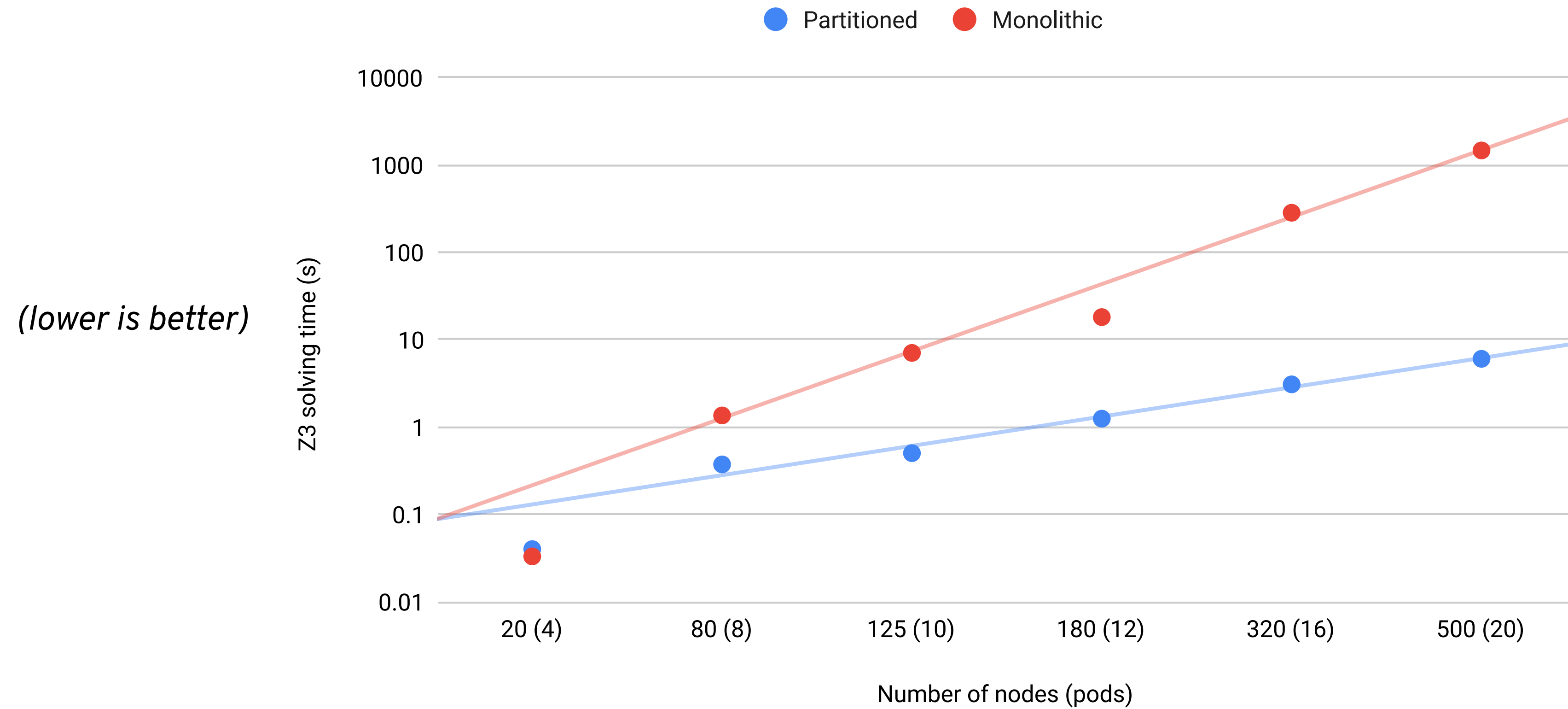
from pods-no-D to spines: (... , true); from spines to pods-no-D: (... , a = ($\exists b, a.bgp = \text{Some } b$))

from spines to pod-with-D: (... , true); from pod-with-D to spines: (... , a = ($\exists b, a.bgp = \text{Some } b$))

Fattree Reachability



Horizontally-Partitioned Fattree Verification Time



A New **Assume-Guarantee** Proof Rule?

[Giannakopoulou et al., *Handbook of Model Checking*. 2018]

A New **Assume-Guarantee** Proof Rule?

New Rule

$$\langle A_S \rangle S \langle G_S \rangle$$
$$\langle A_T \rangle T \langle G_T \rangle$$
$$G_T \Rightarrow A_S$$
$$G_S \Rightarrow A_T$$
$$\langle A_S \rangle S \langle P_S \rangle$$
$$\langle A_T \rangle T \langle P_T \rangle$$
$$\langle \text{true} \rangle S \langle G_S \rangle$$

$$\langle \text{true} \rangle S \parallel T \langle P_S \wedge P_T \rangle$$

[Giannakopoulou et al., *Handbook of Model Checking*. 2018]

A New **Assume-Guarantee** Proof Rule?

New Rule

$$\langle A_S \rangle S \langle G_S \rangle$$

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$$\langle \text{true} \rangle S \parallel T \langle P_S \wedge P_T \rangle$$

Rule CIRC

$$\langle A_2 \rangle M_2 \langle A_1 \rangle$$

$$\langle A_1 \rangle M_1 \langle P \rangle$$

$$\langle \text{true} \rangle M_1 \langle A_2 \rangle$$

$$\langle \text{true} \rangle M_1 \parallel M_2 \langle P \rangle$$

[Giannakopoulou et al., *Handbook of Model Checking*. 2018]

Related Work and Future Directions

Related Work

Related Work

- Network verification

Related Work

- Network verification
 - Control plane verification

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 - **Minesweeper** [Beckett et al., SIGCOMM 2017]: encodes a network into SMT and verifies it *monolithically*

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 - **Split invariants** [Cohen et al., CAV 2010]: uses *vector of assertions* defined over local state variables

Future Work

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- Improving compilation time to SMT

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- Proving a less restrictive initial check

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- Improving compilation time to SMT
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- Designing where to cut the network
 - Consider graph properties, natural structure of topology



Thank you!

Appendix

Kirigami with an Alternate Initial Check

For partition S (resp. T), check validity of the following *verification conditions* (VCs) on \mathcal{M}_S (resp. \mathcal{M}_T , swapping S and T):

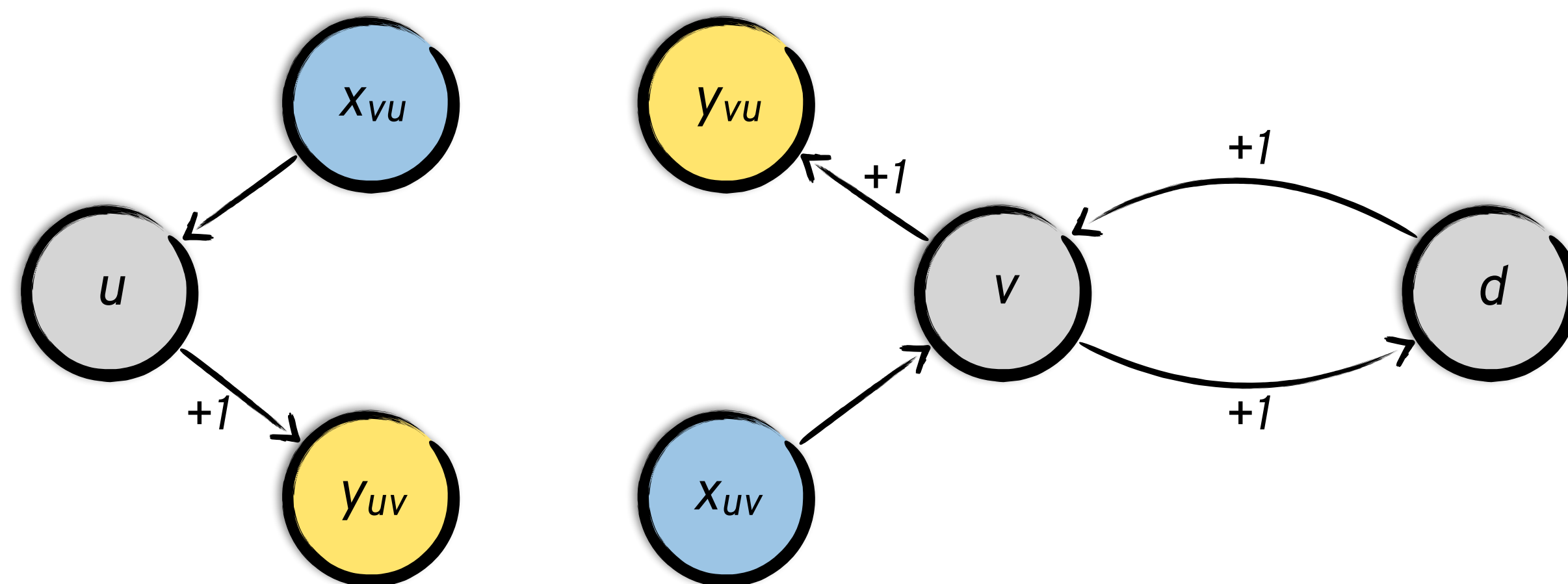
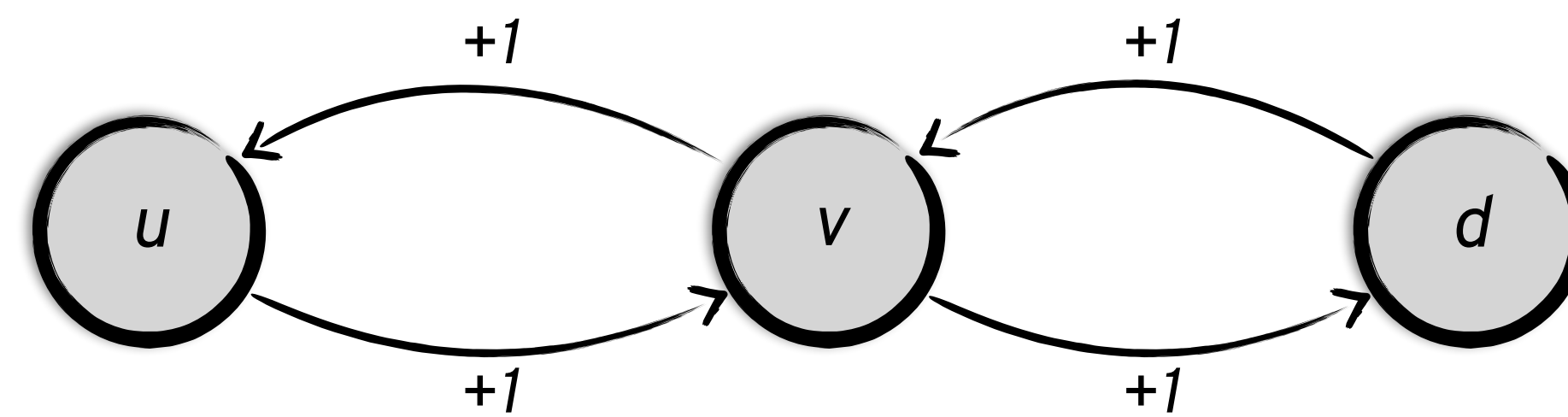
A. Inductiveness: $\langle \mathbf{H}_S \rangle \mathcal{M}_S \langle G_S \rangle \wedge G_S \Rightarrow \mathbf{H}_T$

B. Safety: $\langle \mathbf{H}_S \rangle \mathcal{M}_S \langle P_S \rangle$

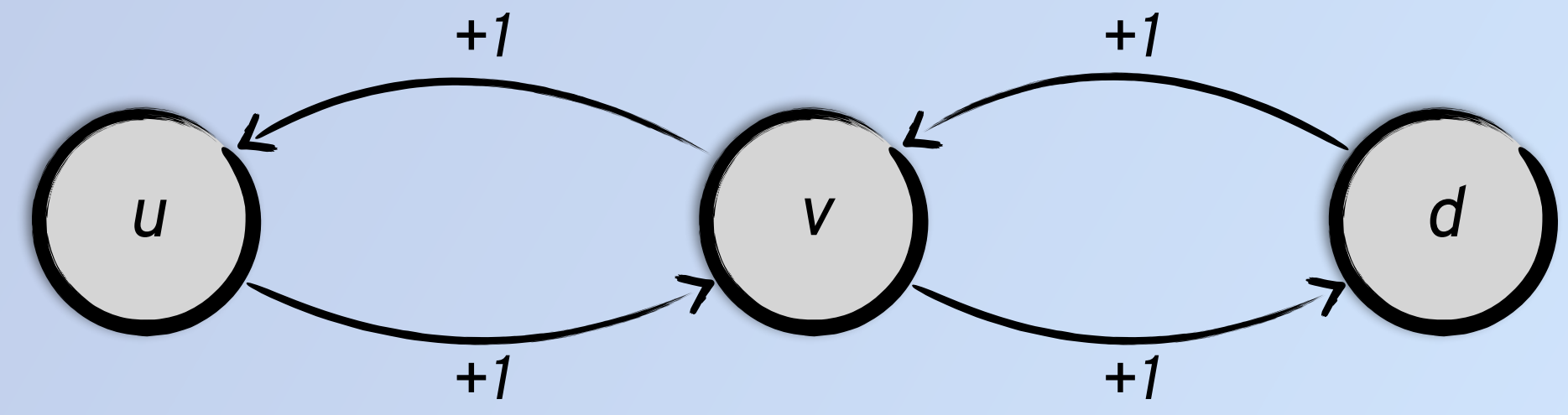
C. Initial: $\langle \text{true} \rangle \mathcal{M}_S \langle G_S \rangle \wedge G_S \Rightarrow \mathbf{H}_T$

If A and B hold for both S and T, and *C holds for S or T*, then return true

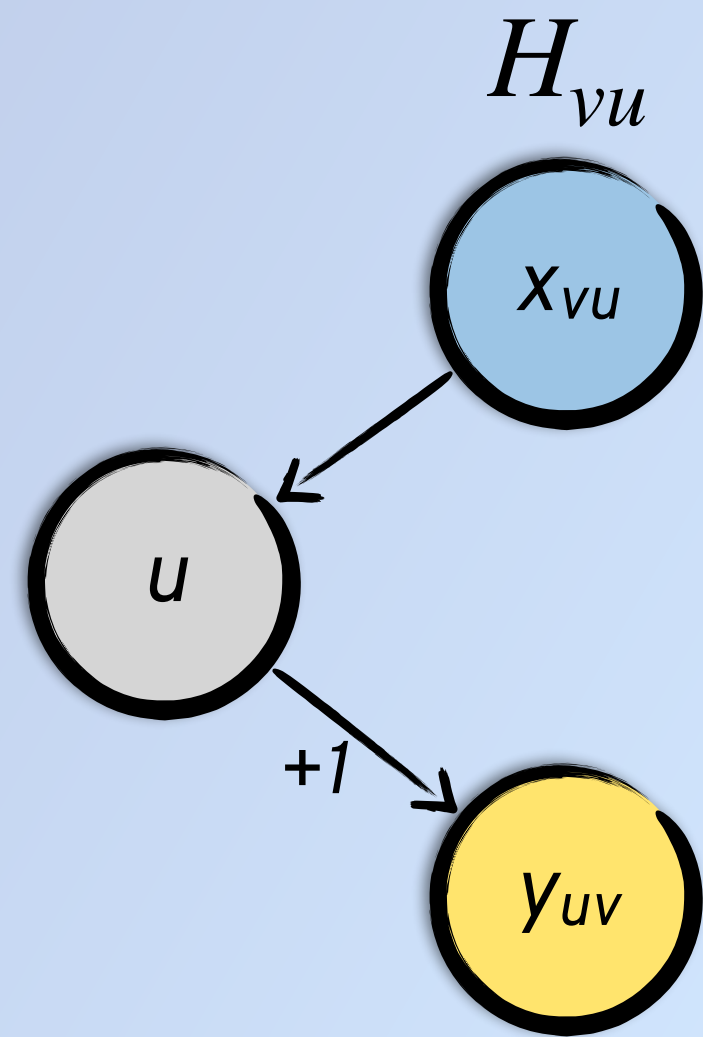
Drafts



R



S



T

