Mike (Deyuan) He

🖬 mikehe@princeton.edu 🕐 in @Mike He 🗞 cs.princeton.edu/~dh7120

EDUCATION

😽 Princeton University, Princeton, NJ

Ph.D. in Computer Science *M.A.* in Computer Science (conferred in May. 2024) Advisor: Prof. Aarti Gupta Fields of study: Compilers; Formal Verification; Distributed Systems; Equality Saturation

W University of Washington, Seattle, WA

B.S. in Computer Science, GPA: 3.89/4.0 (Сим LAUDE) Advisors: Prof. Zachary Tatlock & Dr. Steven Lyubomirsky Selected Honor: CRA Outstanding Undergraduate Researcher Award, Honorable Mention (2022)

🛋 Publications & Pre-prints

- **Mike He**, Ankush Desai, Aishwarya Jagarapu, Doug Terry, Sharad Malik, and Aarti Gupta. PINFER: Automatically learning specifications for distributed systems from event traces [*in submission*]
- Akash Gaonkar, **Mike He**, Yi Li, Bo-Yuan Huang, Andrew Cheung, Vishal Canumalla, Gus Smith, Zachary Tatlock, Sharad Malik, and Aarti Gupta. Verification of software-to-hardware mappings for machine learning accelerators [*in submission*]
- Mike He, Haichen Dong, Sharad Malik, and Aarti Gupta. Improving term extraction with acyclic constraints. In *E*-*Graph Research, Applications, Practices, and Human-factors Symposium* [Paper]
- Marisa Kirisame*, Steven Lyubomirsky*, Altan Haan*, Jennifer Brennan, **Mike He**, Jared Roesch, Tianqi Chen, and Zachary Tatlock. Dynamic tensor rematerialization. In *International Conference on Learning Representations (ICLR'21)* [ArXiv]
- Bo-Yuan Huang*, Steven Lyubomirsky*, Thierry Tambe*, Yi Li, **Mike He**, Gus Smith, Gu-Yeon Wei, Aarti Gupta, Sharad Malik, and Zachary Tatlock. From dsls to accelerator-rich platform implementations: Addressing the mapping gap. In *Workshop on Languages, Tools, and Techniques for Accelerator Design (LATTE'21)* [Paper]
- Bo-Yuan Huang*, Steven Lyubomirsky*, Yi Li, **Mike He**, Thierry Tambe, Gus Henry Smith, Akash Gaonkar, Vishal Canumalla, Gu-Yeon Wei, Aarti Gupta, Sharad Malik, and Zachary Tatlock. Application-level validation of accelerator designs using a formal software/hardware interface. *ACM Trans. Des. Autom. Electron. Syst.* [Paper]

</> </> INTERNSHIPS

Amazon Web Services, Santa Clara, CA

May. 2024 – Aug. 2024

Applied Scientist Intern (**C#**/**Java**/**P**/Distributed Systems/Formal Methods)

- Developed the *PInfer* framework that **discovers likely invariants for distributed systems** from event traces
- Implemented a formal model and specifications of the **Raft** consensus algorithm in **P** framework
- Modeled and proved the correctness of Ring leader election, Paxos, and an AWS internal service in *PVerifier*

Taichi Graphics, Remote and Beijing, China

Compiler R&D Intern (C++/Python)

- June. 2022 Sep. 2022
- Refactored the intermediate representation (IR) of Taichi Language
- Implemented standalone Tensor type for better compilation speed

2022 – Est. 2027

2018 - 2022

- Adapted **compiler passes** (e.g. Load/Store forwarding, Dead code elimination, reaching definition, etc.) to optimize for tensor type expressions
- Implemented LLVM-based code generation for tensor type for Superword-level vectorization

Intel Labs, Hillsboro, OR

Formal Verification Research Intern (Formal Methods/**Python/Dafny**)

Developed the **Py***rope* framework for **correct-by-construction** hardware modeling.

- Facilitated **correct-by-construction** hardware modeling purely in Python
- Encoded the correctness proof of (multi-)montgomery reduction algorithm in Python and **verified successfully by compiling to Dafny**
- Unified "sources of truth" for correctness proofs and programming model implementations

UWPLSE, Seattle, WA

Oct. 2019 – Sep. 2021

Mar. 2022 – June. 2022

Research Assistant (PL/Compiler)

Responsible for conducting research with Prof. Zachary Tatlock, specifically,

- Implemented evaluations in the Dynamic Tensor Rematerialization project
- Designed a flexible matching algorithm for domain-specific language compilers.
- Led research projects with other undergraduate students
- Attended and presented at reading groups

Service

- Sub-reviewer: OOPSLA'24
- Artifact Evaluation Committee: POPL'25, PLDI'24, POPL'24, MLSys'23, MICRO'21
- Mentor in the Ph.D. application mentoring program (Princeton, 2023)

TEACHING

- COS 516: Automated Reasoning about Software (TA, Princeton University, Fall 23 & 24)
- CSE 505: Principles of Programming Languages (TA, University of Washington, Spring 21)

Skills

- **Languages:** C/C++, Python, Rust, OCaml, Coq, Dafny, etc. (Open to other languages)
- **Compiler & Applied PL:** Equality Saturation, Static Analysis, Computer-aided Reasoning, SMT
- PL Theory: Formal Verification, Type Theory, Mathematical Logic
- Systems: Distributed Systems, Machine Learning Systems, Data Center Systems
- **Fun Fact:** I am more seasoned in playing the violin than coding **f**; I have:
 - 1. an Lv.9 certificate* (similar to ABRSM Grade 8) issued by Central Conservative of Music;
 - 2. > **20-year** violin solo experience;
 - 3. Multiple 1st Prizes (various local competitions in Beijing) and a Silver medal (Beijing regional)^{\dagger};
 - 4. 6-year experience with symphony orchestras; 3-year experience as *the Principal 2nd Violinist*;
 - 5. 3-year experience with a piano quartet/quintet and multiple string quartets (with 1 CD produced);
 - 6. \sim 4 public concerts with a philharmonic orchestra^{*} at the The Giant Egg, Beijing, China.

* : The highest level for non-professionals

[†] : Awarded during middle and high school years.

*: The Beijing National Day School Philharmonic Orchestra