

COS320: Compiling Techniques

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Data flow analysis

Recall: constant propagation

- A **constant environment** is a symbol table mapping each variable x to one of:
 - an integer n (indicating that x 's value is n whenever the program is at I)
 - \top (indicating that x might take more than one value at I)
 - \perp (indicating that x may take no values at run-time - I is unreachable)
- An **assignment** $\mathbf{IN}, \mathbf{OUT} : N \rightarrow \text{ConstEnv}$ for a CFG (N, E, s) maps each vertex to
 - $\mathbf{IN}[bb]$: a constant environment that holds immediately *before* bb
 - $\mathbf{OUT}[bb]$: a constant environment that holds immediately *after* bb
- Say that an assignment $\mathbf{IN}, \mathbf{OUT}$ is **conservative** if

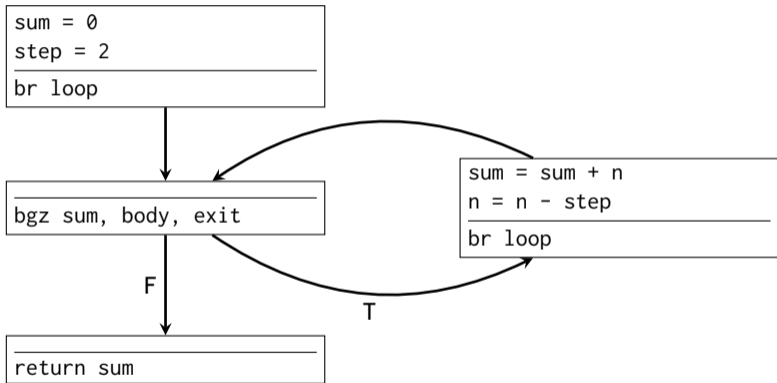
- 1 $\mathbf{IN}[s]$ assigns each variable \top
- 2 For each node $bb \in N$,

$$\mathbf{OUT}[bb] \sqsupseteq \text{post}(bb, \mathbf{IN}[bb])$$

- 3 For each edge $src \rightarrow dst \in E$,

$$\mathbf{IN}[dst] \sqsupseteq \mathbf{OUT}[src]$$

```
int sum2(int n) {  
    int sum = 0;  
    int step = 2;  
    while (n > 0) {  
        sum = sum + n;  
        n = n - step;  
    }  
    return sum;  
}
```



High-level constant propagation algorithm

- Initialize $\mathbf{IN}[s]$ to the constant environment that sends every variable to \top and $\mathbf{OUT}[s]$ to the constant environment that sends every variable to \perp .
- Initialize $\mathbf{IN}[bb]$ and $\mathbf{OUT}[bb]$ to the constant environment that sends every variable to \perp for every other basic block

High-level constant propagation algorithm

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- Initialize $\mathbf{IN}[bb]$ and $\mathbf{OUT}[bb]$ to the constant environment that sends every variable to \perp for every other basic block
- Choose a constraint that is *not* satisfied by \mathbf{IN} , \mathbf{OUT}

- If there is basic block bb with $\mathbf{OUT}[bb] \not\sqsupseteq \text{post}(bb, \mathbf{IN}[bb])$, then set

$$\mathbf{OUT}[bb] := \text{post}(bb, \mathbf{IN}[bb])$$

- If there is an edge $src \rightarrow dst \in E$ with $\mathbf{IN}[dst] \not\sqsupseteq \mathbf{OUT}[src]$, then set

$$\mathbf{IN}[dst] := \mathbf{IN}[dst] \sqcup \mathbf{OUT}[src]$$

- Terminate when all constraints are satisfied.

Some additional vocabulary:

- Define $pred(n) = \{m \in N : m \rightarrow n \in E\}$ (control flow predecessors)
- Define $succ(n) = \{m \in N : n \rightarrow m \in E\}$ (control flow successors)
- Path = sequence of nodes n_1, \dots, n_k such that for each i , there is an edge from $n_i \rightarrow n_{i+1} \in E$

Workset algorithm

Input : Control flow graph (N, E, s) , with variables x_1, \dots, x_n

Output: Least conservative assignment of constant environments

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IN $[n] = \mathbf{OUT}[n] = \{x_1 \mapsto \perp, \dots, x_n \mapsto \perp\}$ for all other nodes n ;

$work \leftarrow N$;

/ Set of nodes that may violate spec */*

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while $work \neq \emptyset$ **do**

 Pick some n from $work$;

$work \leftarrow work \setminus \{n\}$;

$\mathbf{IN}[n] \leftarrow \bigsqcup_{p \in pred(n)} \mathbf{OUT}[p]$;

$\mathbf{OUT}[n] \leftarrow post(n, \mathbf{IN}[n])$;

return $\mathbf{IN}, \mathbf{OUT}$

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$old \leftarrow \mathbf{OUT}[n];$

$\mathbf{IN}[n] \leftarrow \bigsqcup_{p \in pred(n)} \mathbf{OUT}[p];$

$\mathbf{OUT}[n] \leftarrow post(n, \mathbf{IN}[n]);$

if $old \neq \mathbf{OUT}[n]$ **then**

$work \leftarrow work \cup succ(n)$

return $\mathbf{IN}, \mathbf{OUT}$

Common subexpression elimination

- Common subexpression elimination searches for expressions that
 - appear at multiple points in a program
 - evaluate to the same value at those pointsand (possibly) save the cost of re-evaluation by storing that value.

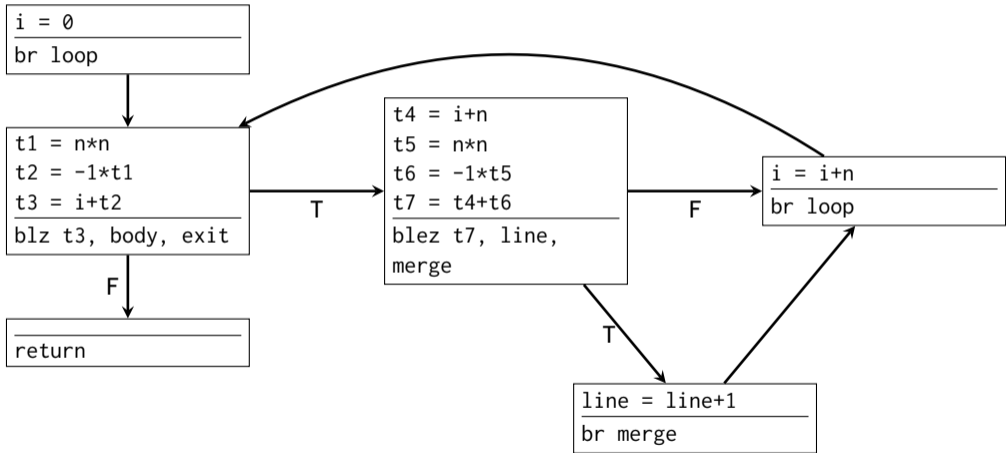
```
void print (long *m, long n) {
    long i,j;
    for (i = 0; i < n*n; i += n) {
        for (j = 0; j < n; j += 1) {
            printf(“ %ld”, *(m + i + j));
        }
        if (i + n < n*n) {
            printf(“\n”);
        }
    }
}
```

→

```
void print (long *m, long n) {
    long i,j;
    long n_times_n = n*n;
    for (i = 0; i < n_times_n; ) {
        for (j = 0; j < n; j += 1) {
            printf(“ %ld”, *(m + i + j));
        }
        long i_plus_n = i+n;
        if (i_plus_n < n_times_n) {
            printf(“\n”);
        }
        i = i_plus_n;
    }
}
```

Available expressions

- An *expression* in our simple imperative language has one of the following forms:
 - add <opn> <opn>
 - mul <opn> <opn>
- Fix control flow graph $G = (N, E, s)$
- An expression e is **available** at basic block $n \in N$ if for every path from s to n in G :
 - 1 the expression e is evaluated along the path
 - 2 after the *last* evaluation of e along the path, no variables in e are overwritten
- Idea: if expression e is available at node n , then can eliminate redundant computations of e within n



Propagating available expressions

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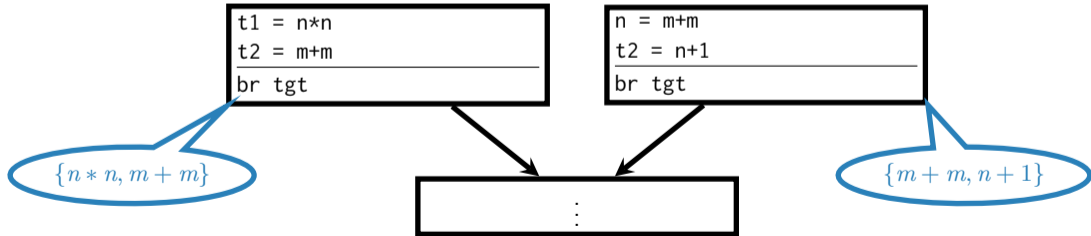
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- How do we propagate available expressions through a basic block?
 - Block takes the form $instr_1, \dots, instr_n, term$.
take $post_{AE}(block, E) = post_{AE}(instr_n, \dots post_{AE}(instr_1, E))$

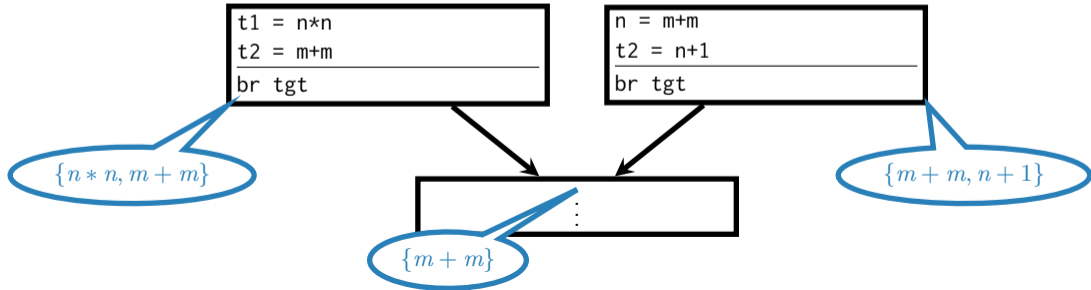
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take $post_{AE}(block, E) = post_{AE}(instr_n, \dots post_{AE}(instr_1, E))$
- How do we combine information from multiple predecessors? *Intersection*



Available expressions as a constraint system

- Let $G = (N, E, s)$ be a control flow graph.
- For each basic block $bb \in N$, associate two sets of expressions, $\mathbf{IN}[bb]$ and $\mathbf{OUT}[bb]$
 - $\mathbf{IN}[bb]$ is the set of expressions available at the *entry* of bb
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- Say that the assignment IN, OUT is **conservative** if

① $\text{IN}[s] = \emptyset$

② For each node $bb \in N$,

$$\text{OUT}[bb] \subseteq \text{post}_{AE}(bb, \text{IN}[bb])$$

③ For each edge $src \rightarrow dst \in E$,

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 - 2 For each node $bb \in N$,
$$\text{OUT}[bb] \subseteq \text{post}_{AE}(bb, \text{IN}[bb])$$
 - 3 For each edge $src \rightarrow dst \in E$,
$$\text{IN}[dst] \subseteq \text{OUT}[src]$$
- Fact: if IN, OUT is a conservative assignment, then:
 - If $e \in \text{IN}[bb]$, then e is available at entry of bb
 - Similarly for OUT

Workset algorithm

Input : Control flow graph (N, E, s) , with expressions U

Output: Least conservative assignment of available expressions

IN $[s] = \emptyset$;

OUT $[s] = U$;

IN $[n] = \mathbf{OUT}[n] = U$ for all other nodes n ;

$work \leftarrow N$;

/ Set of nodes that may violate spec */*

while $work \neq \emptyset$ **do**

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Constant propagation

Available expressions

Want *smallest* assignment **IN**, **OUT** such that

- $\mathbf{IN}[s] = \{x_1 \mapsto \top, \dots, x_n \mapsto \top\}$
- For each $n \in N$,
 $\mathbf{OUT}[n] \supseteq \text{post}_{CP}(n, \mathbf{IN}[n])$
- For each $p \rightarrow n \in E$, $\mathbf{OUT}[p] \subseteq \mathbf{IN}[n]$

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- **Commonality**: constant propagation and available expressions are characterized by **optimal solutions** to a system of local constraints

- “Local”: defined in terms of *edges*; contrast with “global”, which depends on the structure of the whole graph (e.g., paths)

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$$\mathbf{OUT}[n] \supseteq \text{post}_{\text{CP}}(n, \mathbf{IN}[n])$$

- For each $n \in N$,

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- **Commonality:** constant propagation and available expressions are characterized by **optimal solutions** to a system of local constraints

- “Local”: defined in terms of *edges*; contrast with “global”, which depends on the structure of the whole graph (e.g., paths)

- The algorithms for constant propagation & available expressions are *essentially the same*

Dataflow analysis

- *Dataflow analysis* is an approach to program analysis that unifies the presentation and implementation of many different analyses
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- What now:
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- Not covered: *abstract interpretation* – a general theory for relating program analysis to program semantics
 - What does it mean for a constraint system to be correct?
 - How do we prove it?

A (forward) dataflow analysis consists of:

- An **abstract domain** \mathcal{L}
 - Defines the space of program “properties” that we are interested in
- An **abstract transformer** $post_{\mathcal{L}}$
 - Determines how each basic block transforms properties
 - i.e., if property p holds *before* n , then $post_{\mathcal{L}}(n, p)$ is a property that holds *after* n

Abstract domains

An **abstract domain** is a set \mathcal{L} equipped with:

- A partial order \sqsubseteq
 - $x \sqsubseteq y$ means that x represents more precise information about the program than y ¹

¹The other direction also works, and is the one taken in classical compilers literature. In this class, we will stick to this direction, which is the convention established in abstract interpretation.

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- A *least upper bound* (“join”) operator, \sqcup
 - 1 $x \sqsubseteq x \sqcup y$
 - 2 $y \sqsubseteq x \sqcup y$
 - 3 $x \sqcup y \sqsubseteq z$ for any z satisfying 1 and 2

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- A *least element* (“bottom”), \perp
 - $\perp \sqsubseteq x$ for all x
 - $\perp \sqcup x = x \sqcup \perp = x$ for all x

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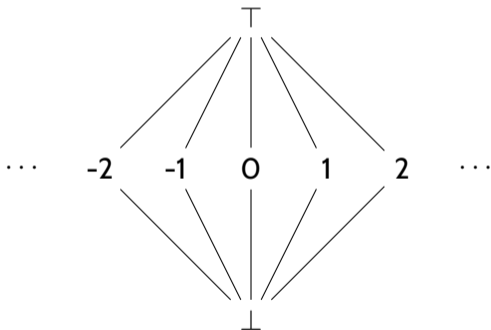
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- A *least element* (“bottom”), \perp
 - $\perp \sqsubseteq x$ for all x
 - $\perp \sqcup x = x \sqcup \perp = x$ for all x
- A *greatest element* (“top”), \top
 - $x \sqsubseteq \top$ for all x
 - $\top \sqcup x = x \sqcup \top = \top$ for all x

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- Often convenient to depict partial order as *Haase diagram*
 - Draw a line from x to y if $x \sqsubseteq y$ and there is no z with $x \sqsubseteq z \sqsubseteq y$ (y covers x)
 - $x \sqsubseteq y$ iff there is an upwards path from x to y



Function spaces

- Constant environments are functions mapping *Variables* $\rightarrow \mathbb{Z} \cup \{\perp, \top\}$

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 - Environments inherit *pointwise ordering* \sqsubseteq^* from the ordering \sqsubseteq on $\mathbb{Z} \cup \{\perp, \top\}$:
 $f \sqsubseteq^* g$ iff $f(x) \sqsubseteq g(x)$ for all $x \in \text{Variables}$
 - There is a least and greatest environment

$$\perp^* = (\text{fun } x \rightarrow \perp)$$

$$\top^* = (\text{fun } x \rightarrow \top)$$

- Environments have least upper bounds

$$f \sqcup^* g = (\text{fun } (x) \rightarrow f(x) \sqcup g(x))$$

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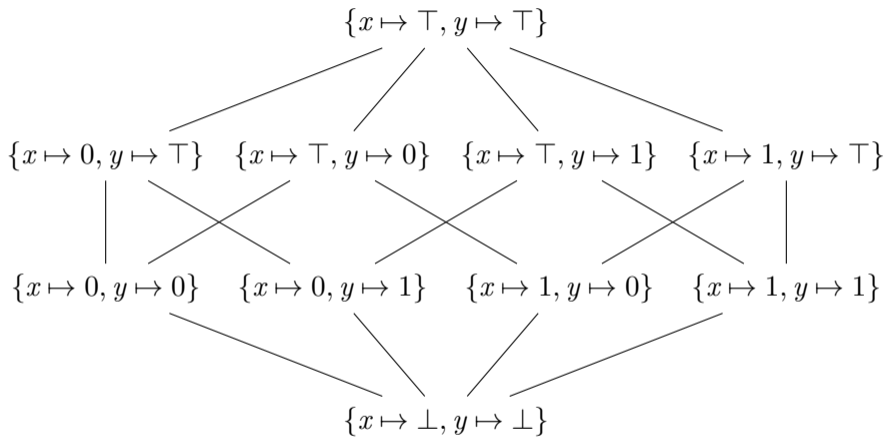
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$$f \sqcup^* g = (\text{fun } (x) \rightarrow f(x) \sqcup g(x))$$

- *This holds more generally:* If \mathcal{L} is an abstract domain and X is any set, the set of functions $X \rightarrow \mathcal{L}$ is an abstract domain under the pointwise ordering.

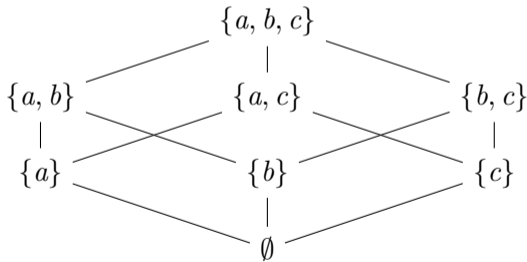


(Identifying $\{x \mapsto \perp, y \mapsto \perp\}$ with all functions that map either x or y to \perp)

Powersets

For any set X , the set 2^X of subsets of X is an abstract domain:

- Order \subseteq , least element \emptyset , greatest element X , join \cup
- Order \supseteq , least element X , greatest element \emptyset , join \cap (*Available Expressions*)



Transfer functions

A transfer function $post_{\mathcal{L}} : Basic\ Block \times \mathcal{L} \rightarrow \mathcal{L}$ maps each basic block & “pre-state” value to a “post-state” value

- Technical requirement: $post_{\mathcal{L}}$ is **monotone**

$$x \sqsubseteq y \Rightarrow post_{\mathcal{L}}(n, x) \sqsubseteq post_{\mathcal{L}}(n, y)$$

(“more information in \Rightarrow more information out”)

- Note: monotonicity is *not* the same as $x \sqsubseteq f(x)$ for all x

Generic (forward) dataflow analysis algorithm

- Given:
 - Abstract domain $(\mathcal{L}, \sqsubseteq, \sqcup, \perp, \top)$
 - Transfer function
 $post_{\mathcal{L}} : \text{Basic Block} \times \mathcal{L} \rightarrow \mathcal{L}$
 - Control flow graph $G = (N, E, s)$
- Compute: *least* annotation **IN**, **OUT** such that
 - 1 **IN**(s) = \top
 - 2 For all $n \in N$, $post_{\mathcal{L}}(n, \mathbf{IN}[n]) \sqsubseteq \mathbf{OUT}[n]$
 - 3 For all $p \rightarrow n \in E$, $\mathbf{OUT}[p] \sqsubseteq \mathbf{IN}(n)$

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 - For all $p \rightarrow n \in E$, $\mathbf{OUT}[p] \sqsubseteq \mathbf{IN}(n)$

```
IN[s] =  $\top$ , OUT[s] =  $\perp$ ;  
IN[n] = OUT[n] =  $\perp$   
  for all other nodes  $n$ ;  
work  $\leftarrow N$ ;  
while work  $\neq \emptyset$  do  
  | Pick some  $n$  from work;  
  | work  $\leftarrow \mathbf{work} \setminus \{n\}$  ;  
  | old  $\leftarrow \mathbf{OUT}[n]$ ;  
  | IN[n]  $\leftarrow \bigsqcup_{p \in \text{pred}(n)} \mathbf{OUT}[p]$ ;  
  | OUT[n]  $\leftarrow post_{\mathcal{L}}(n, \mathbf{IN}[n])$ ;  
  | if old  $\neq \mathbf{OUT}[n]$  then  
  |   | work  $\leftarrow \mathbf{work} \cup \text{succ}(n)$   
return IN, OUT
```

Summary

- Program analyses share common structure
 - Can implement a single workset algorithm and get multiple analyses by “plugging in” different abstract domains and transfer functions
 - Can prove correctness of workset algorithm once-and-for-all in an abstract setting
- Next time: correctness of the general worklist algorithm