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6. TOY MACHINE I

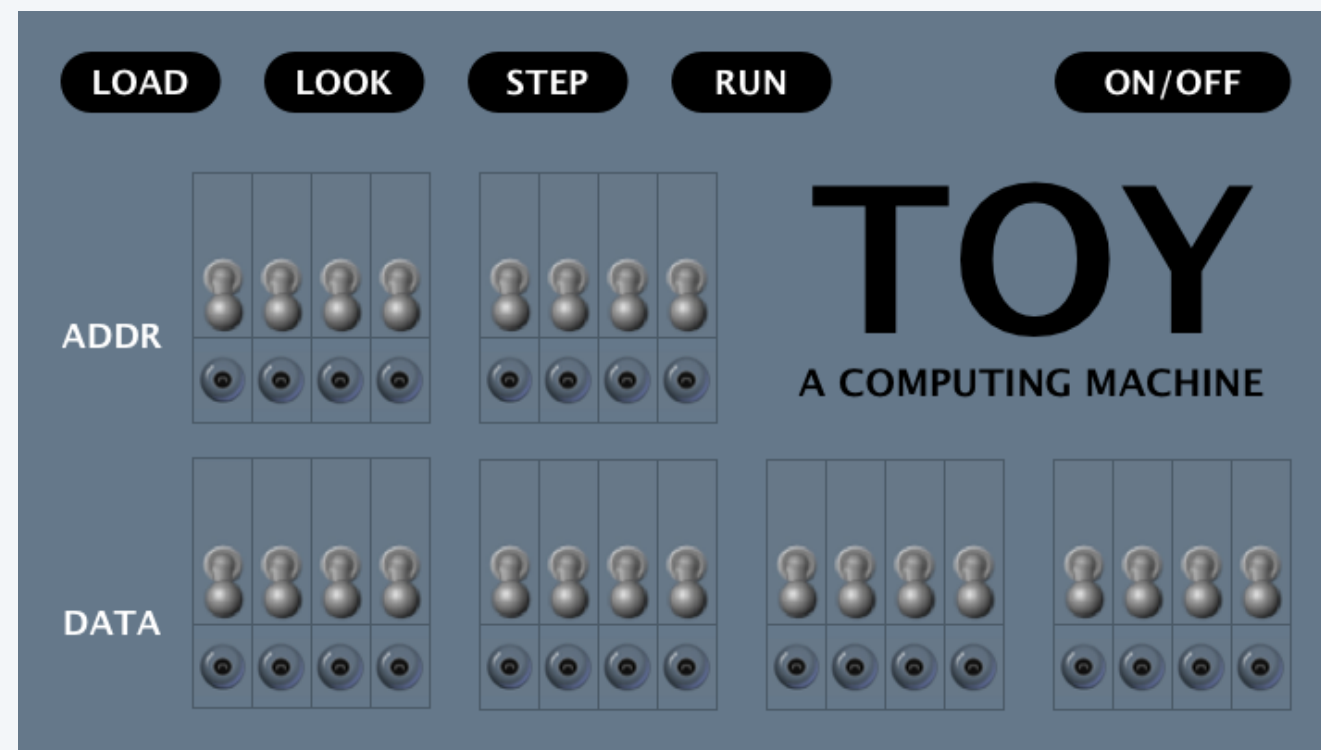
- ▶ *overview*
- ▶ *data types*
- ▶ *instructions*
- ▶ *operating the machine*

The TOY computing machine

TOY is an imaginary machine invented for this course.

It is similar in design to:

- Ancient computers.
- Today's smartphone microprocessors.
- Countless other devices designed and built over the past 50 years.



TOY machine



PDP-8, 1970s



smartphone processor, 2020s

Reasons to study TOY

Learn about machine language programming.

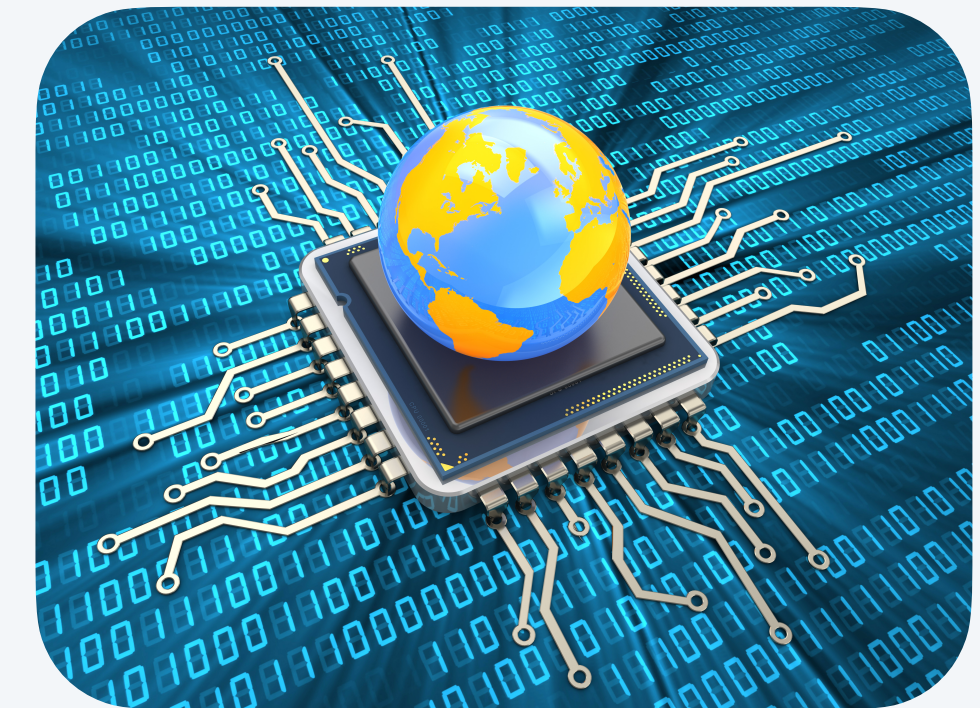
- How do Java programs relate to your computer? ← *see COS 320*
- Key to understanding Java references (and C pointers). ← *see COS 217*
- Still necessary in some modern applications.

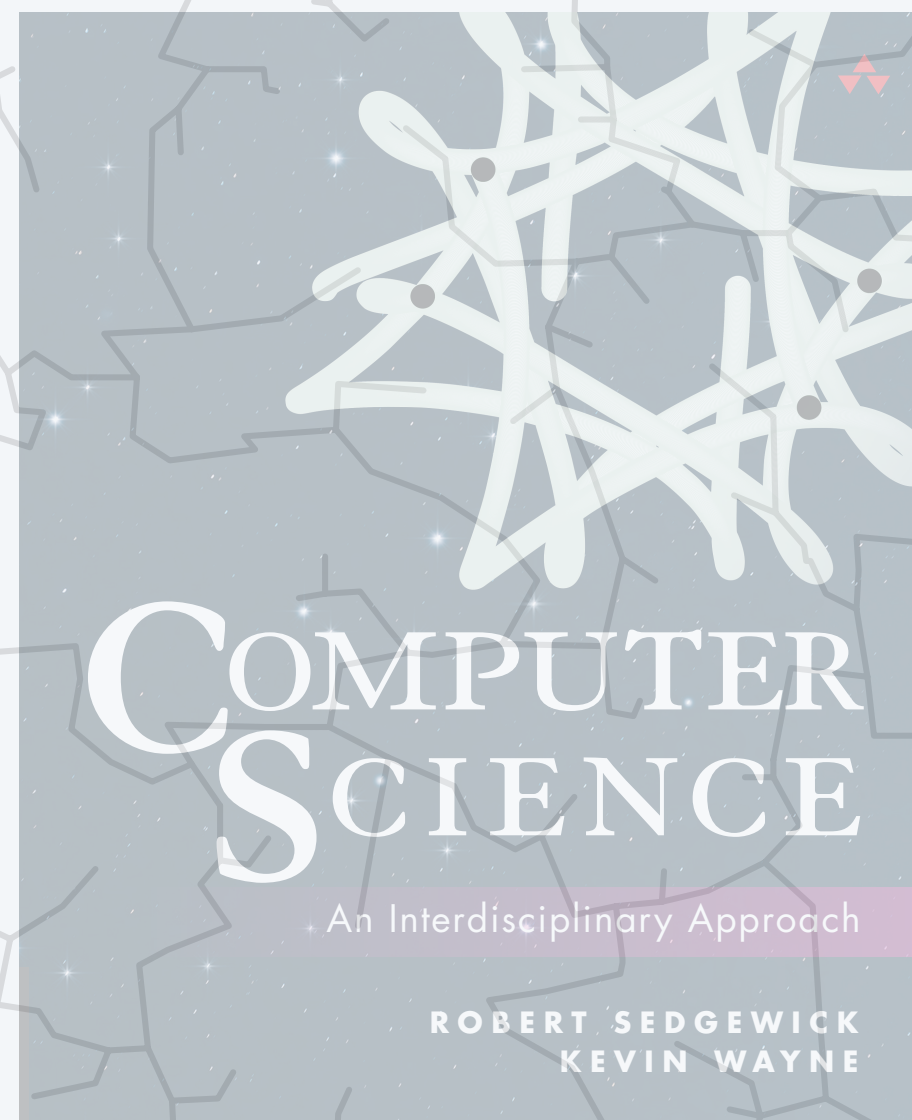
← *multimedia, computer games,
embedded devices, scientific computing, ...*



Prepare to learn about computer architecture. ← *see COS 375 / ECE 375*

- How does your computer's processor work?
- What are its basic components?
- How do they interact?





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Data and programs are encoded in binary



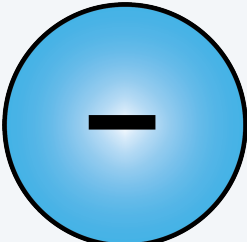
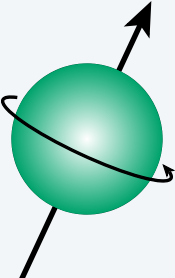



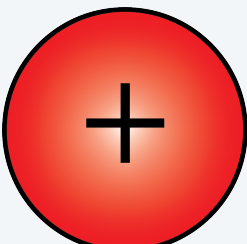
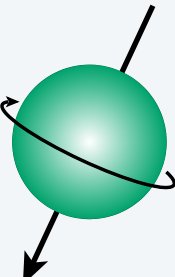

Bit (binary digit). Basic unit of information in computing: either 0 or 1.

Everything stored in a computer is a sequence of bits.

- **Data** and **programs**.
- Numbers, text, pictures, songs, movies, biometrics, 3D objects, ...

Q. Why binary?

A. Easy to represent two states in physical world.

0					
1					



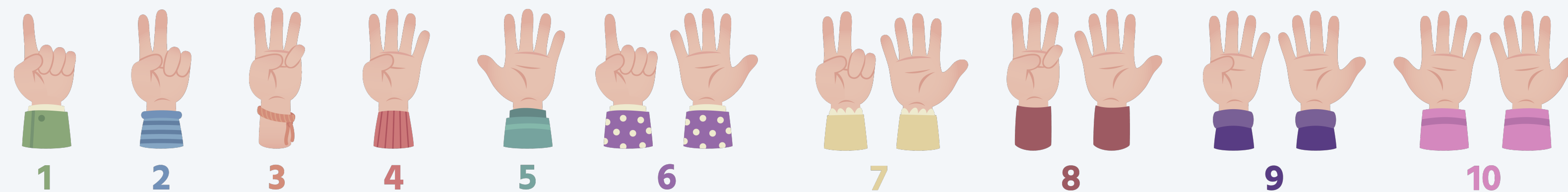
Decimal number system

Decimal number. A number expressed in base 10.

- Place-value notation with ten symbols (0–9).
- Used by most modern cultures.

	3	2	1	0	
decimal	6	3	7	5	

$6 \cdot 10^3 + 3 \cdot 10^2 + 7 \cdot 10^1 + 5 \cdot 10^0 = 6375_{10}$



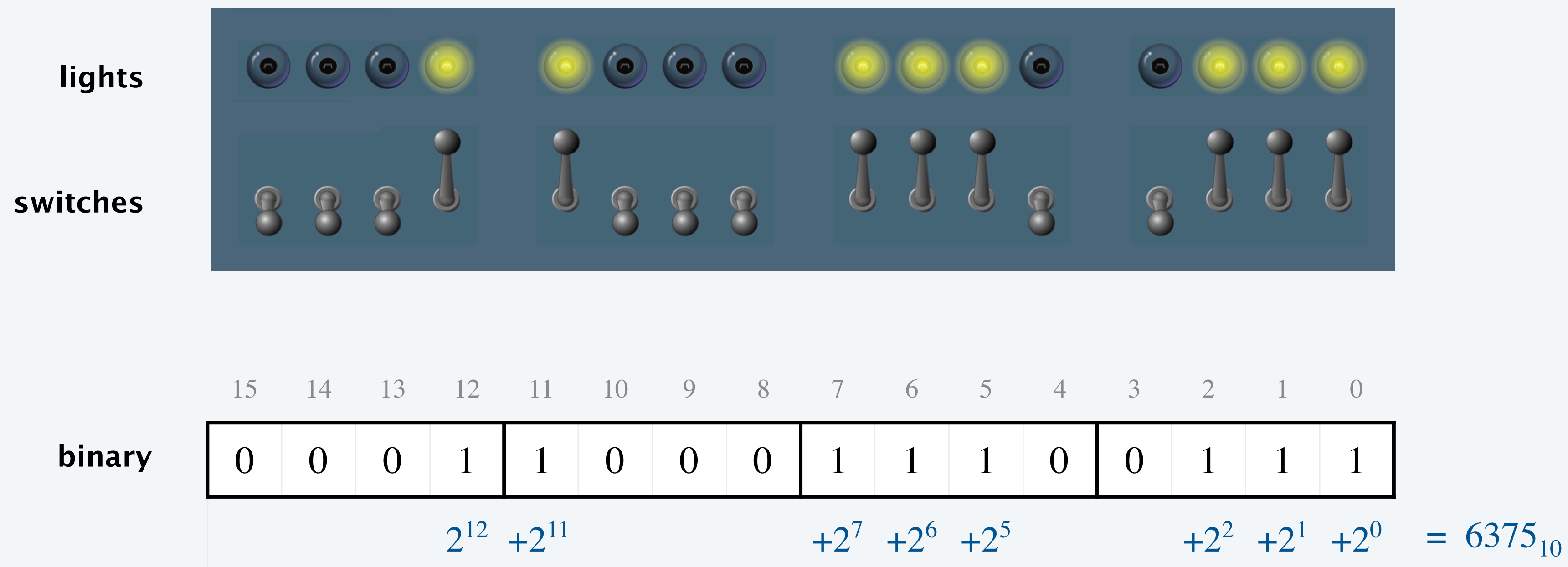
<u>decimal</u>
0
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15

Binary number system

Binary number. A number expressed in base 2.

- Place-value notation with two symbols (0 and 1).
- Used by all modern computers.

decimal	binary
0	0000
1	0001
2	0010
3	0011
4	0100
5	0101
6	0110
7	0111
8	1000
9	1001
10	1010
11	1011
12	1100
13	1101
14	1110
15	1111

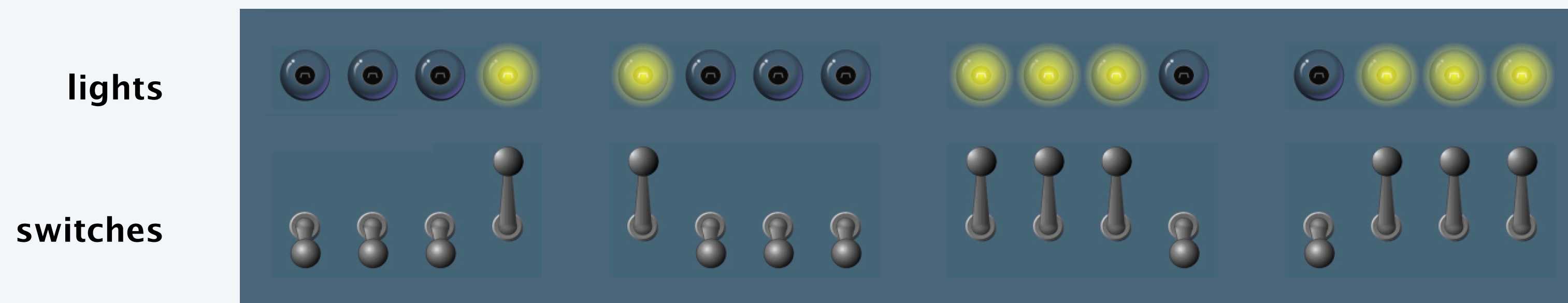


Hexadecimal number system

Hexadecimal number. A number expressed in base 16.

- Place-value notation with 16 symbols (0–9, A–F).
- Easy to convert from binary to hex (and vice versa). ← *4 bits per hex digit (because $2^4 = 16$)*
- More convenient for programmers.

decimal	binary	hex
0	0000	0
1	0001	1
2	0010	2
3	0011	3
4	0100	4
5	0101	5
6	0110	6
7	0111	7
8	1000	8
9	1001	9
10	1010	A
11	1011	B
12	1100	C
13	1101	D
14	1110	E
15	1111	F



	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
binary	0	0	0	1	1	0	0	0	1	1	1	0	0	1	1	1
hex	1				8				E				7			

$$1 \cdot 16^3 + 8 \cdot 16^2 + 14 \cdot 16^1 + 7 \cdot 16^0 = 6375_{10}$$



What is 1100 1010 1111 1110 in hexadecimal?

- A. 7F53
- B. CAB0
- C. CAFE
- D. FACE

decimal	binary	hex
0	0000	0
1	0001	1
2	0010	2
3	0011	3
4	0100	4
5	0101	5
6	0110	6
7	0111	7
8	1000	8
9	1001	9
10	1010	A
11	1011	B
12	1100	C
13	1101	D
14	1110	E
15	1111	F

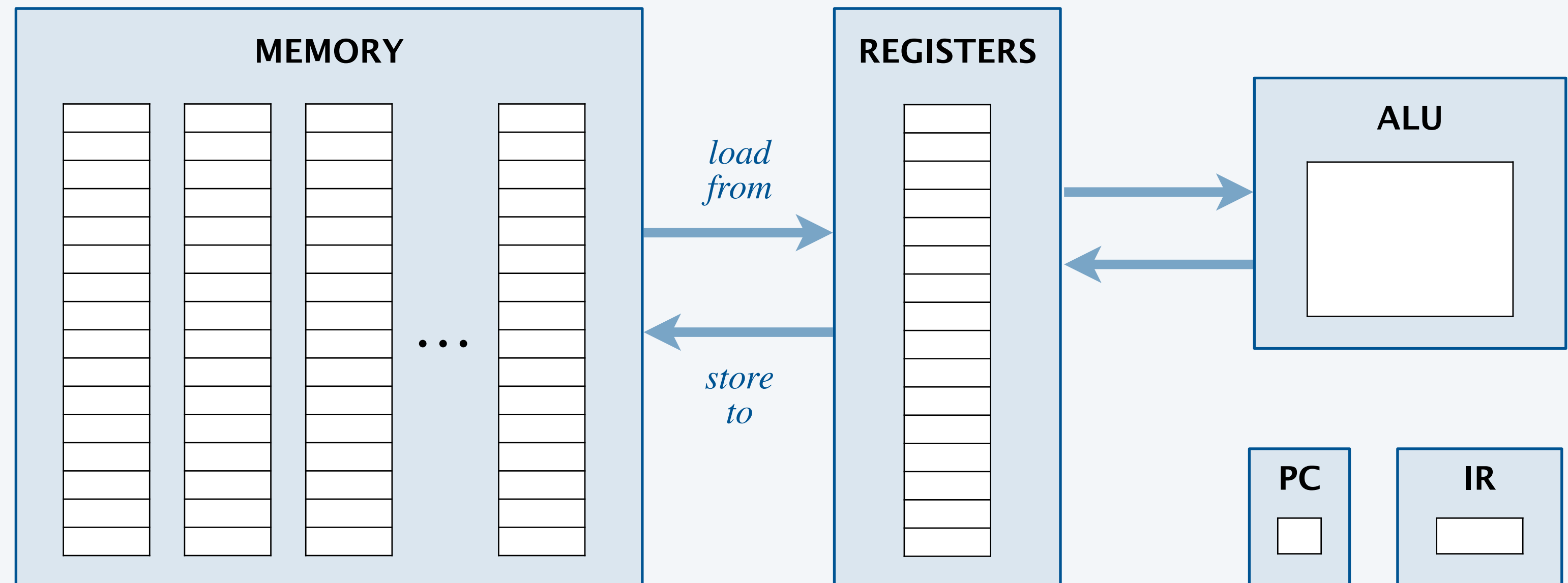
Binary t-shirt



Inside the box

TOY machine components.

- 256 memory cells.
- 16 registers.
- 1 arithmetic logic unit (ALU).
- 1 program counter (PC).
- 1 instruction register (IR).



Memory

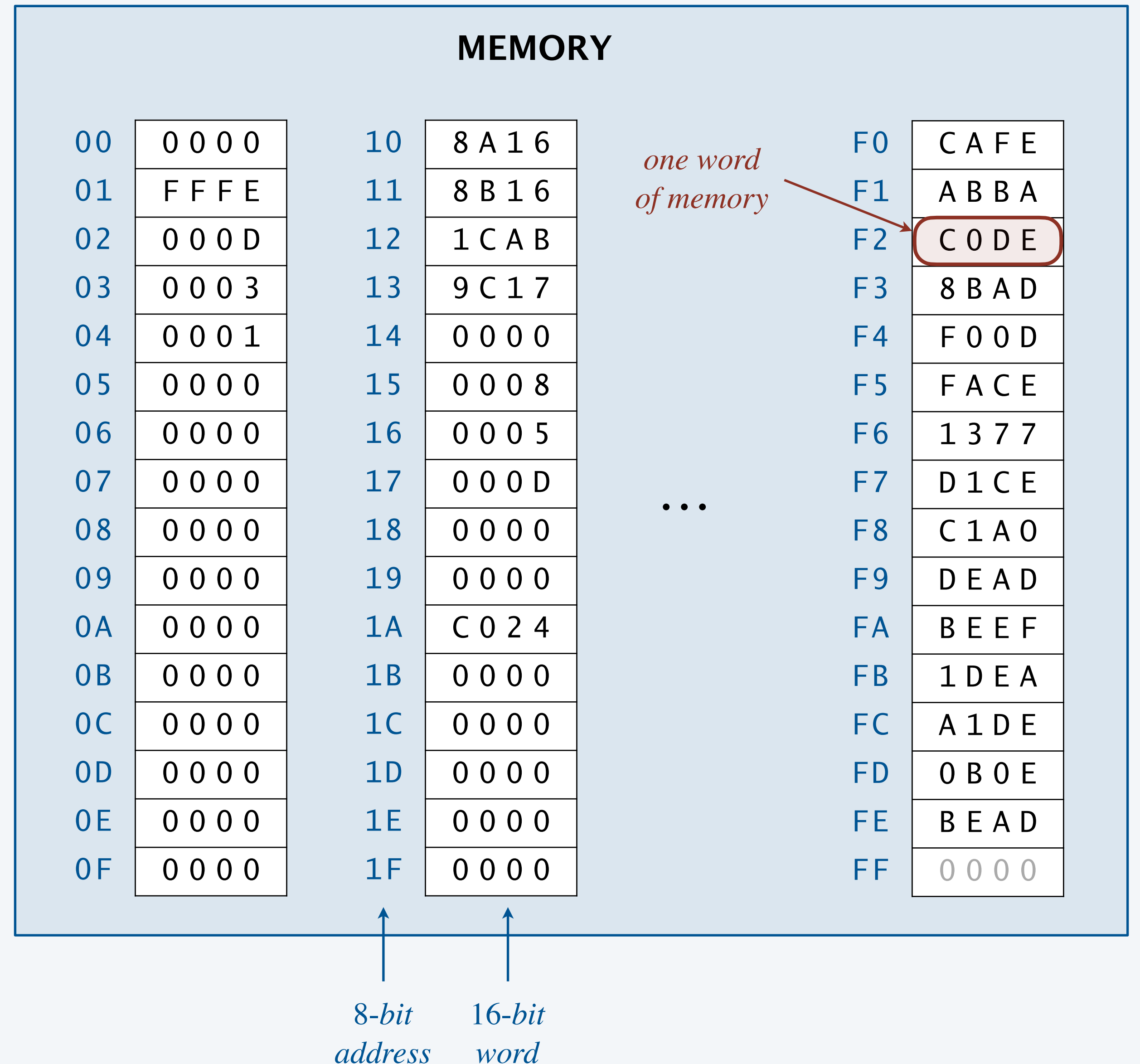
Memory.

- Holds data and instructions.
- 256 words of memory.
- 16 bits per word.

Memory is addressable.

- Specify individual word using array notation.
- Use hexadecimal for addresses: 00 to FF.
- Ex: $M[F2] = \text{CODE}$.

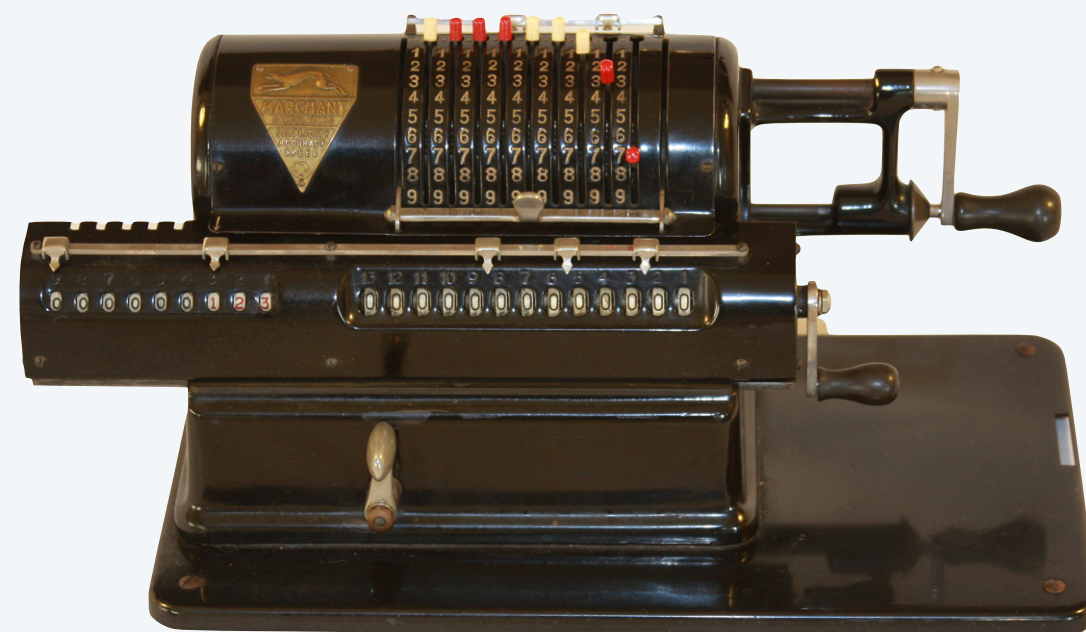
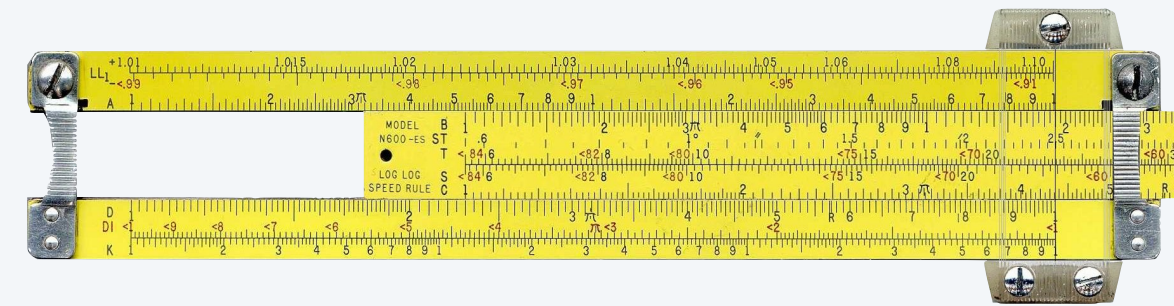
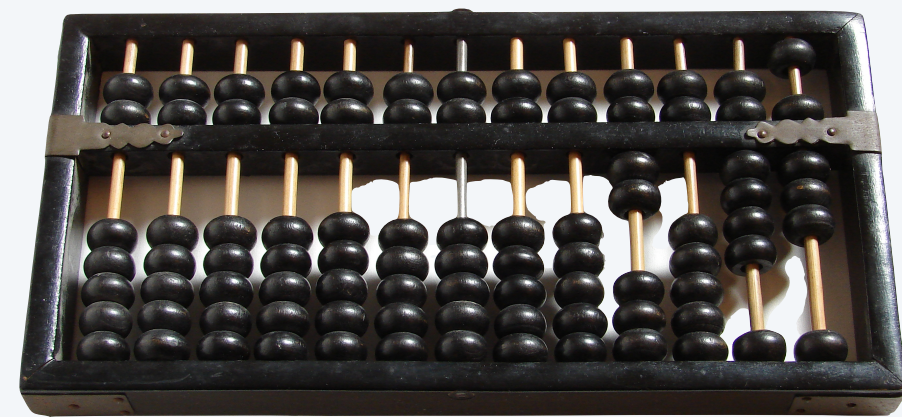
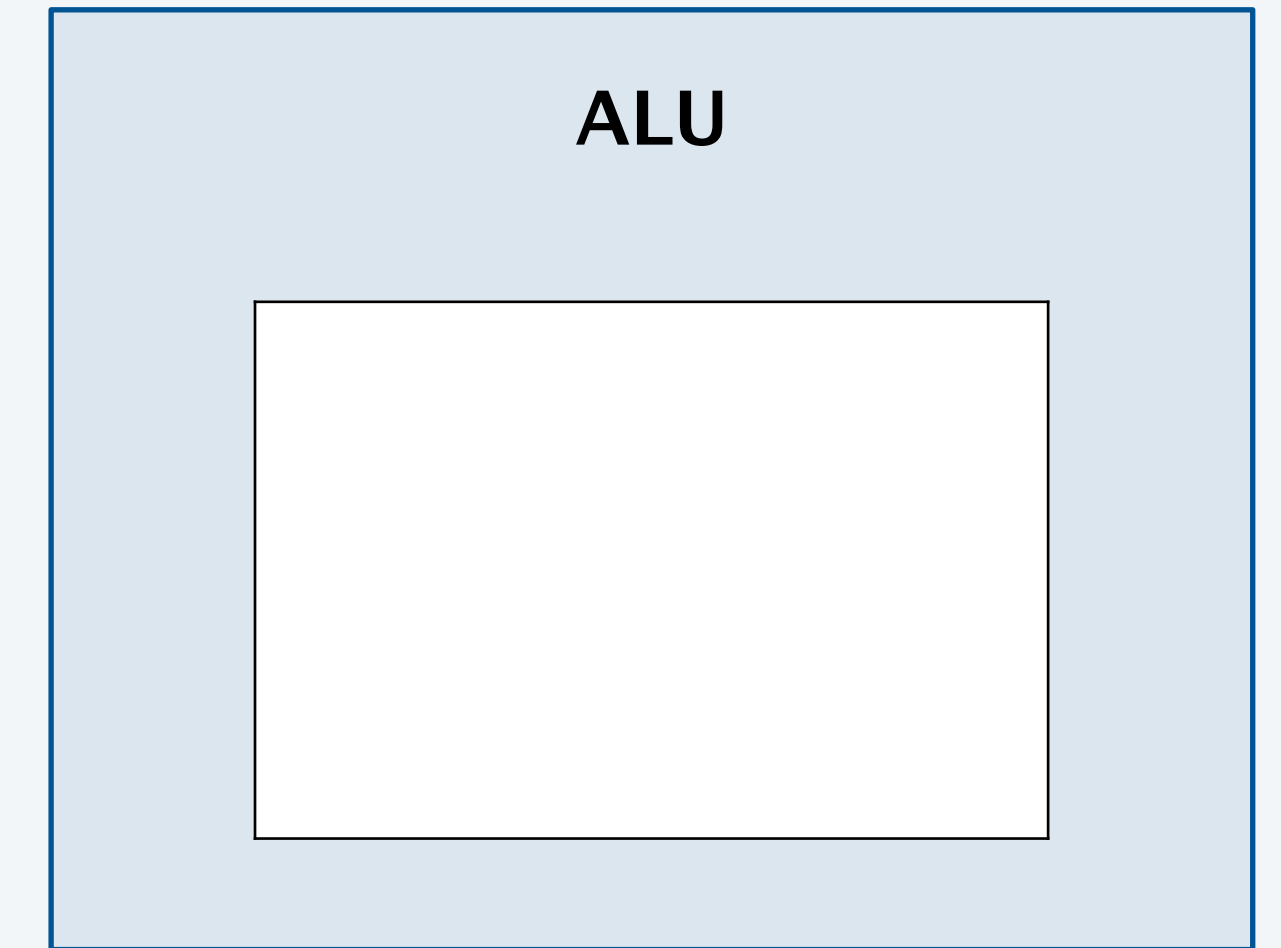
↑
*start thinking
in hexadecimal*



Arithmetic logic unit

Arithmetic logic unit (ALU).

- TOY's computational engine.
- A calculator, not a computer.
- **Hardware** that implements *all* data-type operations (e.g., add and subtract).



Registers

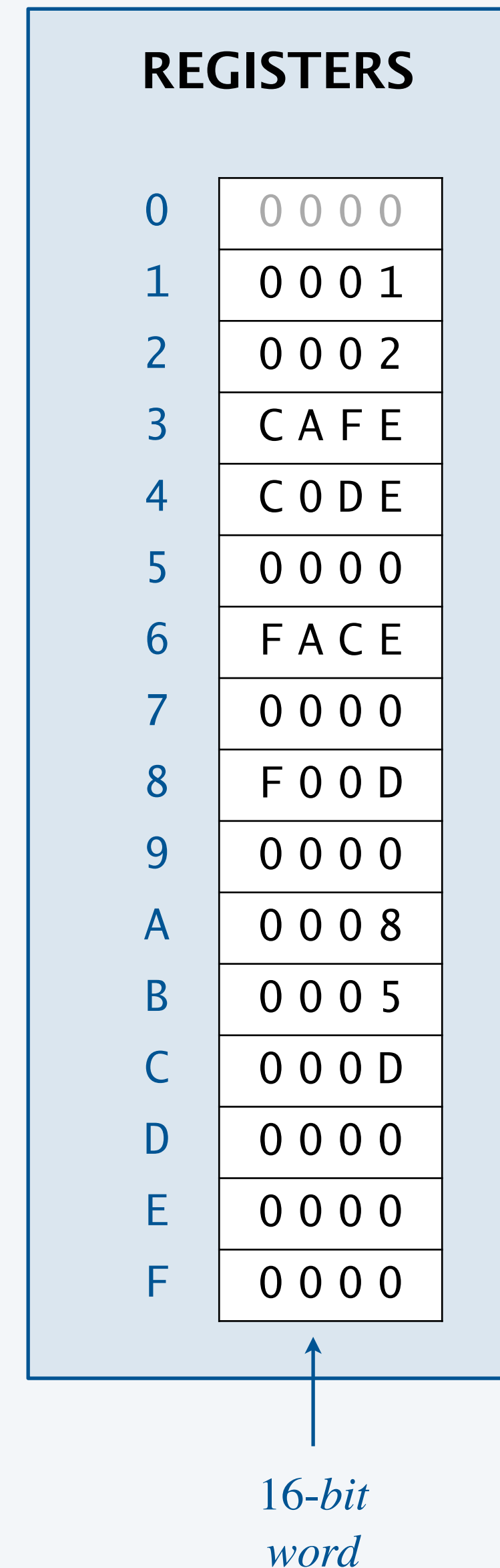
Registers.

- Scratch space for calculations and data movement.
- 16 registers, each storing one 16-bit word.
- Addressable as R[0] through R[F].
- R[0] always stores 0000.

Q. What's the difference between registers and main memory?

A. Registers are connected directly with ALU.

- faster than main memory
- more expensive than main memory

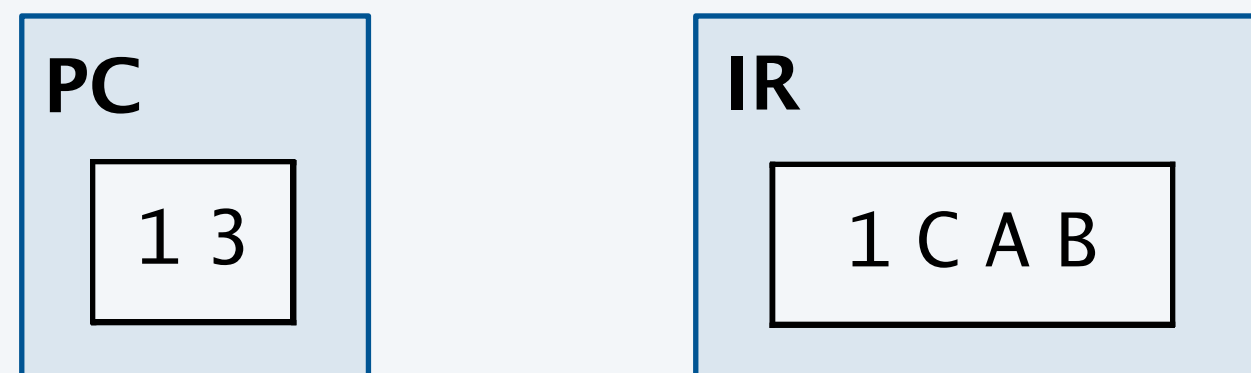


Control

TOY operates by executing a **sequence of instructions**.

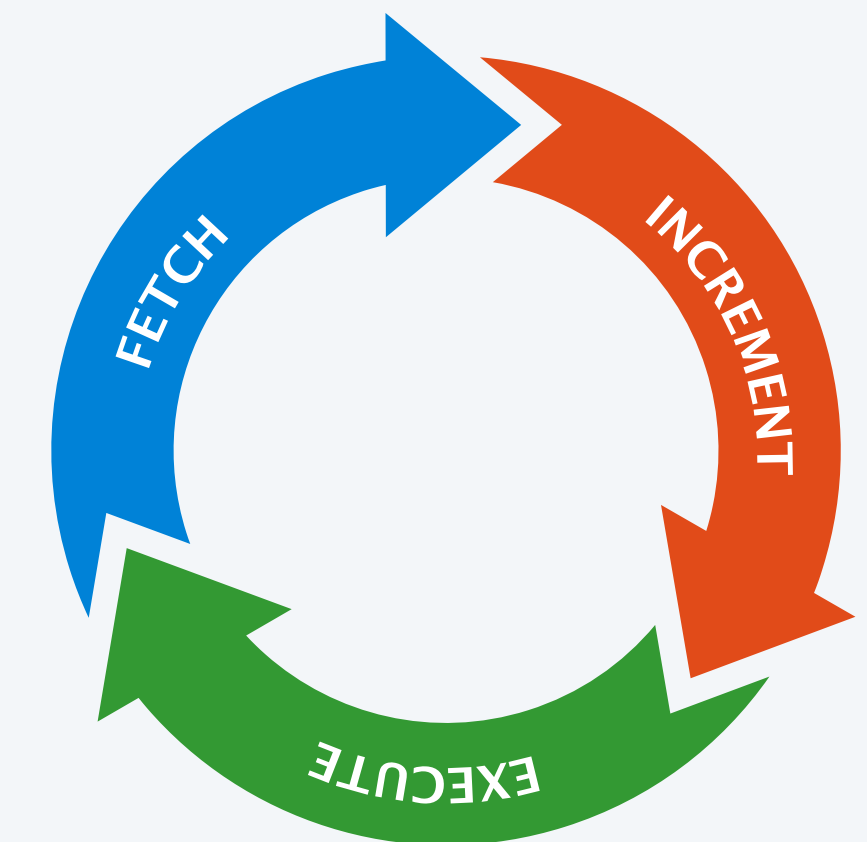
Program counter (PC). Stores memory address (8 bits) of *next* instruction to be executed.

Instruction register (IR). Stores instruction (16 bits) being executed.



Fetch-increment-execute cycle.

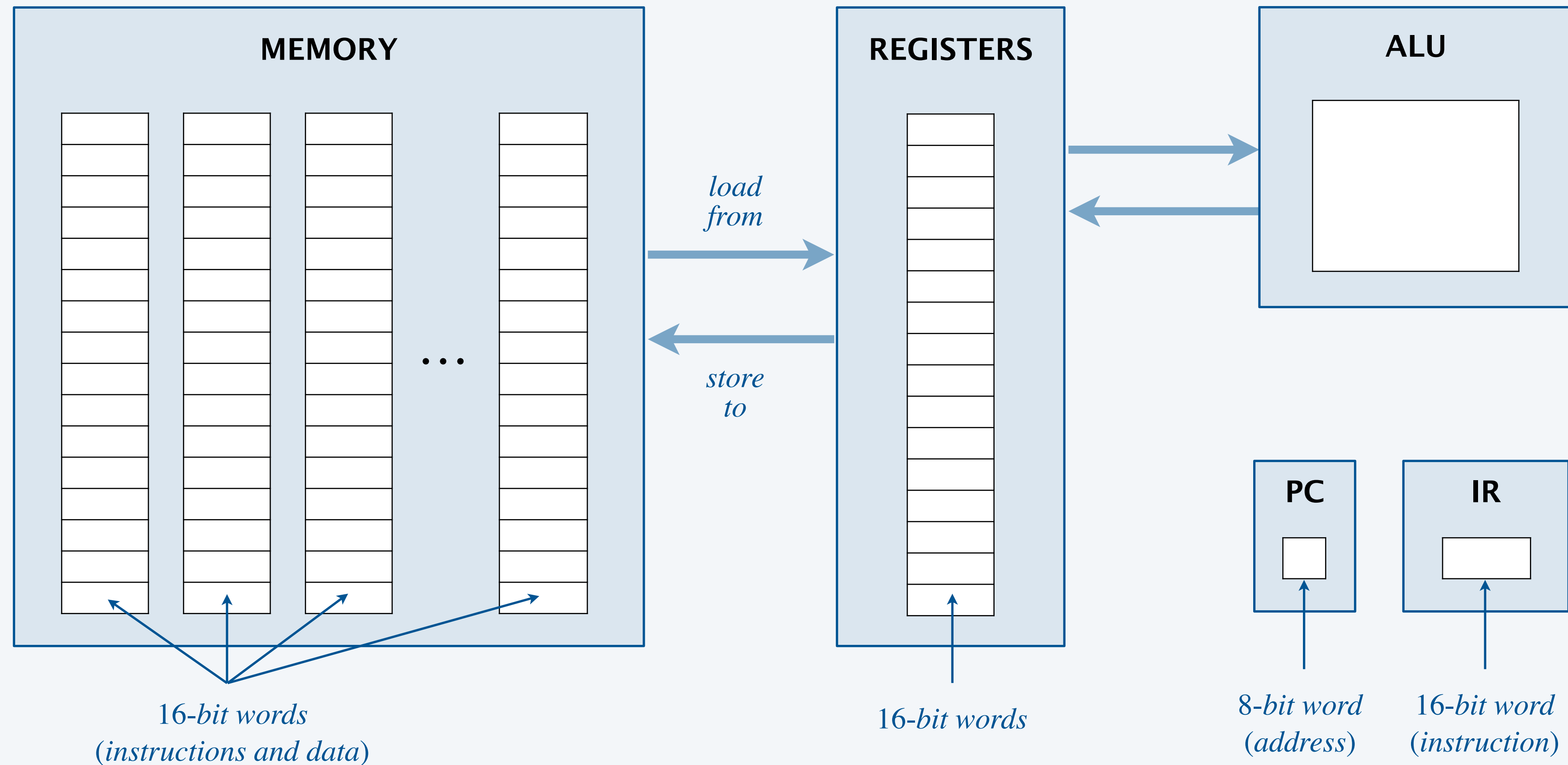
- **Fetch:** Get instruction (indexed by PC) from memory and store in IR.
- **Increment:** Update PC to point to next instruction.
- **Execute:** Move data to (or from) memory; change PC; or perform calculations.



The state of the machine

Contents of memory, registers, and PC at a particular time.

- Provide a record of what a program has done.
- Completely determines the machine will do.





Approximate how many **bytes** of main memory does TOY machine have?

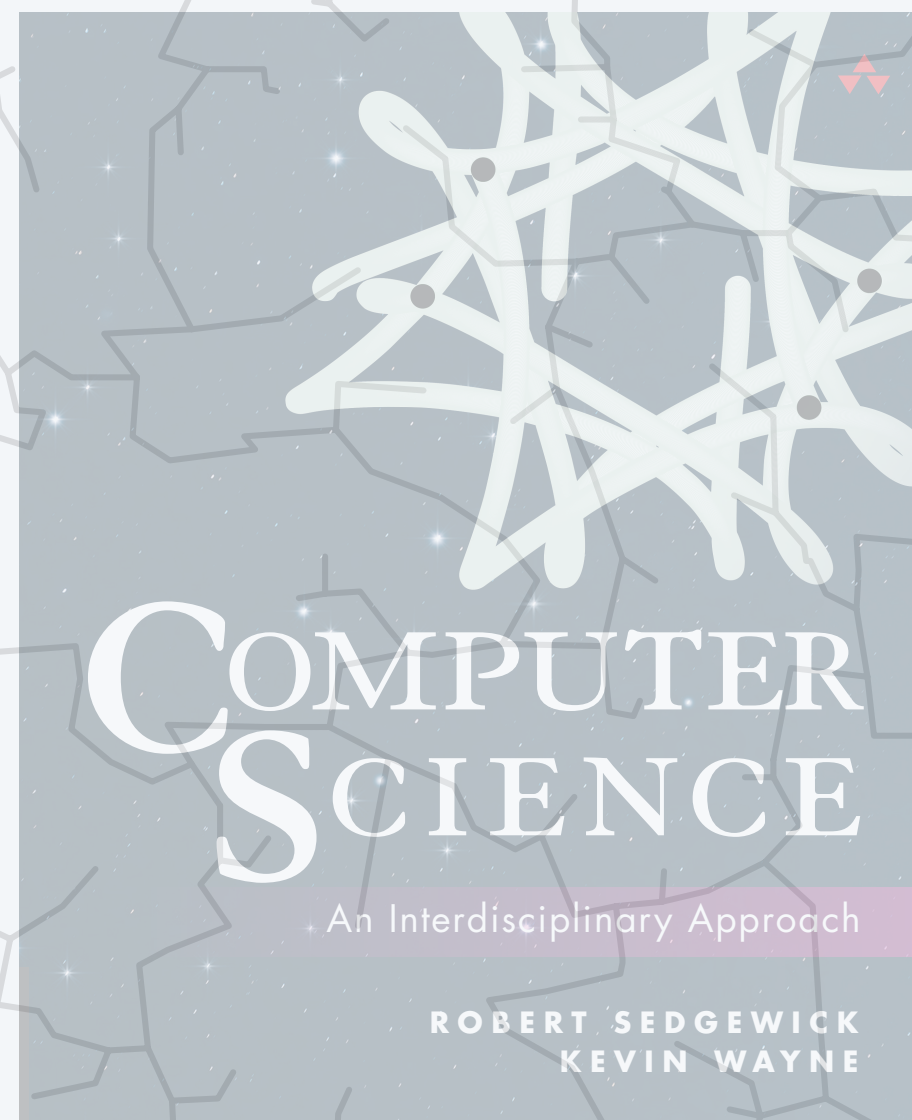
- A. 250 bytes
- B. 500 bytes
- C. 4000 bytes
- D. 250 MB
- E. 500 GB

term	symbol	quantity
<i>bit</i>	b	1 bit
<i>byte</i>	B	8 bits
<i>kilobyte</i>	KB	1000 bytes
<i>megabyte</i>	MB	1000^2 bytes
<i>gigabyte</i>	GB	1000^3 bytes
<i>terabyte</i>	TB	1000^4 bytes
⋮	⋮	⋮

↑
some define using powers of 2
 (MB = 2^{10} bytes)



**6 GB main memory,
 1 TB internal storage**



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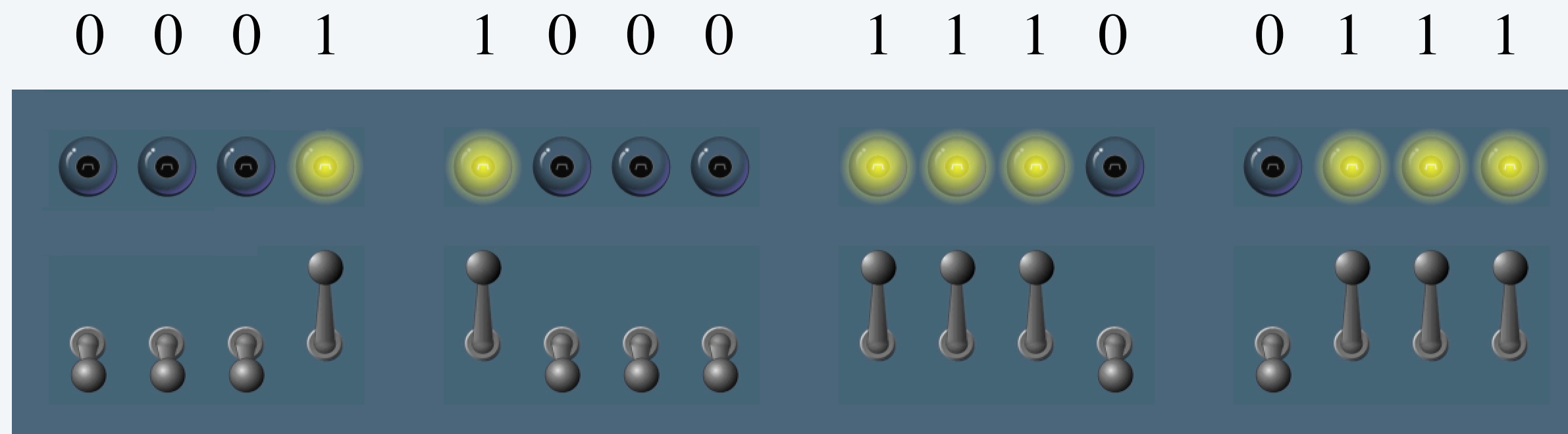
TOY data type

A **data type** is a set of values and a set of operations on those values.

TOY's data type.

- Value: **16-bit two's complement integer**.
- Operations: arithmetic (add, subtract) and bitwise (AND, XOR, shift).

Representation. Each value is represented using one 16-bit word.



Note. All other types of data must be implemented with software. ←

*32-bit integers, floating-point numbers,
booleans, characters, strings, ...*

Unsigned integers (16 bit)

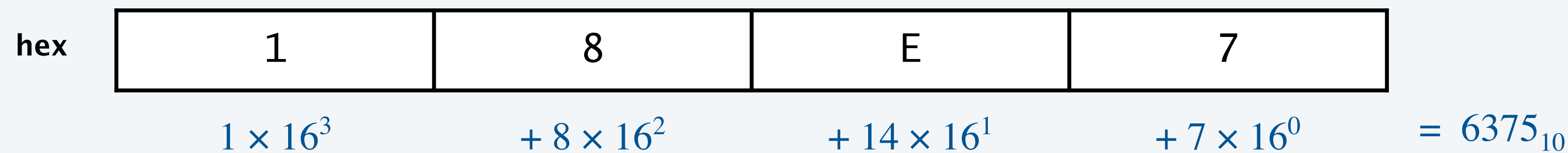
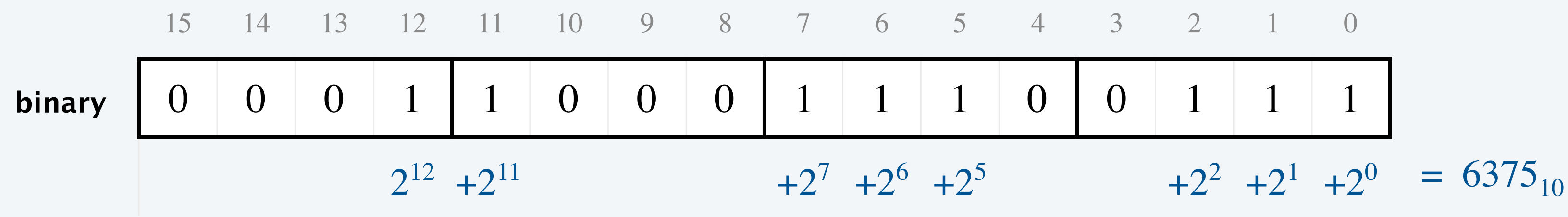
Values. Integers 0 to $2^{16} - 1$. *← only non-negative integers*

Operations.

- Arithmetic: add, subtract.
- Bitwise: AND, XOR, left shift, right shift.

Representation. 16 bits.

decimal	hex	binary
0	0000	0000000000000000
1	0001	0000000000000001
2	0002	0000000000000010
3	0003	0000000000000011
4	0004	0000000000000100
⋮	⋮	⋮
65,533	FFFD	1111111111111101
65,534	FFFE	1111111111111110
65,535	FFFF	1111111111111111



largest integer
($2^{16} - 1$)

Signed integers (16-bit two's complement)

Values. Integers -2^{15} to $2^{15} - 1$. *← includes negative integers!*

Operations.

- Arithmetic: add, subtract.
- Bitwise: AND, XOR, left shift, right shift.
- Comparison: test if positive, test if zero.

Representation. 16-bit two's complement.

- For $0 \leq x < 2^{15}$, 16-bit unsigned representation of x .
- For $-2^{15} \leq x < 0$, 16-bit unsigned representation of $2^{16} - |x|$.

	decimal	hex	binary
	-32,768	8000	1000000000000000
	-32,767	8001	1000000000000001
	-32,766	8002	1000000000000010
	⋮	⋮	⋮
	-3	FFFD	1111111111111101
	-2	FFFE	1111111111111110
	-1	FFFF	1111111111111111
	0	0000	0000000000000000
	+1	0001	0000000000000001
	+2	0002	0000000000000010
	+3	0003	0000000000000011
	⋮	⋮	⋮
	+32,765	7FFD	0111111111111101
	+32,766	7FFE	0111111111111110
	+32,767	7FFF	0111111111111111

smallest integer (-2^{15}) →

representation for 0 is
0000000000000000

→ *largest integer* ($2^{15} - 1$)

↑ *sign bit*

Calculations with two's complement integers

Addition. To compute $x + y$:

- Add as unsigned integers.
- Ignore any overflow.

1111111110000010	-126_{10}
+ 0000001111101000	$1,000_{10}$
<hr/>	
0000001101101010	874_{10}
↑	
<i>ignore carry bit out sign bit</i>	

Negation. To convert from x to $-x$ (or vice versa):

- Flip all bits.
- Add 1.

0000000001111110	126_{10}
1111111110000001	<i>flip bits</i>
+ 0000000000000001	<i>add 1</i>
<hr/>	
1111111110000010	-126_{10}

Overflow with two's complement integers

Integer overflow. Result of arithmetic operation is outside prescribed range (too large or small).

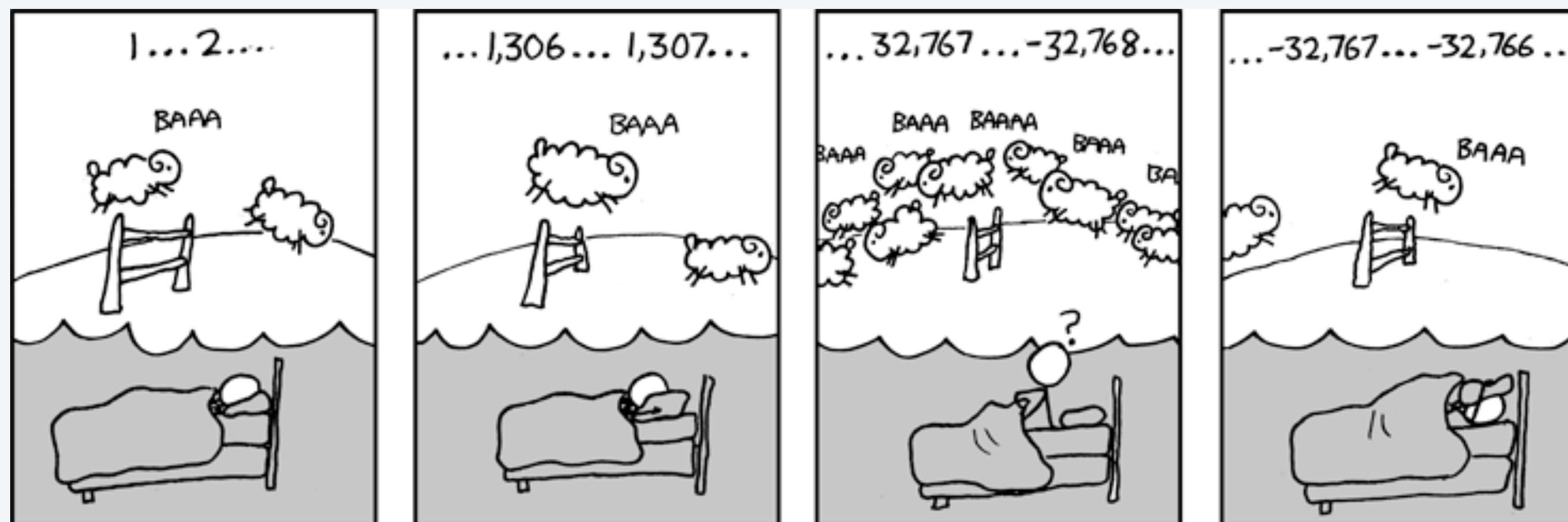
$$\begin{array}{r} 0111111111111111 \\ + 0000000000000001 \\ \hline 1000000000000000 \end{array}$$

$32,767_{10}$ ← largest integer ($2^{15} - 1$)

1_{10}

$-32,768_{10}$ ← smallest integer (-2^{15})

↑
overflow (carry into sign bit)

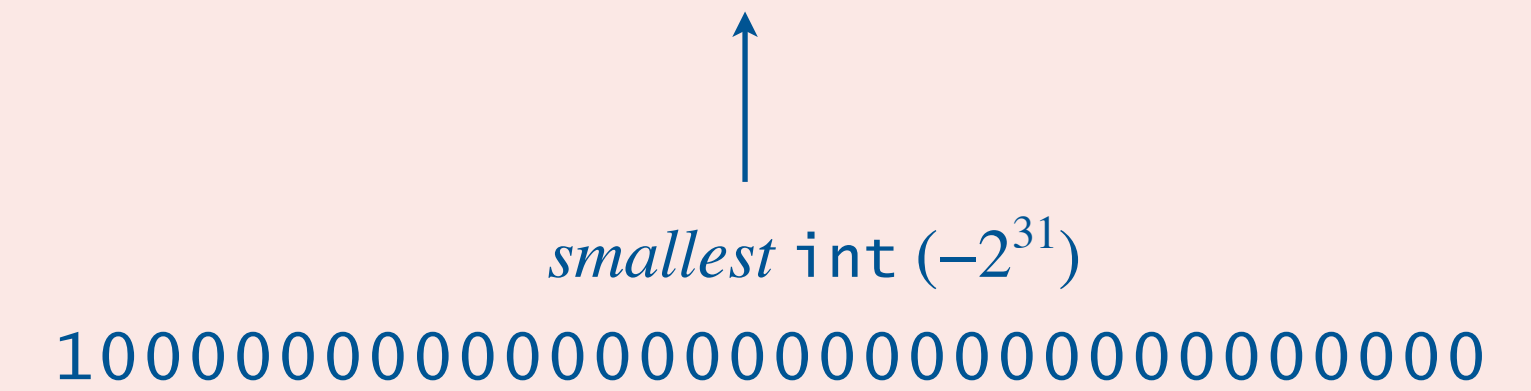


<https://xkcd.com/571>



Java's `int` data type is a 32-bit two's complement integer. What is `Math.abs(-2147483648)` ?

- A. -2147483648
- B. 2147483647
- C. 2147483648
- D. `ArithmeticOverflowError`



TOY data type: bitwise operations

Bitwise AND. Apply *and* operation to corresponding bits.

	0	1	0	1	1	0	0	1	0	1	0	0	1	0	0	0
&	0	0	0	1	1	1	1	1	0	0	0	0	0	1	0	1
<hr/>																
	0	0	0	1	1	0	0	1	0	0	0	0	0	0	0	0

<i>x</i>	<i>y</i>	<i>x & y</i>
0	0	0
0	1	0
1	0	0
1	1	1

AND

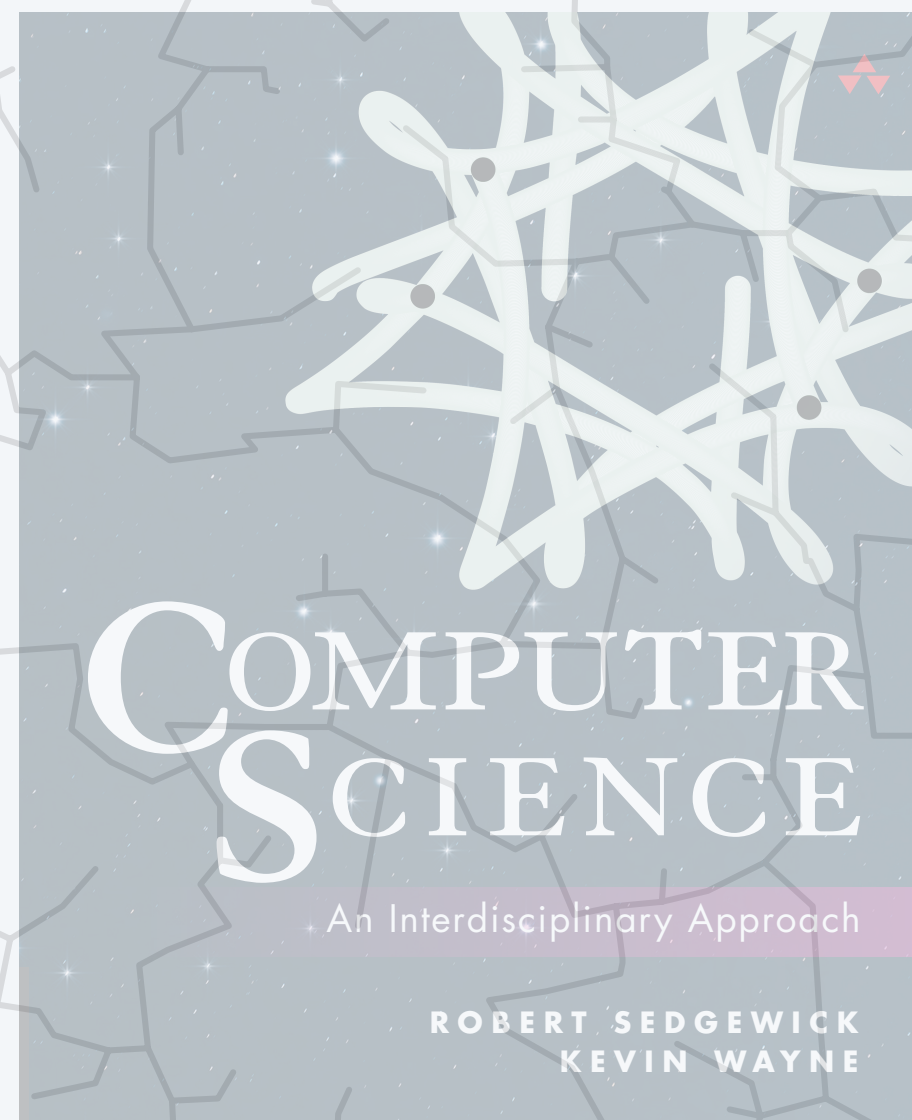
Bitwise XOR. Apply *xor* operation to corresponding bits.

	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1
^	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1
<hr/>																
	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0

<i>x</i>	<i>y</i>	<i>x ^ y</i>
0	0	0
0	1	1
1	0	1
1	1	0

XOR

```
~/toy/toy1> jshell
jshell> int a = 3 ^ 5;
a ==> 6
```



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TOY instructions: halt

TOY program. A TOY program is a sequence of TOY instructions.

Instructions. Any 16-bit value can be interpreted as a TOY instruction.

Halt. Stop executing the program.

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0				0				0				0			
<i>opcode</i> (halt)				<i>destination d</i>				<i>source s</i>				<i>source t</i>			

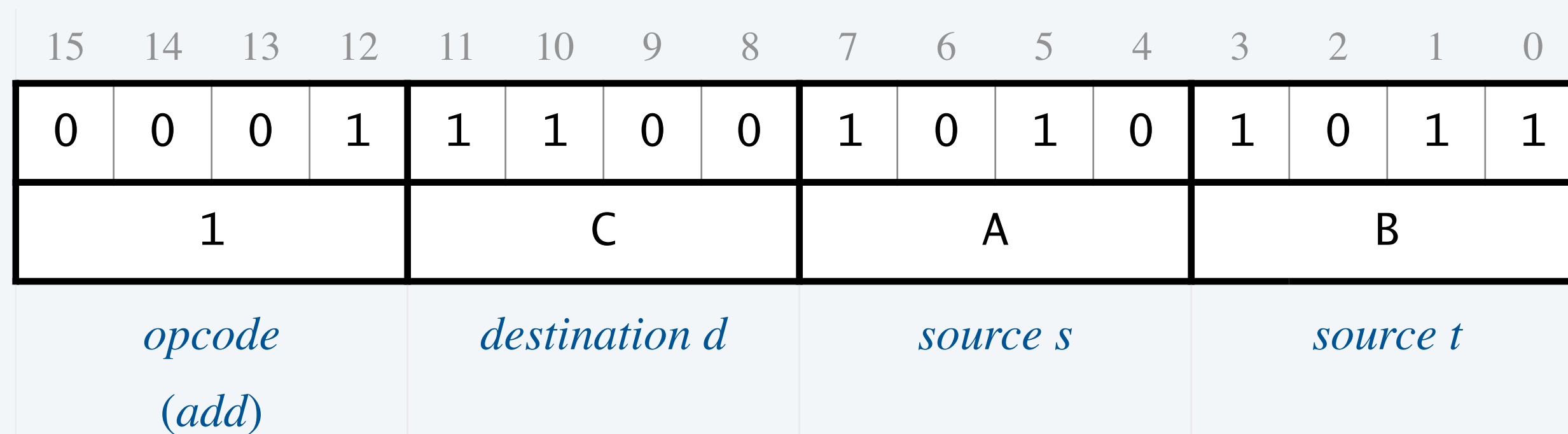


TOY instructions: add

TOY program. A TOY program is a sequence of TOY instructions.

Instructions. Any 16-bit value can be interpreted as a TOY instruction.

Add. Add two 16-bit integers from registers and store the sum in a register.



Pseudocode. $R[C] = R[A] + R[B]$ ← *add R[A] and R[B];
put result in R[C]*

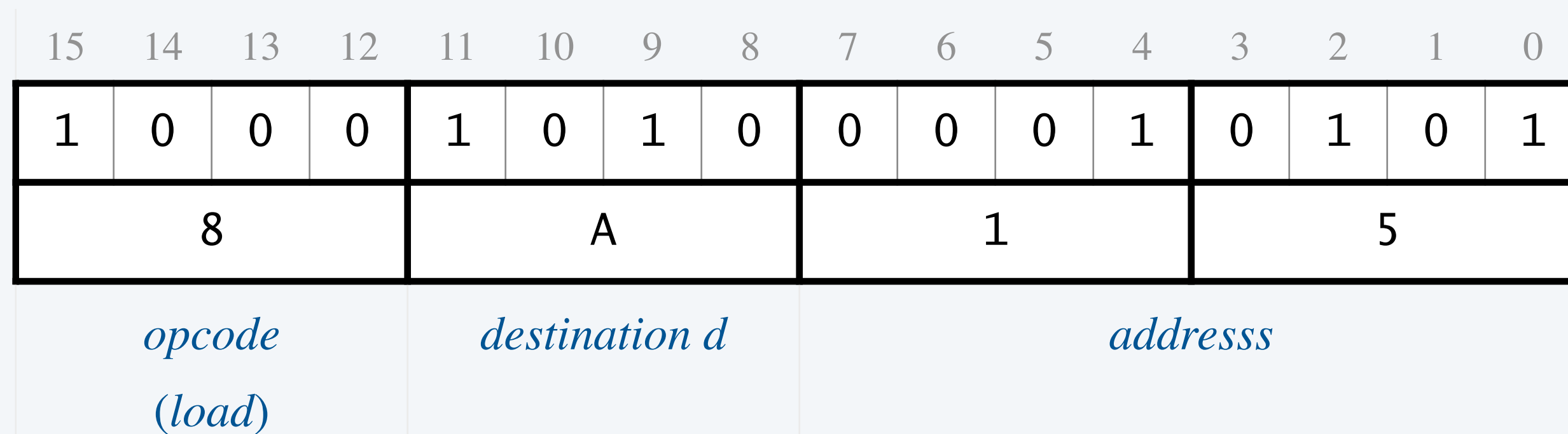
Registers				
R[0]	0	0	0	0
R[1]	0	0	0	1
R[2]	0	0	1	0
R[3]	C	A	F	E
R[4]	0	0	0	1
R[5]	0	0	0	0
R[6]	C	0	D	E
R[7]	0	0	0	0
R[8]	F	0	0	D
R[9]	0	0	0	0
R[A]	0	0	0	8
R[B]	0	0	0	5
R[C]	0	0	0	D
R[D]	0	0	0	0
R[E]	0	0	0	0
R[F]	0	0	0	0

TOY instructions: load and store

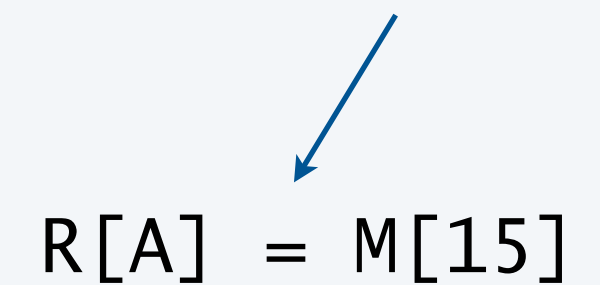
TOY program. A TOY program is a sequence of TOY instructions.

Instructions. Any 16-bit value can be interpreted as a TOY instruction.

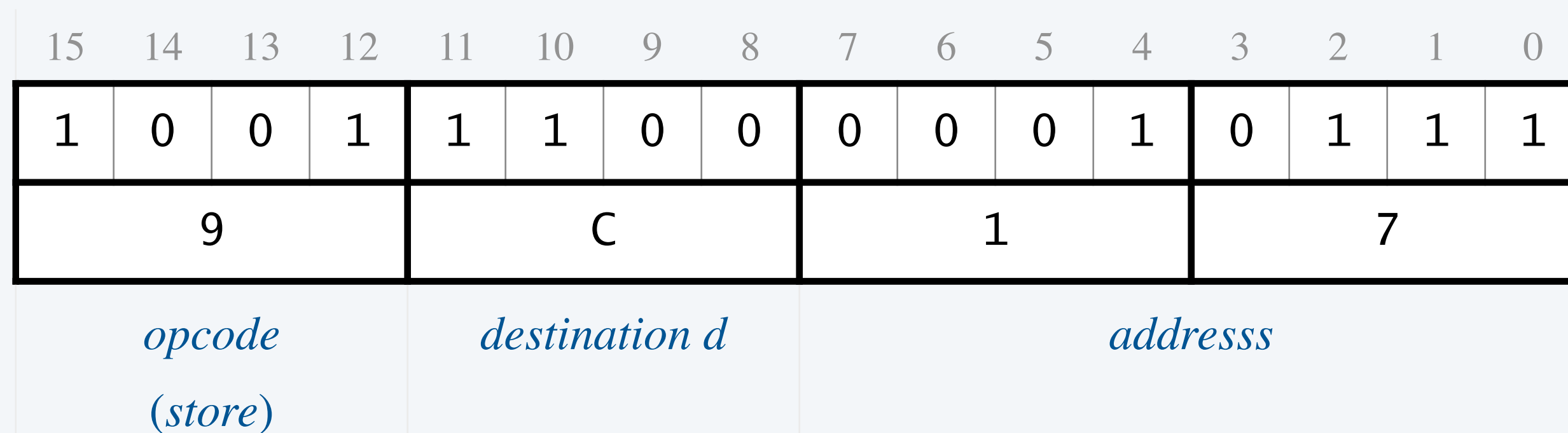
Load. Copy a 16-bit integer from a memory cell to a register.



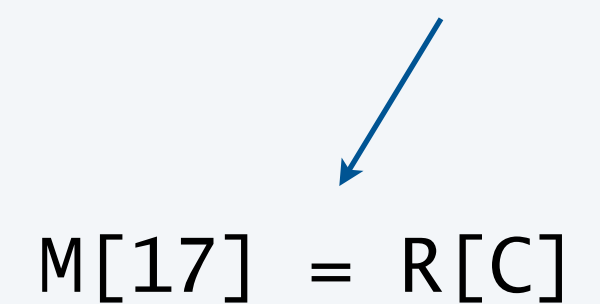
load data from M[15] into R[A]



Store. Copy a 16-bit integer from a register to a memory cell.



store contents of R[C] into M[17]





Add two integers.

- Load operands from memory into two registers.
- Add the 16-bit integers in the two registers.
- Store the result in memory.

MEMORY		
⋮	⋮	
10:	8A15	R[A] = M[15]
11:	8B16	R[B] = M[16]
12:	1CAB	R[C] = R[A] + R[B]
13:	9C17	M[17] = R[C]
14:	0000	halt
15:	0008	input 1
16:	0005	input 2
17:	0000	output
⋮	⋮	

REGISTERS	
⋮	⋮
R[A]	0 0 0 0
R[B]	0 0 0 0
R[C]	0 0 0 0
⋮	⋮

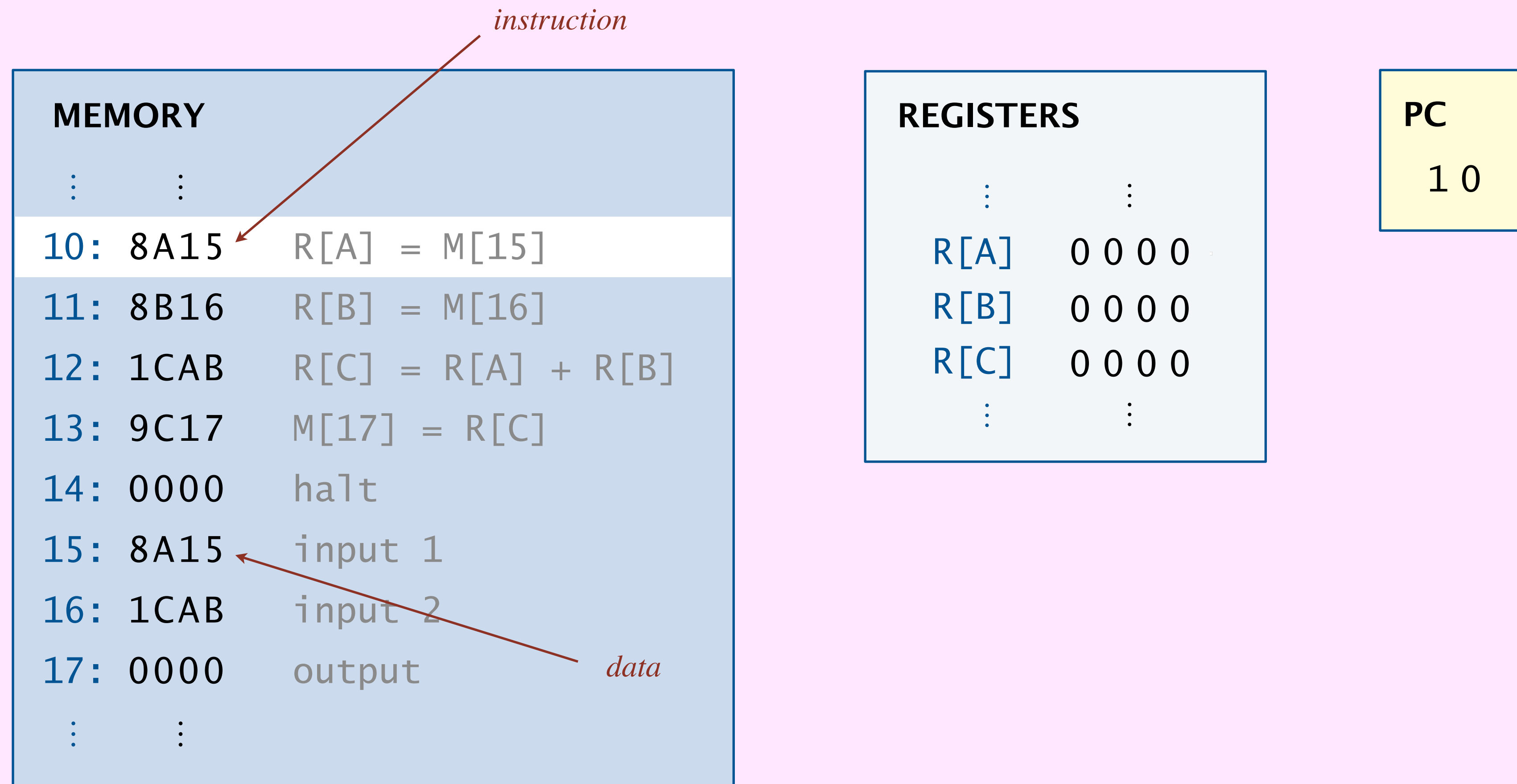
PC
10

Your first TOY program (with different data)



Q. How can you tell whether a word is an instruction or data?

A. If the PC has its address, it is an instruction.



Instruction set

Instruction set. Complete list of machine instructions.

- First hex digit (**opcode**) specifies which instruction.
- Each instruction changes machine in well-defined way.

opcode	instruction
0	<i>halt</i>
1	<i>add</i>
2	<i>subtract</i>
3	<i>bitwise and</i>
4	<i>bitwise xor</i>
5	<i>shift left</i>
6	<i>shift right</i>
7	<i>load address</i>
8	<i>load</i>
9	<i>store</i>
A	<i>load indirect</i>
B	<i>store indirect</i>
C	<i>branch if zero</i>
D	<i>branch if positive</i>
E	<i>jump register</i>
F	<i>jump and link</i>

category	opcodes	implements	changes
<i>arithmetic and logic operations</i>	1 2 3 4 5 6	data-type operations	registers
<i>data movement</i>	7 8 9 A B	data moves between registers and memory	registers, memory
<i>flow of control</i>	0 C D E F	conditionals, loops, and functions	program counter

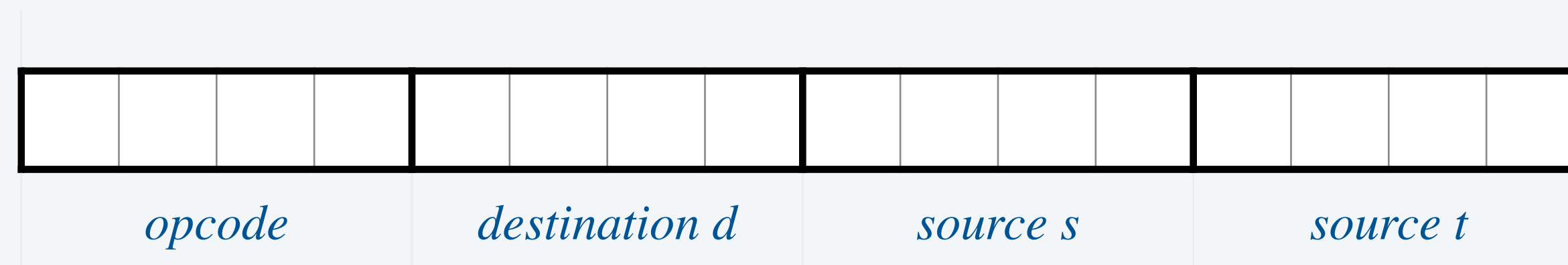
Instruction set

Instruction set. Complete list of machine instructions.

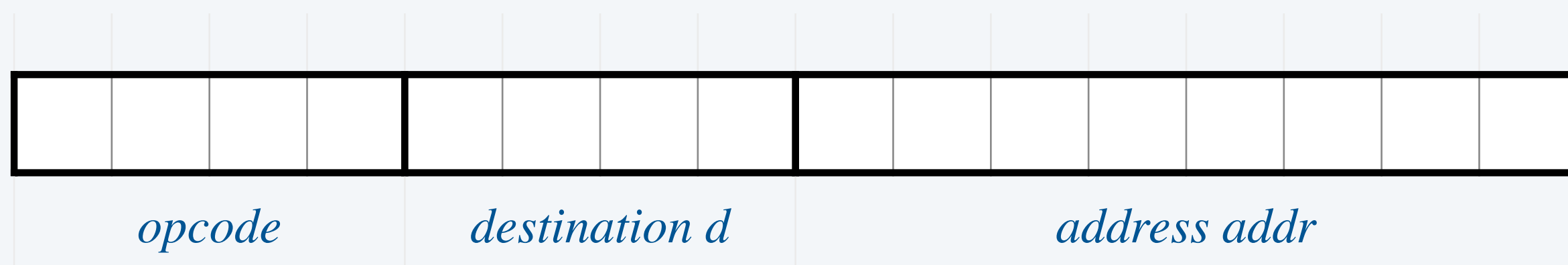
- First hex digit (**opcode**) specifies which instruction.
- Each instruction changes machine in well-defined way.

Instruction formats. How to interpret a 16-bit instruction?

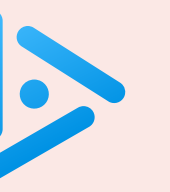
- Format *RR*: opcode and three registers.



- Format *A*: opcode, one register, and one memory address.

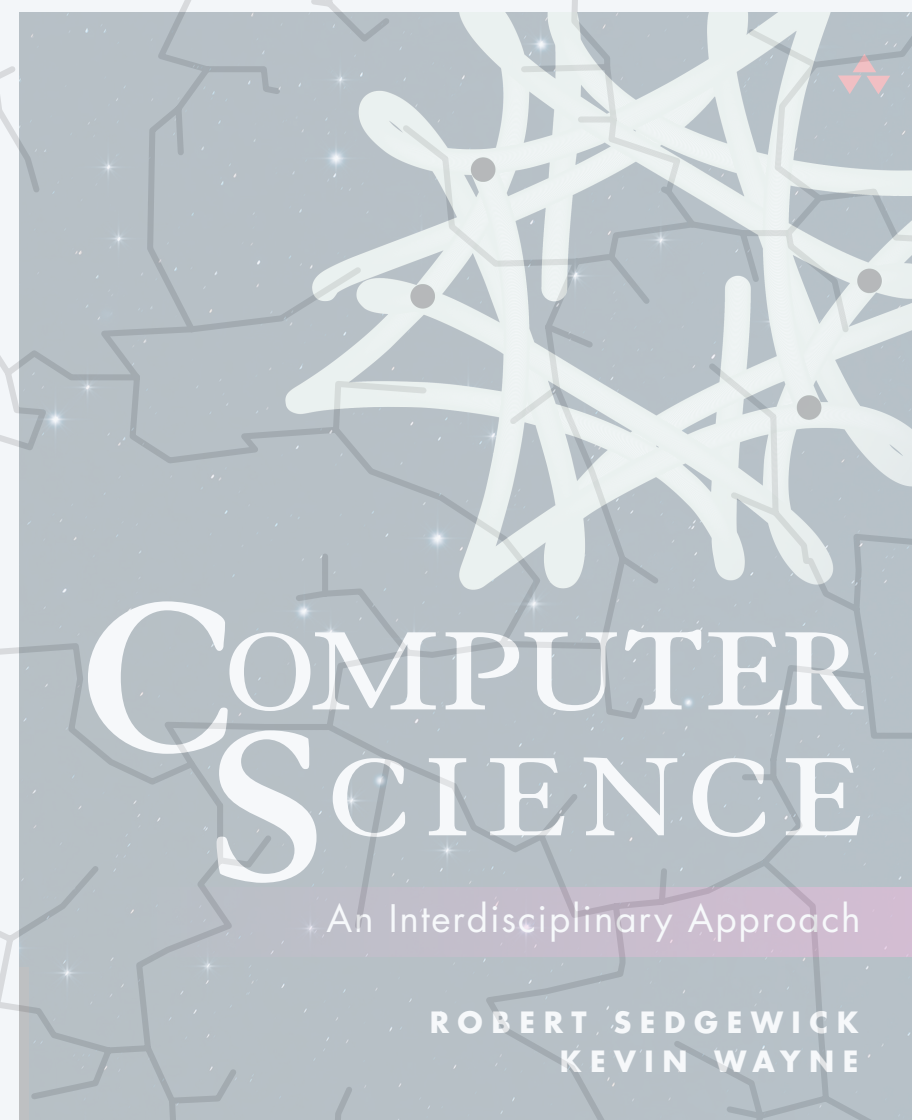


opcode	format	instruction
0	—	<i>halt</i>
1	<i>RR</i>	<i>add</i>
2	<i>RR</i>	<i>subtract</i>
3	<i>RR</i>	<i>bitwise and</i>
4	<i>RR</i>	<i>bitwise xor</i>
5	<i>RR</i>	<i>shift left</i>
6	<i>RR</i>	<i>shift right</i>
7	<i>A</i>	<i>load address</i>
8	<i>A</i>	<i>load</i>
9	<i>A</i>	<i>store</i>
A	<i>RR</i>	<i>load indirect</i>
B	<i>RR</i>	<i>store indirect</i>
C	<i>A</i>	<i>branch if zero</i>
D	<i>A</i>	<i>branch if positive</i>
E	<i>RR</i>	<i>jump register</i>
F	<i>A</i>	<i>jump and link</i>



Which instruction copies the values in R[A] to R[B] ?

- A.** 1BA0 $R[B] = R[A] + R[0]$
- B.** 2BA0 $R[B] = R[A] - R[0]$
- C.** 3BAA $R[B] = R[A] \& R[A]$
- D.** All of the above.
- ← *R[0] is always 0000*



<https://introcs.cs.princeton.edu>

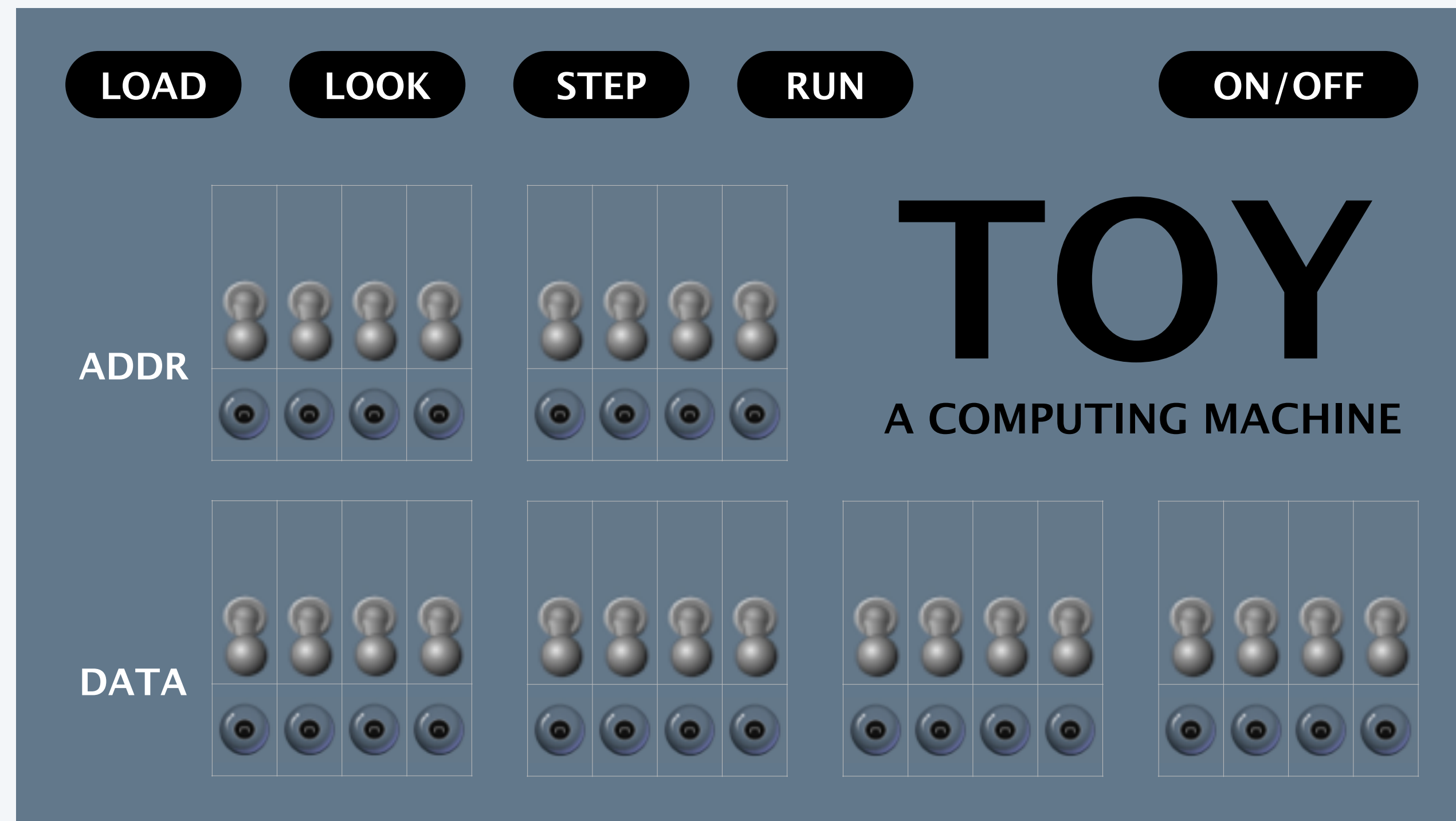
6. TOY MACHINE I

- ▶ *overview*
- ▶ *data types*
- ▶ *instructions*
- ▶ *operating the machine*

Outside the box

User interface

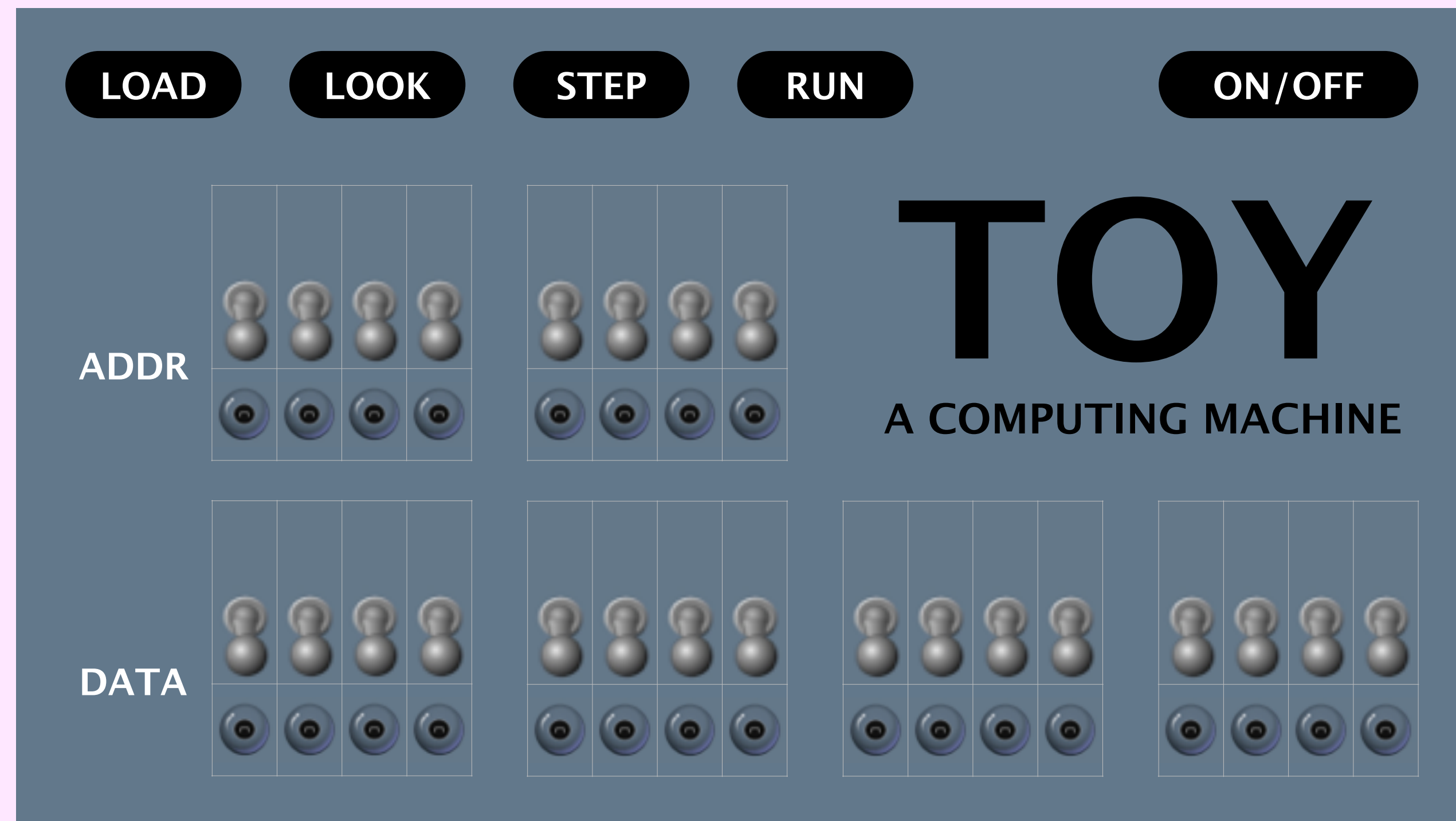
- Switches.
- Lights.
- Control buttons.





To load an instruction or data into memory:

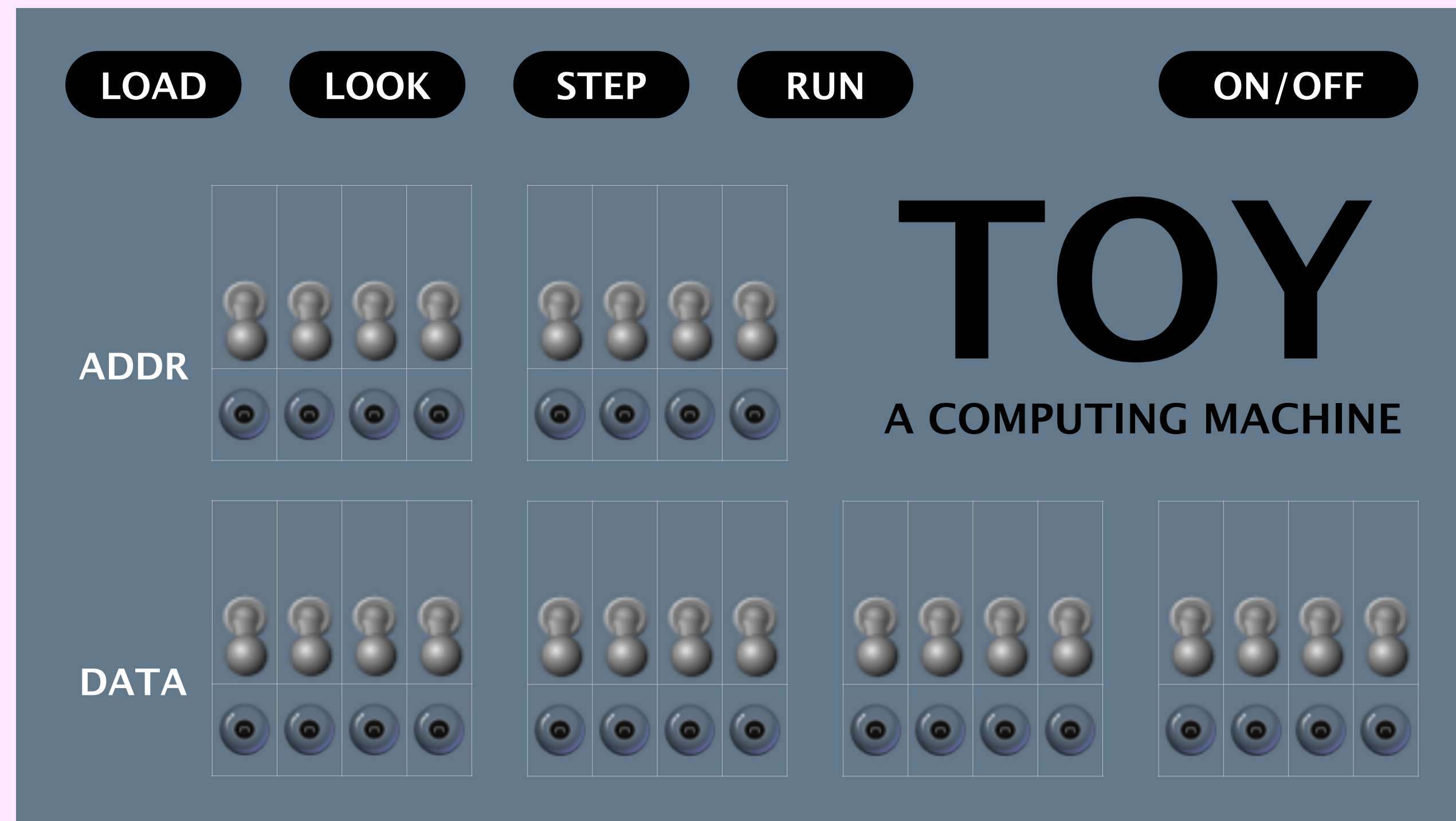
- Set the 8 memory address switches.
- Set the 16 data switches.
- Press LOAD.





To view the data in memory:

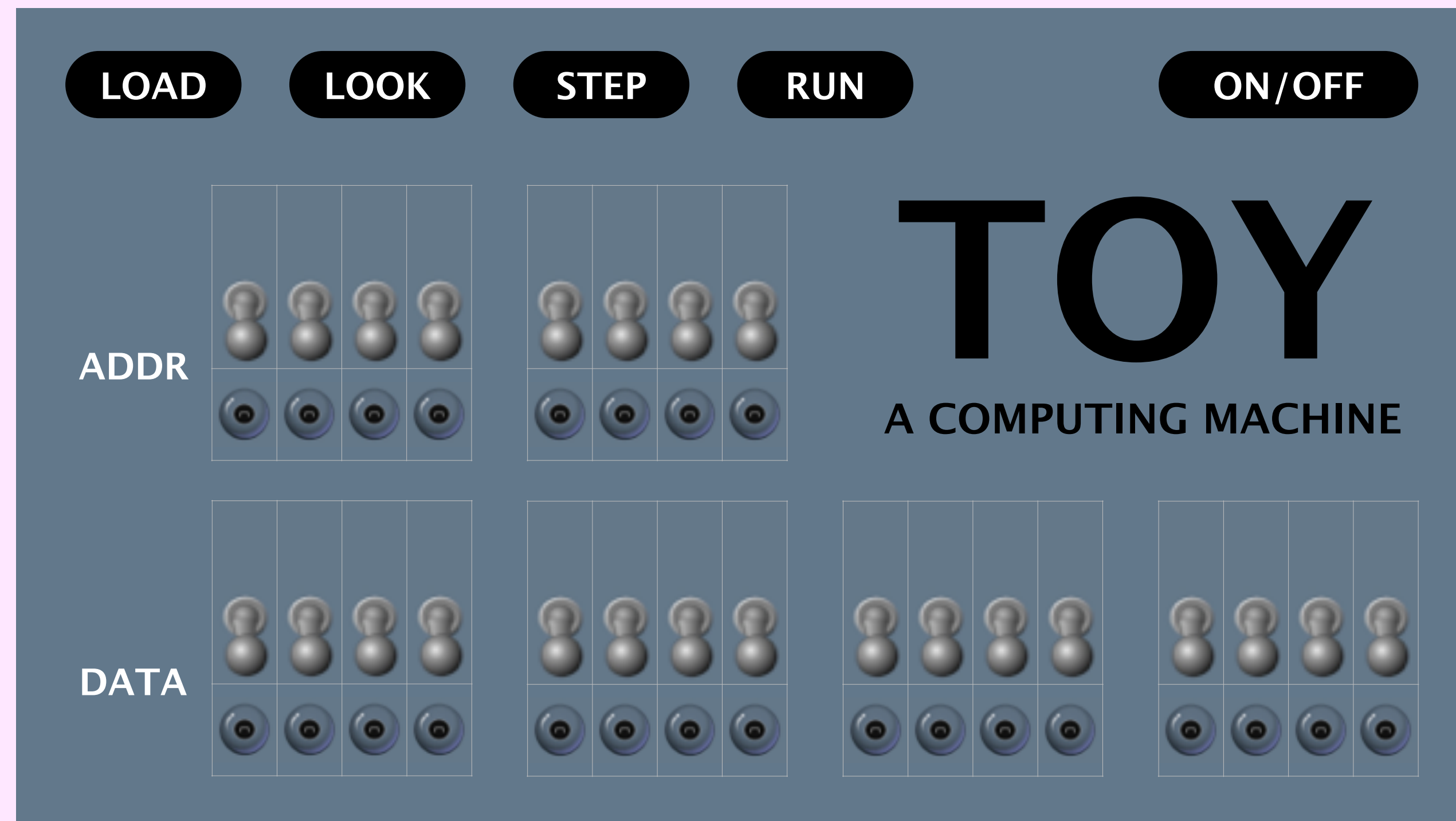
- Set the 8 address switches.
- Press LOOK.





To run a program:

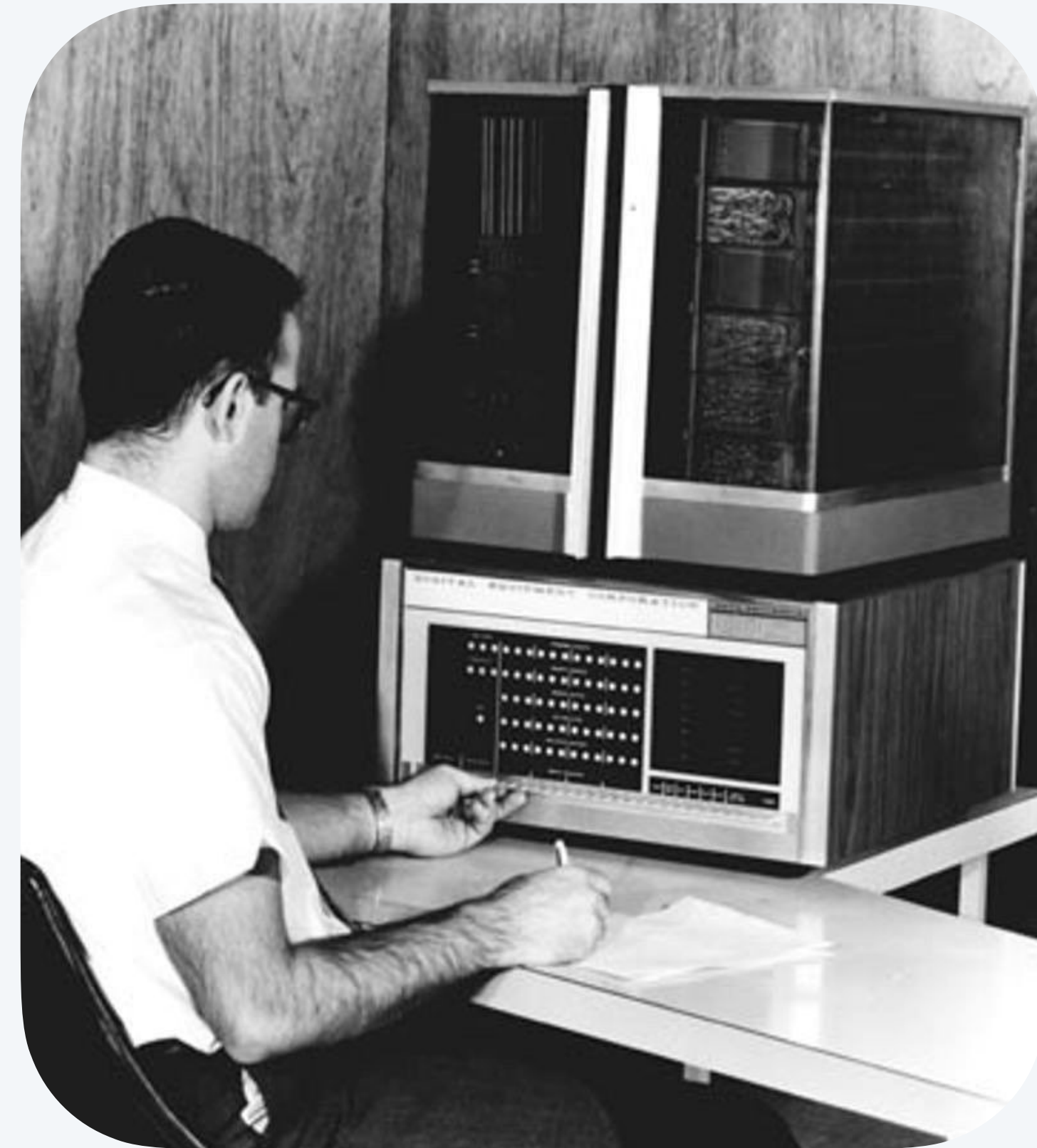
- Set the 8 address switches to the address of first instruction.
- Press RUN.



Switches and lights

Q. Did people really program this way?

A. Yes! We have it good.



DEC PDP-8 (1964)

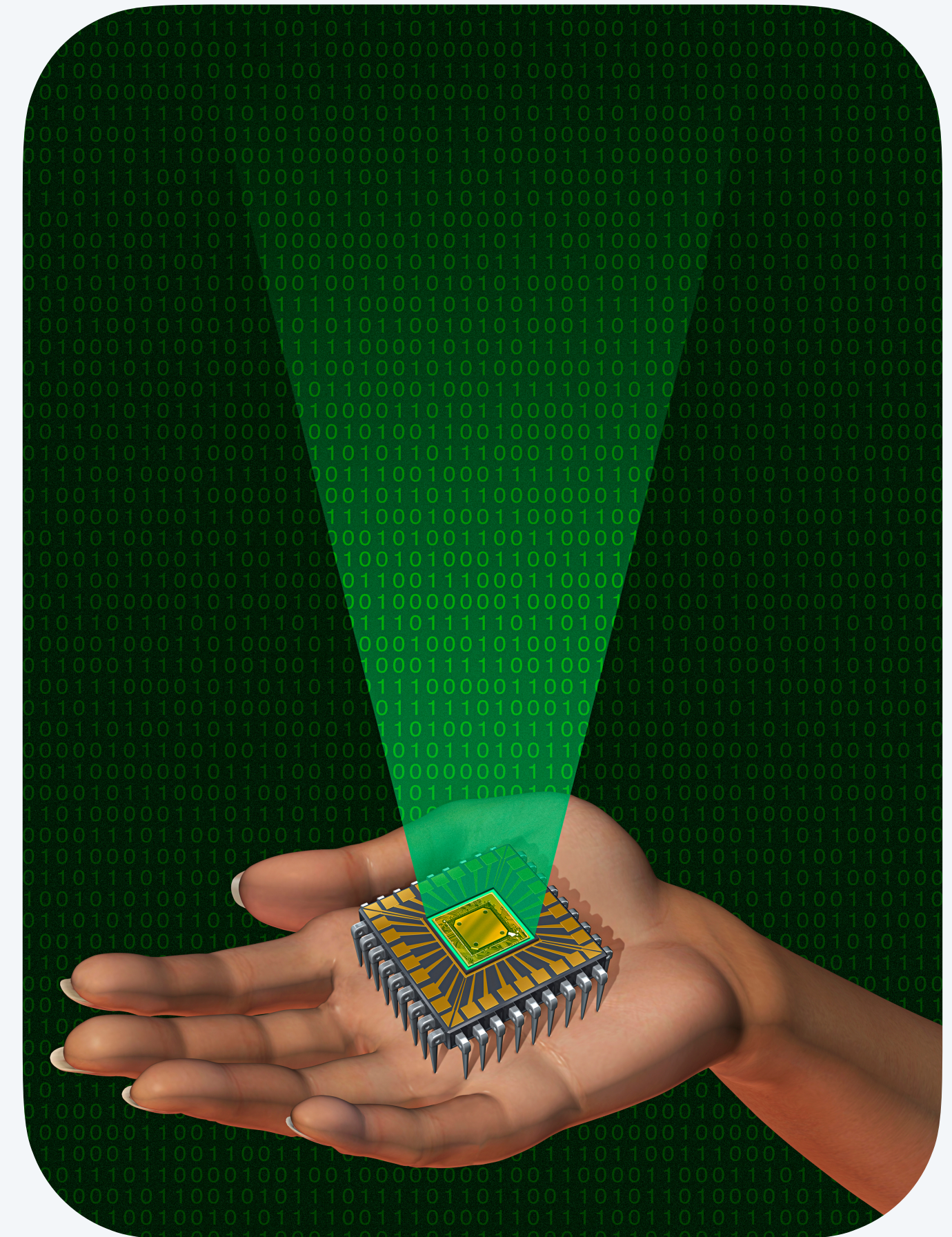
TOY summary

TOY machine has same basic architecture as modern CPUs:

- Arithmetic logic unit (ALU).
- Memory and registers.
- Program counter (PC) and instruction register (IR).
- Input and output.

TOY supports same basic programming constructs as Java:

- Primitive data types.
 - Arithmetic/logic operations.
 - Conditionals and loops.
 - Input and output.
 - Arrays.
 - Functions.
 - Linked structures.
- ← *next lecture*
- ← *see textbook*



TOY reference sheet

opcode	operation	format	pseudo-code
0	<i>halt</i>	—	halt
1	<i>add</i>	RR	$R[d] = R[s] + R[t]$
2	<i>subtract</i>	RR	$R[d] = R[s] - R[t]$
3	<i>bitwise and</i>	RR	$R[d] = R[s] \& R[t]$
4	<i>bitwise xor</i>	RR	$R[d] = R[s] \wedge R[t]$
5	<i>shift left</i>	RR	$R[d] = R[s] \ll R[t]$
6	<i>shift right</i>	RR	$R[d] = R[s] \gg R[t]$
7	<i>load address</i>	A	$R[d] = \text{addr}$
8	<i>load</i>	A	$R[d] = M[\text{addr}]$
9	<i>store</i>	A	$M[\text{addr}] = R[d]$
A	<i>load indirect</i>	RR	$R[d] = M[R[t]]$
B	<i>store indirect</i>	RR	$M[R[t]] = R[d]$
C	<i>branch zero</i>	A	if ($R[d] == 0$) PC = addr
D	<i>branch positive</i>	A	if ($R[d] > 0$) PC = addr
E	<i>jump register</i>	RR	PC = R[d]
F	<i>jump and link</i>	A	$R[d] = PC + 1$; PC = addr

format RR



format A



zero $R[0]$ is always 0000.

standard input Load from M[FF].

standard output Store to M[FF].

Credits

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