

## Procedure Call Instructions

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- Procedure calls involve the following actions
  1. passing arguments
  2. saving a “return address”
  3. transferring from the *caller* to the *callee*
  4. returning from the callee to the caller
  5. returning the results
- Simplest examples include assembly-language “leaf” procedures, like the arithmetic intrinsics `.mul`, etc.

```
a = b*c;
```

```
ld b,%o0
ld c,%o1
call .mul
nop
st %o0,a
```

optimized

```
ld b,%o0
call .mul
ld c,%o1
st %o0,a
```

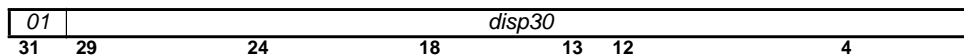
## Call/Return Instructions

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- Procedures are called with either `call` or `jmp1`
- `call` instruction

```
call    label
```

a format 1 instruction



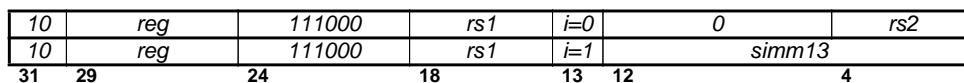
jumps to  $PC + 4 \times \text{zeroextend}(\text{disp30})$

leaves  $PC$ , i.e. the location of the `call`, in `%o7 (%r15)`

- `jmp1` instruction

```
jmp1    address, reg
```

format 3 instruction



jumps to 32-bit address by *address*, which may be any addressing mode

leaves PC in *reg*

## Indirect Calls

- `jmp1` implements *indirect calls*

```
jmp1    reg, %r15
```

jumps to the 32-bit address specified in *reg*

leaves *PC* — the return address — in `%r15`

e.g., for function pointers

```
a = (*apply)(b, c);
```

```
ld b,%o0
ld c,%o1
ld apply,%o3
jmp1 %o3,%r15; nop
st %o0,a
```

- `jmp1` implements procedure return

```
jmp1 %r15+8,%g0
```

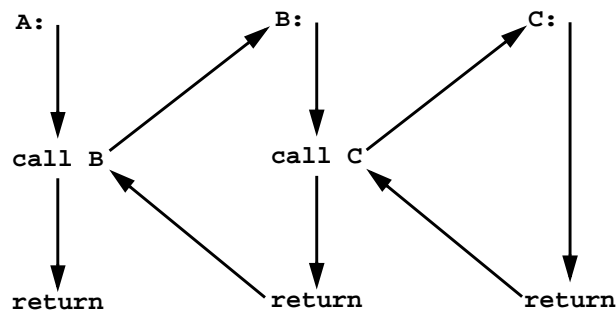
transfers control from the callee to the caller (see also `ret` and `ret1`)

why `+8`?

## Procedure Calls

- Procedure implementation must handle *nested* and *recursive* calls

e.g., A calls B, B calls C



must work when, e.g., B *is* A, etc.

- Other requirements

passing a variable number of arguments  
 passing and returning structures  
 allocating and deallocating space for locals  
 saving and restoring caller's registers

- *Entry* and *exit* sequences collaborate to implement these requirements

## Stack

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- Procedure call information is stored in the stack
  - locals, including compiler “temporaries”
  - caller’s registers, if necessary
  - callee’s arguments, if necessary
- SPARC’s stack grows downwards, i.e. from high to low addresses
- The stack pointer, `%sp` (`%r14`) points to the top 32-bit word on the stack
  - `%sp` **must** always be a multiple of 8
- Stack operations
  - to push `%o1`

```
dec 4,%sp
st %o1,[%sp]
```
  - to pop top word into `%o1`

```
ld [%sp],%o1
inc 4,%sp
```
  - to allocate  $N$  bytes of stack space

```
sub %sp,N,%sp
```

## Arguments and Return Values

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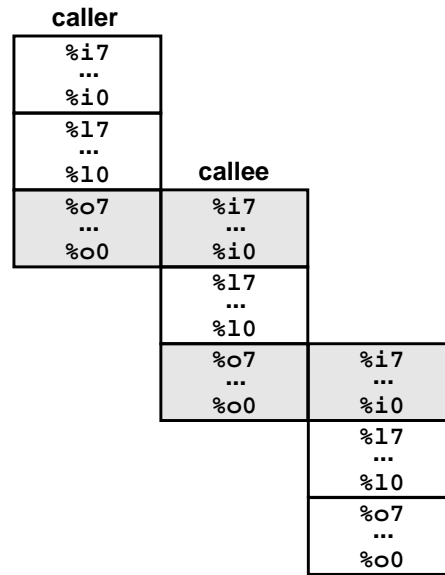
- By convention,
  - the first 6 arguments are passed in registers; the rest are passed on the stack (97% of procedures have 6 or fewer arguments)
- Caller places the arguments in the “out” registers; callee finds its arguments in the “in” registers

<i>caller</i>	<i>what</i>	<i>callee</i>	
<code>%o7</code>	return address - 8	<code>%i7</code>	
<code>%o6</code>	<u>stack</u> pointer	<code>%i6</code>	<u>frame</u> pointer
<code>%o5</code>	sixth argument	<code>%i5</code>	
...	...	...	
<code>%o1</code>	second argument	<code>%i1</code>	
<code>%o0</code>	first argument	<code>%i0</code>	

- Callee places its return value in the “in” registers; caller finds the return value in the “out” registers
- | <i>caller</i>    | <i>what</i>         | <i>callee</i>    |
|------------------|---------------------|------------------|
| <code>%o5</code> | sixth return value  | <code>%i5</code> |
| ...              | ...                 | ...              |
| <code>%o1</code> | second return value | <code>%i1</code> |
| <code>%o0</code> | first return value  | <code>%i0</code> |

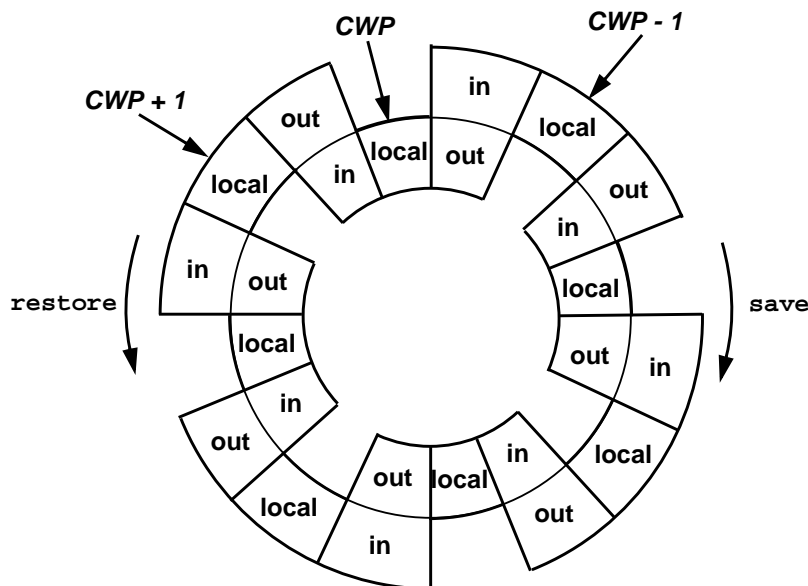
## Register Windows

- SPARC register windows: each procedure gets 16 “new” registers
- The window “slides” at a call  
 callee's in registers become synonymous with the caller's out registers
- The SPARCs have 2–32 windows
- `save` slides the window “forward”
- `restore` slides the window “backwards”



## Register Windows, cont'd

- Most SPARCs have 8 windows



- `save/restore` decrement/increment the current window pointer, *CWP*

## Window Management

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- **save** instruction

`save %sp, N, %sp`      e.g., `save %sp, -4*16, %sp`

slides the register window so the current window becomes the previous window

decrements the current window pointer (**CWP**) and checks for window **overflow**

adds **N** to the stack pointer, `%sp`; i.e., allocates **N** bytes if  $N < 0$

- If an overflow occurs, the registers are saved on the stack

there **must** be enough stack space

- **restore** instruction

slides the register window so the previous window becomes the current window

increments the current window pointer (**CWP**) and checks for window **underflow**

- In **save** and **restore**

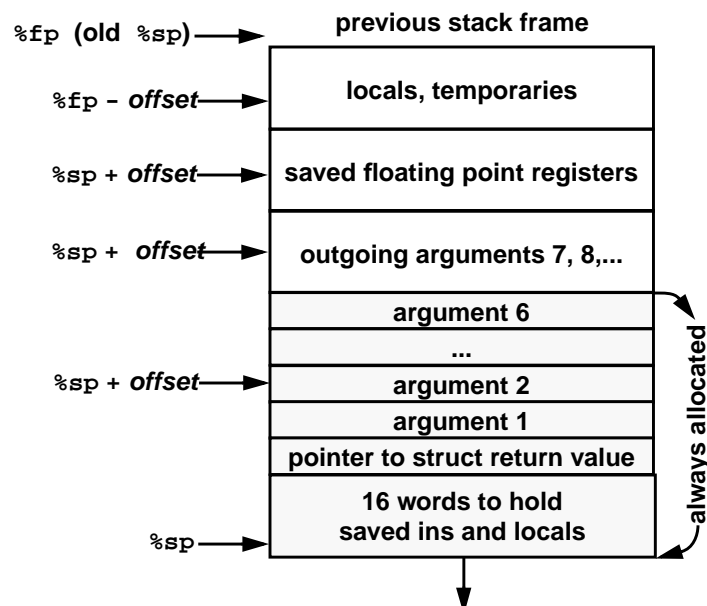
**source** registers refer to the **current** window

**destination** registers refer to the **new** window

## Stack Frame

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- see §7.5 in Paul



## C Calling Convention

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- First 6 arguments are passed in %00 — %05, the rest in the stack

```
char out[30], str[] = "this is a sample string";
main() { bcopy(out, str, sizeof str); }
bcopy(char *dst, char *src, int nbytes) { ... }
```

- Assembly language

```
.seg "bss"
.global _out
.common _out,30
.seg "data"
.global _str
_str:.ascii "this is a sample string\000"
.seg "text"
.global _main
_main:save %sp,-96,%sp
    set _out,%00
    set _str,%01
    call _bcopy
    set 24,%02
    ret; restore
.global _bcopy
_bcopy:...
    retl; nop
```

## Example Stack Frames

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main() {	_main:save %sp,-104,%sp
t(1,2,3,4,5,6,7,8);	set 1,%00
}	set 2,%01
	set 3,%02
	set 4,%03
	set 5,%04
	set 6,%05
	<b>set 7,%i5</b>
	<b>st %i5,[%sp+4*6+68]</b>
	<b>set 8,%i5</b>
	<b>st %i5,[%sp+4*7+68]</b>
	call _t; nop
	ret; restore
t(int a1, int a2,	_t: save %sp,-96,%sp
int a3, int a4,	<b>st %i0,[%fp-4]</b>
int a5, int a6,	ld [%fp-4],%00
int a7, int a8) {	<b>ld [%fp+96],%01</b>
int b1 = a1;	call _s; nop
return s(b1, a8);	mov %00,%i0
}	ret; restore
s(int c1, int c2) {	_s:
return c1 + c2;	add %00,%01,%00
}	retl; nop

## Example Stack Frames, cont'd

