

<b>GRIGORY FEDYUKOVICH</b>	
<b>Current Position</b>	<b>Postdoctoral Research Associate</b> in the Computer Science Department at Princeton University
<b>Research interests</b>	<p><b>Formal Software Verification:</b></p> <ul style="list-style-type: none"> <li>• constrained Horn clauses, inductive invariant synthesis;</li> <li>• probabilistic, SMT-based, and syntax-guided techniques for proving safety, termination, and non-termination of programs;</li> <li>• security verification</li> </ul> <p><b>Automated Regression / Incremental Verification:</b></p> <ul style="list-style-type: none"> <li>• proving equivalence and simulation between programs;</li> <li>• function summarization based on Craig Interpolation</li> </ul> <p><b>Program Synthesis:</b></p> <ul style="list-style-type: none"> <li>• automatic parallelization;</li> <li>• synthesis from Skolemized proofs of realizability;</li> <li>• synthesis of secure programs</li> </ul>
<b>Email / Web</b>	<a href="mailto:grigoryf@cs.princeton.edu">grigoryf@cs.princeton.edu</a> <span style="margin-left: 100px;"><a href="http://www.cs.princeton.edu/~grigoryf">http://www.cs.princeton.edu/~grigoryf</a></span>

<b>AFFILIATION HISTORY</b>	
Oct 2017 - ...	<p><b>PostDoc:</b> Computer Science Department at Princeton University;</p> <p><b>Supervised by:</b> Prof. Aarti Gupta;</p> <p><b>Projects:</b> Horn-based Symbolic Model Checking, Verification for Security</p>
Jan 2016 - Oct 2017	<p><b>PostDoc:</b> Paul G. Allen School of Computer Science &amp; Engineering at University of Washington (UW), Seattle, USA;</p> <p><b>Supervised by:</b> Prof. Rastislav Bodík;</p> <p><b>Projects:</b> Automatic Parallelization; Horn-based Symbolic Model Checking, Probabilistic SyGuS-based Invariant Synthesis</p>
Oct 2010 - Dec 2015	<p><b>PhD:</b> Faculty of Informatics, Università della Svizzera italiana (USI), Lugano, Switzerland;</p> <p><b>Supervised by:</b> Prof. Natasha Sharygina;</p> <p><b>Thesis:</b> Automated Incremental Software Verification</p>
Apr - Oct 2010	<p><b>Internship:</b> School of Computing, National University of Singapore, Singapore;</p> <p><b>Project:</b> Verification of quantified list properties in Coq</p>
Jun 2009 - March 2010	<p><b>Internship:</b> Logic and Semantics group at Tallinn Institute of Cybernetics, Estonia;</p> <p><b>Project:</b> A Coq formalization of an analysis and optimization of While</p>
2007 - 2009	<b>Software Engineer (Java EE):</b> Reksoft, Saint Petersburg, Russia
2003 - 2008	<b>Specialist Degree (Diplom):</b> Department of Computer Science, Faculty of Mathematics and Mechanics, Saint-Petersburg State University, Russia

<b>COLLABORATION</b>	
2015 - present	<b>Topic:</b> Synthesis from Skolemized Proofs of Realizability; <b>JSyn</b> tool <b>Collaboration</b> with: Dr. Michael W. Whalen, University of Minnesota, USA
2016 - present	<b>Topic:</b> SMT-based Incremental Bounded Model Checking; <b>HiFrog</b> tool <b>Collaboration</b> with: Dr. Hana Chockler, King's College, London, UK and Prof. Natasha Sharygina, USI, Switzerland
2012 - 2016	<b>Topic:</b> Horn-based Incremental Model Checking; <b>Niagara</b> tool <b>Collaboration</b> with: Prof. Arie Gurfinkel, University of Waterloo, Canada
2014 - 2015	<b>Topic:</b> Partial Interpolation Framework; <b>PVAIR</b> tool <b>Collaboration</b> with: Prof. Jan Kofroň, Charles University, Czech Republic
2010 - 2013	EU project <b>PINCETTE</b> : number 257647, supported by European Community under the call FP7-ICT-2009-5; <b>Topic:</b> Validating Changes and Upgrades in Networked Software; <b>Collaboration</b> with: University of Oxford, IBM Israel, University of Milano Bicocca, VTT Finland, Israel Aerospace Industries Ltd., ABB Schweiz, and ABB Germany

<b>GRANTS AND FELLOWSHIPS</b>	
2018	<b>University of Tokyo - Princeton Strategic Partnership Grant</b> , 10 000 USD to support a new research collaboration that will facilitate the flow of scholars and graduate students between institutions
2016	<b>Postdoc Award</b> , University of Washington, 23 000 USD for a side-project on Horn-based Symbolic Model Checking (salaries for interns, conference travel, equipment)
2015	<b>Early Postdoc.Mobility Fellowship</b> , Swiss National Science Foundation, ~100 000 USD for 18 months in University of Washington (salaries for myself, medical insurance, equipment, conference travel)

<b>SERVICE</b>	
2018	<b>Program Chair:</b> Workshop on Verification and Synthesis for Software Evolution (VSSE 2018) <b>Program Committee Member:</b> International Conference on Computer Aided Verification (CAV 2018), The 16th International Symposium on Automated Technology for Verification and Analysis (ATVA 2018), ACM Transactions on Programming Languages and Systems (TOPLAS) <b>Artifact Evaluation Committee Member:</b> International Conference on Computer Aided Verification (CAV 2018) <b>Organization Committee Member:</b> 1st International Competition on Satisfiability of Constrained Horn Clauses (CHC-COMP 2018) <b>Sub-reviewer:</b> International Conference on Formal Methods in Computer-Aided Design (FMCAD 2018), 25th International Symposium on Model Checking of Software (SPIN 2018), International Conference on Computer Aided Verification (CAV 2018)

2017	<p><b>Local Organization Chair:</b> The 17th International Conference on Runtime Verification (RV 2017)</p> <p><b>Program Committee Member:</b> The 17th International Conference on Runtime Verification (RV 2017), the 4th Workshop on Horn Clauses for Verification and Synthesis (HCVS 2017)</p> <p><b>Sub-reviewer:</b> International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2018), The 13th Haifa Verification Conference (HVC 2017), International Conference on Formal Methods in Computer-Aided Design (FMCAD 2017), the 24th Static Analysis Symposium (SAS 2017), International Conference on Software Engineering and Formal Methods (SEFM 2017), International Conference on Computer Aided Verification (CAV 2017)</p>
2016	<p><b>Program Chair:</b> Workshop on Verification and Synthesis for Software Evolution (VSSE 2016)</p> <p><b>Program Committee Member:</b> International Journal on Software Tools for Technology Transfer (STTT, Selected Papers at TACAS 2016), Journal of Automated Reasoning (JAR, Selected Papers at VSTTE 2016)</p> <p><b>Sub-reviewer:</b> International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2017), International Symposium on Formal Methods (FM 2016), International Conference on Formal Methods in Computer-Aided Design (FMCAD 2016), Conference on Verified Software: Theories, Tools, and Experiments (VSTTE 2016), International Conference on Computer Aided Verification (CAV 2016), 3rd Workshop on Horn Clauses for Verification and Synthesis (HCVS 2016)</p>
2015	<p><b>Sub-reviewer:</b> International Conference on Formal Methods in Computer-Aided Design (FMCAD 2015), Journal of Automated Reasoning (JAR, Special Issue on Interpolation Techniques for Program Verification and Synthesis), International Conference on Computer Aided Verification (CAV 2015), International Symposium on Formal Methods (FM 2015), NASA Formal Methods Symposium (NFM 2015)</p>
2014	<p><b>Sub-reviewer:</b> International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2015), International Conference on Formal Methods in Computer-Aided Design (FMCAD 2014), Ershov Informatics Conference (PSI 2014), International Conference on Computer Aided Verification (CAV 2014)</p> <p><b>Organization Committee Member:</b> Workshop on Validation Strategies for Software Evolution (VSSE 2014)</p>
2013	<p><b>Sub-reviewer:</b> International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2014), International Conference on Formal Methods in Computer-Aided Design (FMCAD 2013), Workshop on Validation Strategies for Software Evolution (VSSE 2013)</p> <p><b>Organization Committee Member:</b> International Conference on Computer Aided Verification (CAV 2013)</p>
2012	<p><b>Sub-reviewer:</b> International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2013), International Conference on Formal Methods in Computer-Aided Design (FMCAD 2012), Conference on Design, Automation and Test in Europe (DATE 2012), International Symposium on Games, Automata, Logics and Formal Verification (GandALF 2012), Working Conference on Verified Software: Theories, Tools, and Experiments (VSTTE 2012), International Conference on Computer Aided Verification (CAV 2012)</p>
2011	<p><b>Sub-reviewer:</b> International Conference on Formal Methods in Computer-Aided Design (FMCAD 2011), International Conference on Formal Methods and Models for System Design (MEMOCODE 2011)</p>

2010	<p><b>Sub-reviewer:</b> International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2011)</p> <p><b>Organization Committee Member:</b> International Conference on Formal Methods in Computer-Aided Design (FMCAD 2010), Alpine Verification Meeting (AVM 2010)</p>
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<b>SOFTWARE</b>	
2017 - present	<b>FreqHorn / FreqTerm</b> , Probabilistic SyGuS-based SMT-based Invariant Synthesizer and a Termination / Non-termination prover
2014 - present	<b>AE-VAL</b> , Solver of Forall-Exists-formulas in linear arithmetic and extractor of Skolem functions (core engine in Niagara and JSyn)
2016 - 2017	<b>Rosette/Unbound</b> , Constrained-Horn-Clause-based Invariant Synthesizer for functional programs in Racket
2013 - 2016	<b>Niagara</b> , Constrained-Horn-Clause-based incremental model checker for C
2010 - 2017	<b>HiFrog / FunFrog / eVolCheck</b> , incremental SAT/SMT-based bounded model checker for C with function summarization, automated detection of recursion depth, checking assertion dependencies, support for flexible interpolation, and upgrade checking capabilities

<b>TALKS AND SEMINARS</b>	
2018 (TBA)	Conference talk <b>Syntax-Guided Termination Analysis</b> . CAV, Oxford, UK
2018	Invited seminar <b>Sampling Invariants from Frequency Distributions</b> , Saint Petersburg State University, Russia. Host: Dmitry Mordvinov
2018	Invited seminar <b>Sampling Invariants from Frequency Distributions</b> , New York University, USA. Host: Prof. Thomas Wies
2018	Invited seminar <b>Sampling Invariants from Frequency Distributions</b> , Yale University, USA. Host: Prof. Ruzica Piskac
2018	Conference talk <b>Accelerating Syntax-Guided Invariant Synthesis</b> . TACAS, Thessaloniki, Greece
2018	Invited talk <b>Property Directed Equivalence via Abstract Simulation</b> , Dagstuhl Seminar 18151 on Program Equivalence, Dagstuhl, Germany
2017	Invited seminar <b>Synchronizing Constrained Horn Clauses</b> , USI, Switzerland. Host: Prof. Natasha Sharygina

2017	Invited seminar <b>Sampling Invariants from Frequency Distributions</b> , Charles University, Prague, Czech Republic. Host: Prof. Jan Kofroň
2017	Conference talk <b>Sampling Invariants from Frequency Distributions</b> , FMCAD, Vienna, Austria
2017	Invited seminar <b>Synchronizing Constrained Horn Clauses</b> , Macquarie University, Sydney, Australia. Host: Prof. Franck Cassez
2017	Invited seminar <b>Synchronizing Constrained Horn Clauses</b> , SRI International, Menlo Park, USA. Host: Dr. Jorge Navas
2017	Invited seminar <b>Synchronizing Constrained Horn Clauses</b> , KTH, Stockholm, Sweden. Host: Prof. Philipp Haller
2017	Invited seminar <b>Synchronizing Constrained Horn Clauses</b> , IMDEA Software Institute, Madrid, Spain. Host: Dr. Pedro López García
2017	Conference talk <b>Gradual Synthesis for Static Parallelization of Single-Pass Array-Processing Programs</b> , PLDI, Barcelona, Spain
2017	Invited seminar <b>Synchronizing Constrained Horn Clauses</b> , University of California San Diego, USA. Host: Prof. Ranjit Jhala
2017	Conference talk <b>Synchronizing Constrained Horn Clauses</b> , LPAR, Maun, Botswana
2017	Invited seminar <b>Automated Incremental Software Verification</b> , Tsinghua University, Beijing, China. Host: Prof. Fei He
2017	Invited seminar <b>SMT-based Function Summarization for Incremental Software Verification</b> , University of Massachusetts Lowell, USA. Host: Prof. Jay McCarthy
2017	Invited seminar <b>SMT-based Function Summarization for Incremental Software Verification</b> , USI, Switzerland. Host: Prof. Natasha Sharygina
2016	Invited seminar <b>Automated Incremental Software Verification</b> , Seoul National University, Seoul, Korea. Host: Prof. Chung-Kil Hur
2016	Invited seminar <b>Automated Incremental Software Verification</b> , Hong Kong University of Science and Technology, Hong Kong. Host: Prof. S.C. Cheung
2016	Invited seminar <b>Witnessing Existential Quantifiers with AE-VAL</b> , USI, Switzerland. Host: Prof. Natasha Sharygina
2016	Workshop talk <b>Gradual Synthesis for Static Parallelization</b> . PLSE Retreat, Leavenworth, USA

2016	Workshop talk <b>What's Reusable in Program Analysis</b> . TAPAS, Edinburgh, UK
2016	Conference talk <b>Property Directed Equivalence via Abstract Simulation</b> , CAV, Toronto, Canada
2016	Workshop talk <b>Approaching Symbolic Parallelization by Synthesis of Recurrence Decompositions</b> . SYNT, Toronto, Canada
2016	Invited seminar <b>Property Directed Equivalence via Abstract Simulation</b> , USI, Switzerland. Host: Prof. Natasha Sharygina
2016	Invited seminar <b>Automated Incremental Software Verification</b> , UW, Seattle, USA
2015	Conference talk <b>Automated Discovery of Simulation Between Programs</b> , LPAR, Suva, Fiji
2015	Workshop talk <b>AE-VAL: Horn clause-based Skolemizer for Forall-Exists-formulas</b> , HCVS, San Francisco, USA
2015	Workshop talk <b>Incremental Proof-Based Verification of Compiler Optimizations</b> , AVM, Attersee, Austria
2015	Conference talk <b>Symbolic Detection of Assertion Dependencies for Bounded Model Checking</b> , FASE, London, UK
2014	Conference talk <b>Towards Completeness in Bounded Model Checking Through Automatic Recursion Depth Detection</b> , SBMF, Maceio, Brazil
2014	Conference talk <b>Incremental Verification of Compiler Optimizations</b> , NASA Formal Methods, Houston, USA
2014	Invited talk <b>Producing Effective Interpolants for SAT-based Incremental Verification and Upgrade Checking</b> , VSSE, Grenoble, France
2013	Conference talk <b>PeRIPLO: A Framework for Producing Effective Interpolants in SAT-based Software Verification Hardware Verification</b> , LPAR, Stellenbosch, South Africa
2013	Workshop talk <b>Incremental Upgrade Checking by means of Interpolation-based Function Summaries</b> , VPT, Saint Petersburg, Russia
2013	Invited seminar <b>Interpolation-based Model Checking for Efficient Incremental Analysis of Software</b> at SEI/CMU, Pittsburgh, USA. Host: Dr. Arie Gurfinkel
2013	Conference talk <b>eVolCheck: Incremental Upgrade Checker for C</b> , TACAS, Rome, Italy

2013	Invited talk <b>Incremental Model Checking for Upgrade Checks</b> , VSSE, Rome, Italy
2013	Invited talk and demo <b>Upgrade Checking in eVolCheck</b> , ABB Schweiz, Baden, Switzerland. Host: Dr. Manuel Oriol
2012	Conference talk <b>FunFrog: Bounded Model Checking with Interpolation-based Function Summarization</b> , ATVA, Trivandrum, India
2012	Workshop talk <b>Interpolation-based Function Summaries in Bounded Model Checking</b> , LfSA, Berkeley, USA
2012	Invited talk <b>Software Model Checking</b> , IMT, Irkutsk, Russia
2012	Workshop talk <b>Bounded Model Checking with Interpolation-based Function Summarization</b> , AVM, Passau, Germany
2011	Poster session <b>Function Summaries in Software Upgrade Checking</b> , HVC, Haifa, Israel
2011	Tutorials <b>FunFrog: Bounded Model Checking with Interpolation-based Function Summarization</b> , VTT and Nokia, Tampere, Finland. Host: Dr. Ali Muhammad
2010	Seminar <b>Of the verification of list properties</b> , NUS, Singapore
2010	Workshop talk <b>A Coq formalization of an analysis and optimization of While</b> , Estonian Computer Science Theory Days, Andu, Estonia

<b>TEACHING</b>	
2018	External Review Committee of Bachelor Theses (by Lidiia Chernigovskaia and Aleksandr Misonizhnik), Saint-Petersburg State University, Russia
2017	Invited lecture <b>Invariant Generation</b> at the <b>Automated Reasoning about Software</b> course, Princeton University, USA
2017	Student projects on <b>Equivalence Checking, Incremental BMC, and Invariant Generation</b> at the <b>Automated Reasoning about Software</b> course, Princeton University, USA
2016	Invited lecture <b>SMT solving in Software Verification</b> at the <b>Logic</b> course, USI, Switzerland
2015	Teaching assistantship at the <b>Computer Aided Verification</b> course, USI, Switzerland

2014	Lab <b>Spin Model Checker</b> at the <b>Validation and Verification</b> course, ALaRI, USI, Switzerland
2012	Teaching assistantship at the <b>Computer Architecture</b> course, USI, Switzerland
2012	Teaching assistantship at the <b>Automata and Formal Languages</b> course, USI, Switzerland
2011	Teaching assistantship at the <b>Linear Algebra</b> course, USI, Switzerland

<b>SUMMER SCHOOLS</b>	
2015	2nd Summer School on Software Synthesis, MIT, Cambridge, USA
2013	3rd Summer School on <b>Formal Techniques</b> , Menlo College, Atherton, CA, USA
2011	1st International <b>SAT/SMT</b> Summer School, MIT, Cambridge, USA
2010	40th International Summer <b>School Software and Systems Safety: Specification and Verification</b> , Marktoberdorf, Germany <b>DAAD scholarship granted</b>
2010	3rd International School on <b>Trends in Concurrency</b> , Bangalore, India <b>student grant awarded</b>
2010	15th Estonian Winter School in Computer Science, Palmse, Estonia
2009	2nd International Summer School <b>Verification Technology, Systems &amp; Applications</b> , Nancy, France
2009	1st Microsoft Research / HPC Summer School, Moscow State University, Moscow, Russia <b>student grant awarded</b>
2009	14th Estonian Winter School in Computer Science, Palmse, Estonia <b>student grant awarded</b>
2008	38th International Summer <b>School Engineering Methods and Tools for Software Safety and Security</b> , Marktoberdorf, Germany <b>DAAD scholarship awarded</b>

<b>JOURNAL PUBLICATIONS AND BOOK CHAPTERS</b>	
2017	Grigory Fedyukovich, Ondrej Sery, Natasha Sharygina: <b>Flexible Framework for Incremental Upgrade Checking.</b> STTT, 19(5): 517-534, <a href="http://dx.doi.org/10.1007/s10009-015-0405-y">http://dx.doi.org/10.1007/s10009-015-0405-y</a>
2015	Hana Chockler, Daniel Kroening, Leonardo Mariani, Natasha Sharygina (editors) <b>Validation of Evolving Software.</b> (4 chapters) <a href="http://dx.doi.org/10.1007/978-3-319-10623-6">http://dx.doi.org/10.1007/978-3-319-10623-6</a>

<b>CONFERENCE PUBLICATIONS</b>	
2018	Grigory Fedyukovich, Yueling Zhang, Aarti Gupta: <b>Syntax-Guided Termination Analysis.</b> CAV, to appear
2018	Lauren Pick, Grigory Fedyukovich, Aarti Gupta: <b>Exploiting Synchrony and Symmetry in Relational Verification.</b> CAV, to appear
2018	Grigory Fedyukovich, Rastislav Bodík: <b>Accelerating Syntax-Guided Invariant Synthesis.</b> TACAS (1): 251-269
2018	Andreas Katis, Grigory Fedyukovich, Huajun Guo, Andrew Gacek, John Backes, Arie Gurfinkel, Michael Whalen: <b>Validity-Guided Synthesis of Reactive Systems from Assume-Guarantee Contracts.</b> TACAS (2): 176-193
2017	Grigory Fedyukovich, Samuel Kaufman, Rastislav Bodík: <b>Sampling Invariants from Frequency Distributions.</b> FMCAD: 100-107, <i>invited for a Special Issue of the Formal Methods in Systems Design (FMSD) journal</i>
2017	Antti Eero Johannes Hyvärinen, Sepideh Asadi, Karine Even-Mendoza, Grigory Fedyukovich, Hana Chockler, Natasha Sharygina: <b>Theory Refinement for Program Verification.</b> SAT: 347-363
2017	Grigory Fedyukovich, Maaz Bin Safeer Ahmad, Rastislav Bodík: <b>Gradual Synthesis for Static Parallelization of Single-Pass Array-Processing Programs.</b> PLDI: 572-585
2017	Dmitry Mordvinov, Grigory Fedyukovich: <b>Synchronizing Constrained Horn Clauses.</b> LPAR: 338-355
2017	Leonardo Alt, Sepideh Asadi, Hana Chockler, Karine Even-Mendoza, Grigory Fedyukovich, Antti Eero Johannes Hyvärinen, Natasha Sharygina: <b>HiFrog: SMT-based Function Summarization for Software Verification.</b> TACAS (2): 207-213
2016	Grigory Fedyukovich, Rastislav Bodík: <b>Approaching Symbolic Parallelization by Synthesis of Recurrence Decompositions.</b> SYNT@CAV: 55-66
2016	Grigory Fedyukovich, Arie Gurfinkel, Natasha Sharygina: <b>Property Directed Equivalence via Abstract Simulation.</b> CAV (2): 433-453

2016	Pavel Jancík, Leonardo Alt, Grigory Fedyukovich, Antti Eero Johannes Hyvärinen, Jan Kofroň, Natasha Sharygina: <b>PVAIR: Partial Variable Assignment InterpolatoR</b> . FASE: 419-434
2015	Grigory Fedyukovich, Arie Gurfinkel, Natasha Sharygina: <b>Automated Discovery of Simulation Between Programs</b> . LPAR: 606-621
2015	Leonardo Alt, Grigory Fedyukovich, Antti Eero Johannes Hyvärinen, Natasha Sharygina: <b>A Proof-Sensitive Approach for Small Propositional Interpolants</b> . VSTTE: 1-18
2015	Grigory Fedyukovich, Andrea Callia D'Iddio, Antti Eero Johannes Hyvärinen, Natasha Sharygina: <b>Symbolic Detection of Assertion Dependencies for Bounded Model Checking</b> . FASE: 186-201 student grant awarded
2014	Grigory Fedyukovich, Natasha Sharygina: <b>Towards Completeness in Bounded Model Checking Through Automatic Recursion Depth Detection</b> . SBF: 96-112
2014	Fabrizio Pastore, Leonardo Mariani, Antti Eero Johannes Hyvärinen, Grigory Fedyukovich, Natasha Sharygina, Stephan Sehestedt, Ali Muhammad: <b>Verification-aided Regression Testing</b> . ISSTA: 37-48
2014	Grigory Fedyukovich, Arie Gurfinkel, Natasha Sharygina: <b>Incremental Verification of Compiler Optimizations</b> . NFM: 300-306
2013	Simone Fulvio Rollini, Leonardo Alt, Grigory Fedyukovich, Antti Eero Johannes Hyvärinen, Natasha Sharygina: <b>PeRIPLO: A Framework for Producing Effective Interpolants in SAT-based Software Verification</b> . LPAR: 683-693
2013	Grigory Fedyukovich, Ondrej Sery, Natasha Sharygina: <b>eVolCheck: Incremental Upgrade Checker for C</b> . TACAS: 292-307 best student contribution award, invited for a Special Issue of Software Tools for Technology Transfer (STTT) journal
2012	Ondrej Sery, Grigory Fedyukovich, Natasha Sharygina: <b>Incremental Upgrade Checking by Means of Interpolation-based Function Summaries</b> . FMCAD: 114-121
2012	Ondrej Sery, Grigory Fedyukovich, Natasha Sharygina: <b>FunFrog: Bounded Model Checking with Interpolation-based Function Summarization</b> . ATVA: 203-207
2011	Ondrej Sery, Grigory Fedyukovich, Natasha Sharygina: <b>Interpolation-Based Function Summaries in Bounded Model Checking</b> . HVC: 160-175 student grant awarded