Topic 1: Introduction

COS 320

Compiling Techniques

Princeton University
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The Usual Suspects

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What is a Compiler?

• A compiler is a program that takes a program written in a source language and translates it into a functionally equivalent program in a target language.

• Source Languages: C, C++, Swift, FORTRAN,

• Target Languages: x86 Assembly, Arm, Assembly, C,

• Compiler can also:
  • Report errors in source
  • Warn of potential problems in source
  • Optimize program
### What is a Compiler?

- **Source Program**
- **Lexical Analysis**
- **Syntax Analysis**
- **Semantic Analysis**
- **IR Code Generation**
- **Intermediate Representation**
- **IR Optimization**
- **Target Code Generation**
- **Target Code Optimization**
- **Target Program**

```c
for(i=0; i<20; i++) {
    printf("%d\n", i);
}
```

```assembly
i = 0
L6:
    CALL printf, "%d\n", i
    i = i + 1
    if(i < 20) GOTO L6

.LC0: stringz"%d\n"
    addl r37 = 0, r0
    addl r36 = @ltoff(.LC0), gp
.L6: br.call.sptk.many b0 = printf#
    adds r37 = 1, r37
    cmp4.ge p6, p7 = 19, r37
    (p6) br.cond.dptk .L6
```

### Why Learn About Compilers?

**Compiler technology everywhere.**

- C++ → Assembly
- Assembly → Machine Code
- Microcode → microcode binary
- Interpreters: Perl, Python, Java, ...
- JITs: Android Dalvik VM, Java VM, ...
- Publishing: Latex → PDF → Print on Paper
- Hardware Design: HW Description → Circuit/FPGA
- SPAM → /dev/null
- Automation: Water Fountain DL → Water Display
- Next Revolution in Processors

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**Almost all code goes through a compiler.**

**Linux**

- C = 2,558,100 lines
- x86 assembly = 12,164 lines

**99.5% of Linux source goes through a compiler!**

**Compilers teach us about:**

- Programming Languages
- Computer Architectures
Why Learn About Compilers?

- IBM developed the first FORTRAN compiler in 1957
- Took 18 person-years of effort
- You will be able to do it in less than a week!

Hardware Design

```verilog
module toplevel(clock, reset);
input clock;
input reset;
reg flop1;
reg flop2;
always @ (posedge reset or posedge clock)
if (reset)
begin
  flop1 <= 0;
  flop2 <= 1;
end
else
begin
  flop1 <= flop2;
  flop2 <= flop1;
end
endmodule
```
Princeton Research on Fault Tolerance wins CGO Test of Time Award

February 2, 2015

Every year, the International Symposium on Code Generation and Optimization (CGO) recognizes the paper appearing 10 years earlier that is judged to have had the most impact on the field over the intervening decade. This year at CGO 2015, the paper entitled “Software Implementation Fault Tolerance” by George A. Rais, Jonathan Chang, Neil Vachharajani, Ram Rangan, and David L. August won the award. The paper originally appeared at CGO 2005 and also won the best paper award that year at the conference. Congratulations to Princeton’s Liberty Research Group for winning this prestigious award!

Why Learn About Compilers?

Your chosen field of computer architecture effectively dead?

Why Learn About Compilers?
Why Take 320 Seriously?

Grading

<table>
<thead>
<tr>
<th>Assignments</th>
<th>50%</th>
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<tbody>
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<td>Exams</td>
<td>50%</td>
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<td>Quizzes</td>
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<td>Extra Credit</td>
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Exams

- Exams cover concepts presented in the lecture material, homework assignments, and/or required readings
- One double sided 8.5x11 page of notes allowed

Midterm Exam

- Thursday before break
- In class

Final Exam

- The final exam will be cumulative, three hours in length
- Time/Place determined by the Registrar
Pick a number 1,2,3

If the random number is the picture of a “processor”, then we have a quiz.

Quizzes

- Chance quiz at the beginning of each Tuesday class
- Not intended as a scare tactic – liberally graded
- Helps assess progress of class
- Just one question usually

Participation

Negatives
- Class disruptions (snoring, email, reading a book, etc.)
- Mistreatment of TAs

Positives
- Contribute questions and comments to class
- Participate in discussions
- Feedback
- Stop by office hours to introduce yourself
Reading


- Required: Andrew W. Appel, Modern Compiler Implementation in ML. Cambridge University Press.

- CHECK ERRATA ON BOOK WEB SITES!

- Course Web Page – Off of CS page
  - Lecture Notes
  - Project Assignments
  - Course Announcements

Who Am I?

At Princeton (Computer Science, 1999-Present):
- Professor
- Compiler and computer architecture research
- Liberty Research Group

Education (Ph.D. in 2000):
- Ph.D. Electrical Engineering from University of Illinois
- Thesis Topic: Predication
- The IMPACT Compiler Research Group

Who Am I?

Professional Experience:
- Intel (Oregon) – P6 multiprocessor validation
- Hewlett-Packard (San Jose, CA) – research compiler
- Intel (Santa Clara, CA) – IA-64 design
- Startups inspired by compiler technology
- Consulting for Intel, Google, LG, Samsung, Amazon, etc.
Our Pledge to You

• Quick response to questions and issues
• Reasonable late policy
  • Up to 3 days late for any single assignment without penalty
  • Up to 7 days late total across all assignments
  • Contact me prior to deadline for special circumstances
• Fast turn-around on grading

END OF ADMINISTRATIVE STUFF

It’s Tuesday: Pick a number 1,2,3

Quiz 0: Background (use index cards)

Front:
1. Full name and Email Address above the red line
2. Major/UG or G/Year (immediately below the red line)
3. Area (G: Research Area/UG: Interests)
4. Briefly describe any ML experience.
5. Briefly describe any C/C++ experience.
7. In which programming languages are you fluent?

Back:
1. Why do processors have registers?
2. What is an instruction cache?
3. Can one always convert an NFA to a DFA? (yes, no, or what?)