



Assembler and Linker

CS 217

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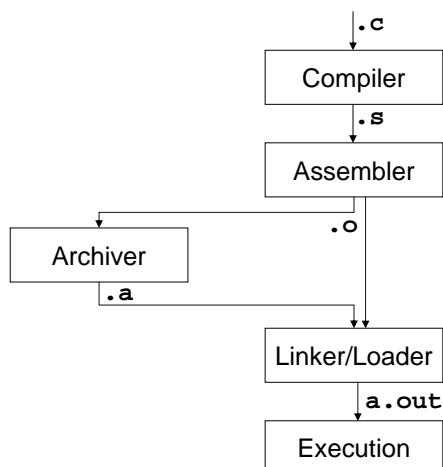


Compilation Pipeline

- Compiler (gcc): .c → .s
 - translates high-level language to assembly language
- Assembler (as): .s → .o
 - translates assembly language to machine language
- Archiver (ar): .o → .a
 - collects object files into a single library
- Linker (ld): .o + .a → a.out
 - builds an executable file from a collection of object files
- Execution (execvp)
 - loads an executable file into memory and starts it

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Compilation Pipeline



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Assembler

- Purpose
 - Translates assembly language into machine language
 - Store result in object file (.o)
- Assembly language
 - A symbolic representation of machine instructions
- Machine language
 - Contains everything needed to link, load, and execute the program

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Translating to Machine Code



- Assembly language:

```
leal (%eax,%eax,4), %eax
```

- Machine code:

- Byte 1: 8D (opcode LEA)

```
1000 1101
```

- Byte 2: 04 (Dest %eax, with SIB)

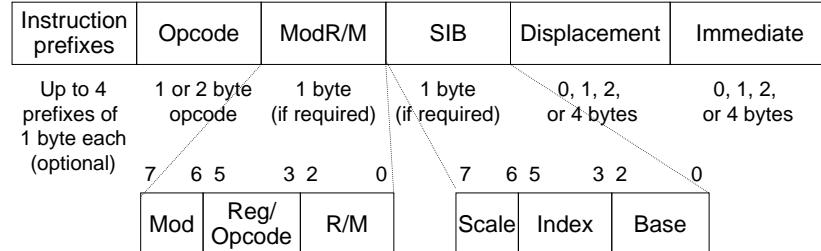
```
0000 0100
```

- Byte 3: 80 (base=%eax, index %eax *4)

```
1000 0000
```

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General IA32 Instruction Format



- Prefixes: lock, rep, repne/repnz, repe/repz, segment overwrite, operand-size overwrite, address-size overwrite
- Opcode: see Intel manual
- ModR/M and SIB: most memory operands need these
- Displacement and immediate: depending on opcode, ModR/M and SIB
- IA32's byte order is little endian

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Assembly Language Statements



- Imperative statements specify instructions
 - Typically map 1 imperative statement to 1 machine instruction
- Synthetic instructions
 - They are mapped to one or more machine instructions
- Declarative statements specify assembly time actions
 - Reserve space (.comm, .lcomm, ...)
 - Define symbols (.globl Foo, ...)
 - Identify segments (.text, .rodata, ...)
 - Initialize data (they do not yield machine instructions but they may add information to the object file that is used by the linker)

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Main Task: Symbol Manipulation



```
.text
...
movl count, %eax
...
.data
count:
.word 0
...
```

```
.globl loop
loop:
cmpl %edx, %eax
jge done
pushl %edx
call foo
jmp loop
done:
```

Create labels and remember their addresses
Deal with the "forward reference problem"

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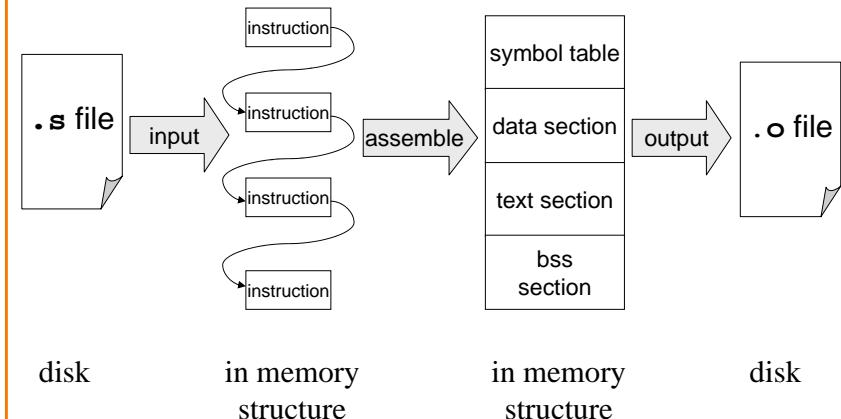
Dealing with Forward References



- Most assemblers have two passes
 - Pass 1: symbol definition
 - Pass 2: instruction assembly
 - Or, alternatively,
 - Pass 1: instruction assembly
 - Pass 2: patch the cross-reference

I will illustrate this technique

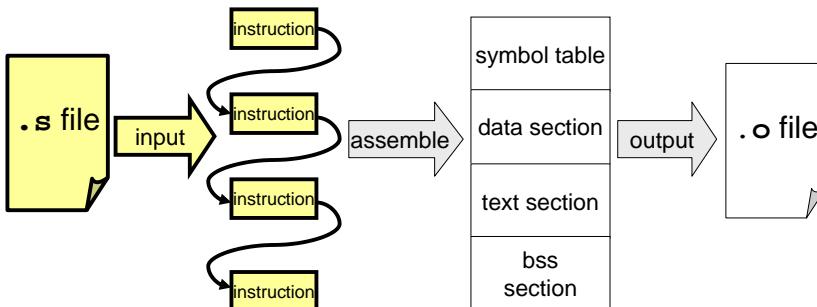
Implementing an Assembler



Input Functions



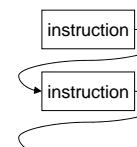
- Read assembly language and produce list of instructions



Input Functions



- Lexical analyzer
 - Group a stream of characters into tokens
`add %g1 , 10 , %g2`
 - Syntactic analyzer
 - Check the syntax of the program
`<MNEMONIC><REG><COMMA><REG><COMMA><REG>`
 - Instruction list producer
 - Produce an in-memory list of instruction data structures



Instruction Assembly



```

...
loop:
    cmpl %edx, %eax
    jge done
    pushl %edx
    call foo
    jmp loop
done:

```

D	0	3	9	[0]
Disp?	7	D	[2]	
	5	2	[4]	
Disp?	E	8	[5]	
Disp?	E	9	[10]	
			[15]	

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Symbol Table



loop	def	loop	0
done	disp10	done	2
	disp21	foo	5
	disp10	loop	10
	def	done	15


```

.globl loop
loop:
    cmpl %edx, %eax
    jge done
    pushl %edx
    call foo
    jmp loop
done:

```

D	0	3	9	[0]
Disp10	7	D	[2]	
	5	2	[4]	
Disp21	E	8	[5]	
Disp10	E	9	[10]	
			[15]	

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Filling in Local Addresses



loop	def	loop	0
done	disp10	done	2
	disp21	foo	5
	disp10	loop	10
	def	done	15


```

.globl loop
loop:
    cmpl %edx, %eax
    jge done
    pushl %edx
    call foo
    jmp loop
done:

```

	D	0	3	9	[0]
	+13	7	D	[2]	
		5	2	[4]	
Disp21	E	8	[5]		
-10	E	9	[10]		
			[15]		

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Relocation Records



def	loop	0
disp21	foo	5


```

...
.globl loop
loop:
    cmpl %edx, %eax
    jge done
    pushl %edx
    call foo
    jmp loop
done:

```

D	0	3	9	[0]
+13	7	D	[2]	
	5	2	[4]	
Disp21	E	8	[5]	
-10	E	9	[10]	
			[15]	

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Assembler Directives



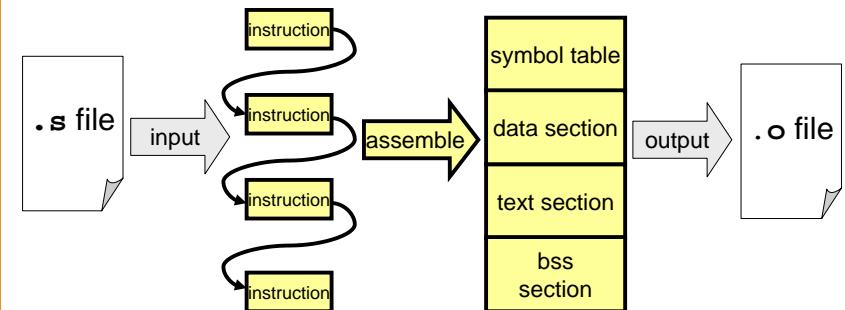
- Delineate segments
 - .section
 - may need multiple location counters (one per segment)
- Allocate/initialize data and bss segments
 - .word .half .byte
 - .ascii .asciz
 - .align .skip
- Make symbols in text externally visible
 - .global

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Assemble into Sections



- Process instructions and directives to produce object file output structures

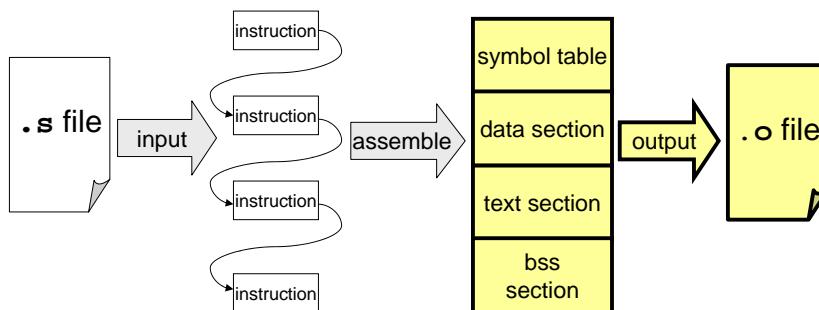


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Output Functions



- Machine language output
 - Write symbol table and sections into object file

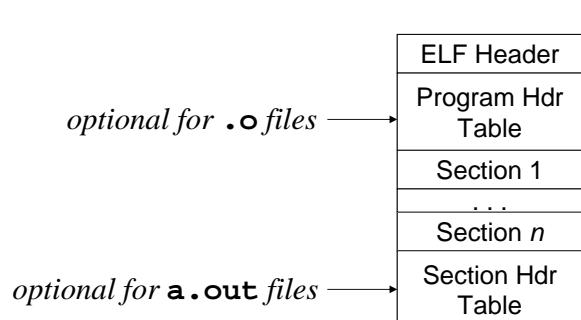


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ELF: Executable and Linking Format



- Format of .o and a.out files
 - Output by the assembler
 - Input and output of linker



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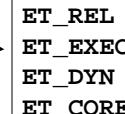
ELF (cont)



ELF Header

```
typedef struct {  
    unsigned char e_ident[EI_NIDENT];  
    Elf32_Half e_type;  
    Elf32_Half e_machine;  
    Elf32_Word e_version;  
    Elf32_Addr e_entry;  
    Elf32_Off e_phoff;  
    Elf32_Off e_shoff;  
    ...  
} Elf32_Ehdr;
```

E_ident[EI_CLASS]=ELFCLASS32
E_ident[EI_DATA]=ELFDATA2LSB



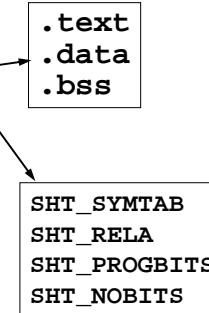
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ELF (cont)



- Section Header Table: array of...

```
typedef struct {  
    Elf32_Word sh_name;  
    Elf32_Word sh_type;  
    Elf32_Word sh_flags;  
    Elf32_Addr sh_addr;  
    Elf32_Off sh_offset;  
    Elf32_Word sh_size;  
    Elf32_Word sh_link;  
    ...  
} Elf32_Shdr;
```



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Invoking the Linker



```
• ld bar.o main.o -l libc.a -o a.out
```

compiled program modules library (contains more .o files) output (also in ".o" format, but no undefined symbols)

- Invoked automatically by gcc,
- but you can call it directly if you like.

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Multiple Object Files



start	main	0
def	foo	8
disp21	loop	15
[0]		
[2]		
[4]		
[7]		
[8]		
[12]		
[15]		
[20]		
	Disp21	

main.o

def	loop	0
disp21	foo	5
	D 0 3 9 [0]	
	+13 7 D [2]	
	5 2 [4]	
	Disp21 E 8 [5]	
	-10 E 9 [10]	
		[15]

bar.o

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Step 1: Pick An Order



bar.o		
def	loop	0
disp21	foo	5
	D 0 3 9	[0]
	+11 7 D	[2]
	5 2	[4]
Disp21	E 8	[5]
-15	E 9	[10]
		[15]

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Step 2: Patch



main.o

start	main	15+0
def	foo	15+8
disp21	loop	15+15

def	loop	0
disp21	foo	5
	D 0 3 9	[0]
	+11	7 D [2]
		5 2 [4]
15+8-5=13	E 8	[5]
-15	E 9	[10]
		[15]

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Step 3: Concatenate



a.out		
start	main	15+0
	D 0	3 9
+13		7 D
		5 2
+13	E 8	[5]
-10	E 9	[10]
		[15]
		[17]
		[19]
		[22]
		[23]
		[27]
-30		[30]
		[35]

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Summary



- Assembler
 - Read assembly language
 - Two-pass execution (resolve symbols)
 - Produce object file
 - Linker
 - Relocation records
 - Order object codes
 - Patch and resolve displacements
 - Produce executable

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