

History

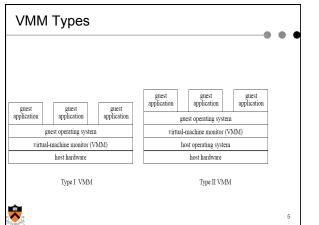
- Have been around since 1960's on mainframes
 - Used to run apps on different OSes on same (very expensive) mainframe
 - Good example VM/370
- Computers became cheaper, people lost interest
- Have resurfaced on commodity platforms
 - Server Consolidation: save space, power; data centers
 - High-Performance Compute Clusters: run different OSes
 - Managed desktop / thin-client
 - Save desktop in a VM and bring it with you on a USB drive
 Software development / kernel hacking
 - Crash your development kernel but don't disable whole machine

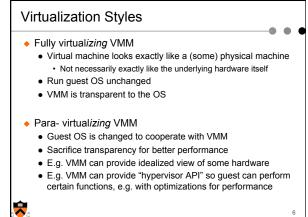
Goals

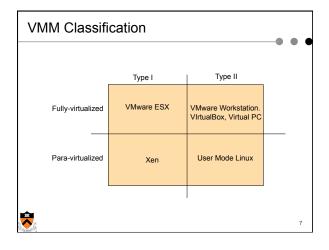
- Manageability
- Creation, maintenance, administration, provisioning, etc.
- Performance
 - Overhead of virtualization should be small
- Isolation
 - Activity of one VM should not impact other active VMs
 - Data of one VM is inaccessible by another
- Scalability
 - Minimize cost per VM

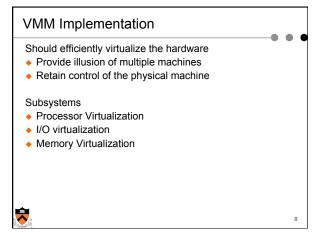
Same goals as for many susbystems











Processor Virtualization

Popek and Goldberg (1974)

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- Sensitive instructions: only executed in kernel mode
- Privileged instructions: trap when run in user mode
- CPU architecture is virtualizable only if sensitive instructions are subset of privileged instructions
 i.e. sensitive instructions will always trap if run in user mode
- When guest OS, which runs in user mode, runs a sensitive instruction, must trap to VMM so it maintains control

Example: System Call (Type 1 Hypervisor)			
Process 1.System call: Trap to OS	Operating System	<u>VMM</u>	
		2. Process trapped: call OS trap handler (at reduced privilege)	
	3. OS trap handler: Decode trap and execute syscall; When done: issue return- frrom-trap		
		4. OS tried to return from trap; do real return-from-trap	
5. Resume execution (@PC after trap)			
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x86 Processor Virtualization

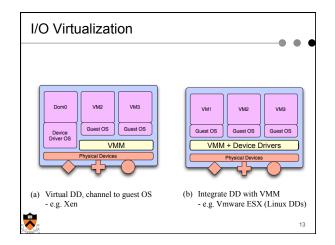
- x86 architecture is not fully virtualizable
 - Certain privileged instructions behave differently when run in unprivileged mode, e.g. do nothing (e.g. POPF)
 - Certain unprivileged instructions can access privileged state (so guest OS would be able to see that it's not running in kernel mode)
- Techniques to address inability to virtualize x86
 - Replace non-virtualizable instructions with easily virtualized ones statically (Paravirtualization)
 - Perform Binary Translation (Full Virtualization)
 - Note: both basically remove problematic (nonvirtualizable) instructions from the guest OS
 - izable) instructions from the guest of

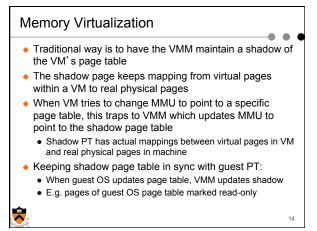
I/O Virtualization

- Issue: lots of I/O devices
- Problem: Writing device drivers for all I/O device in the VMM layer is not a feasible option
- Insight: Device driver already written for popular Operating Systems
- One Solution:
 - Present virtual I/O devices to guest VMs
 - Channel I/O requests to a trusted *host* VM running a popular OS that has the device drivers

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Case Study: VMware ESX Server

• Type I VMM - Runs on bare hardware

 Full-virtualized – Legacy OS can run unmodified on top of ESX server

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 Fully controls hardware resources and provides good performance

ESX Server – CPU Virtualization Most user code executes in Direct Execution mode; near native performance For kernel code, uses *runtime* Binary Translation for x86 virtualization Privileged mode code is run under control of a Binary Translator, which emulates problematic instructions Fast compared to other binary translators as source and destination instruction sets are nearly identical

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ESX Server – Memory Virtualization

- Maintains shadow page tables with virtual to machine address mappings.
- Shadow page tables are used by the physical processor
- ESX maintains a "pmap" data structure for each VM, which holds "physical" to machine address mappings
- Shadow page tables are kept consistent with pmap
 With pmap, ESX can easily remap a physical to machine

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page mapping, without guest VM knowing the difference

ESX Server – Memory Mgmt

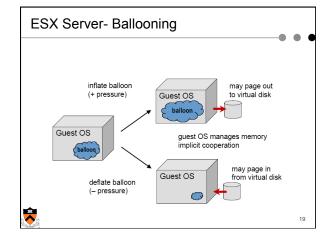
Page reclamation

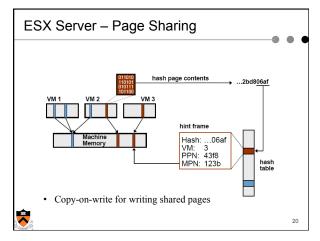
- Problem: VMM does not have as good information on page usage as guest OS, for actual page replacement algorithms
 Solution: Ballooning technique
- Reclaims memory from other VMs when memory is overcommitted
- Page sharing

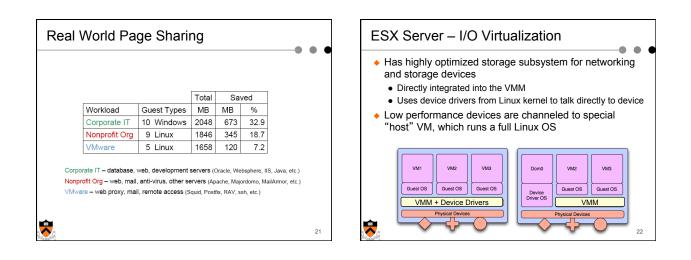
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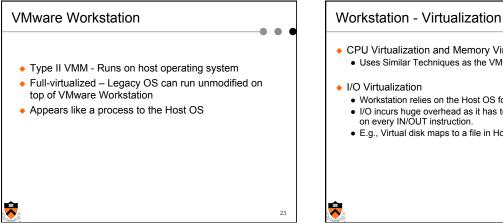
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- Many VMs will use the same pages
- Solution: Content based sharing
- Eliminates redundancy and saves memory pages when VMs use same operating system and applications



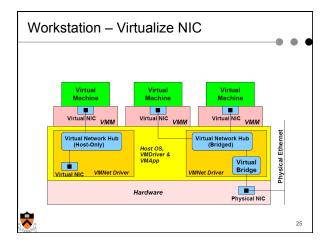






CPU Virtualization and Memory Virtualization • Uses Similar Techniques as the VMware ESX server • Workstation relies on the Host OS for satisfying I/O requests • I/O incurs huge overhead as it has to switch to the Host OS • E.g., Virtual disk maps to a file in Host OS

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Xen	
 Type I VMM Para-virtualized Open-source Designed to run about 100 virtual machines on a single machine 	••
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Xen – CPU Virtualization

- Privileged instructions are para-virtualized by requiring them to be validated and executed with Xen
- Processor Rings

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- Guest applications run in Ring 3
- Guest OS runs in Ring 1 (not ring 0 as without virtualization)
- Xen runs in Ring 0
- So if guest OS executes privileged instruction, it traps to Xen

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Xen – Memory Virtualization(1) Initial memory allocation is specified and memory is statically partitioned A maximum allowable reservation is also specified. Balloon driver technique similar to ESX server used to reclaim pages

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Xen – Memory Virtualization(2)

- Guest OS is responsible for allocating and managing hardware page table
- Xen involvement is limited to ensure safety and isolation
- OS maps Xen VMM into the top 64 MB section of every address space to avoid TLB flushes when entering and leaving the VMM

Xen – I/O Virtualization

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- Xen exposes its own set of clean and simple device abstractions – doesn't emulate existing devices
- I/O data is transferred to and from each domain via Xen, using shared memory, asynchronous buffer descriptor rings
- Xen supports lightweight event delivery mechanism used for sending asynchronous notifications to domains

Summary

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- Classifying Virtual Machine Monitors
 - Type I vs. type II
 - Full vs. para-virtualization
- Processor virtualization
- Memory virtualization
- I/O virtualization

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